

General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, fast reverse recovery time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for electronic ballast and switching mode power supplies.

FEATURES

- $V_{DSS} = 500V$, $I_D = 3A$
- Drain-Source ON Resistance : $R_{DS(ON)} = 2.5\Omega$ (Max) @ $V_{GS} = 10V$
- $Q_g(\text{typ}) = 7.50nC$
- $t_{rr}(\text{typ}) = 120ns$ (KF3N50FS)
- $t_{rr}(\text{typ}) = 300ns$ (KF3N50FZ)

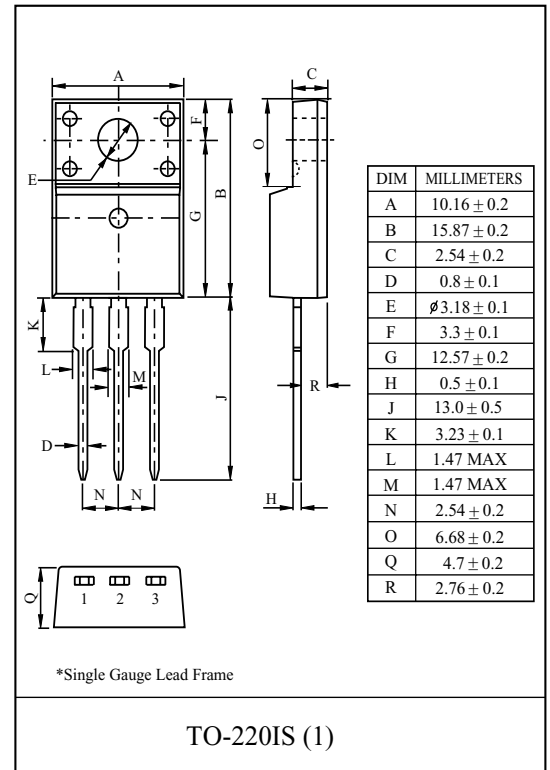
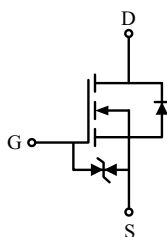
MAXIMUM RATING (Tc=25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	V_{DSS}	500	V
Gate-Source Voltage	V_{GSS}	± 30	V
Drain Current	@T _C =25°C	3*	A
	@T _C =100°C	1.8*	
	Pulsed (Note1)	9*	
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	110	mJ
Repetitive Avalanche Energy (Note 1)	E_{AR}	4	mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	10	V/ns
Drain Power Dissipation	Tc=25°C	25	W
	Derate above 25°C	0.2	W/°C
Maximum Junction Temperature	T _j	150	°C
Storage Temperature Range	T _{stg}	-55 ~ 150	°C
Thermal Characteristics			
Thermal Resistance, Junction-to-Case	R _{thJC}	5.0	°C/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	°C/W

* : Drain Current limited by maximum junction temperature

PIN CONNECTION

(KF3N50FZ/FS)



KF3N50FZ/FS

ELECTRICAL CHARACTERISTICS (Tc=25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu A$, $V_{GS}=0V$	500	-	-	V
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_j$	$I_D=250\mu A$, Referenced to 25°C	-	0.55	-	V/°C
Drain Cut-off Current	I_{DSS}	$V_{DS}=500V$, $V_{GS}=0V$,	-	-	10	μA
Gate Threshold Voltage	V_{th}	$V_{DS}=V_{GS}$, $I_D=250\mu A$	2.5	-	4.5	V
Gate Leakage Current	I_{GSS}	$V_{GS}=\pm 25V$, $V_{DS}=0V$	-	-	± 10	μA
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS}=10V$, $I_D=1.5A$	-	2.0	2.5	Ω
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=400V$, $I_D=3A$ $V_{GS}=10V$ (Note4,5)	-	8.0	-	nC
Gate-Source Charge	Q_{gs}		-	2.0	-	
Gate-Drain Charge	Q_{gd}		-	3.5	-	
Turn-on Delay time	$t_{d(on)}$	$V_{DD}=250V$ $I_D=3A$ $R_G=25\Omega$ (Note4,5)	-	15	-	ns
Turn-on Rise time	t_r		-	20	-	
Turn-off Delay time	$t_{d(off)}$		-	25	-	
Turn-off Fall time	t_f		-	20	-	
Input Capacitance	C_{iss}	$V_{DS}=25V$, $V_{GS}=0V$, $f=1.0MHz$	-	350	-	pF
Output Capacitance	C_{oss}		-	45	-	
Reverse Transfer Capacitance	C_{rss}		-	4.5	-	
Source-Drain Diode Ratings						
Continuous Source Current	I_S	$V_{GS}<V_{th}$	-	-	3	A
Pulsed Source Current	I_{SP}		-	-	12	
Diode Forward Voltage	V_{SD}	$I_S=3A$, $V_{GS}=0V$	-	-	1.4	V
Reverse Recovery Time	KF3N50FZ	$I_S=3A$, $V_{GS}=0V$, $dI_S/dt=100A/\mu s$	-	300	-	ns
	KF3N50FS		-	120	-	
Reverse Recovery Charge	KF3N50FZ		-	1.1	-	μC
	KF3N50FS		-	0.25	-	

Note 1) Repetivity rating : Pulse width limited by junction temperature.

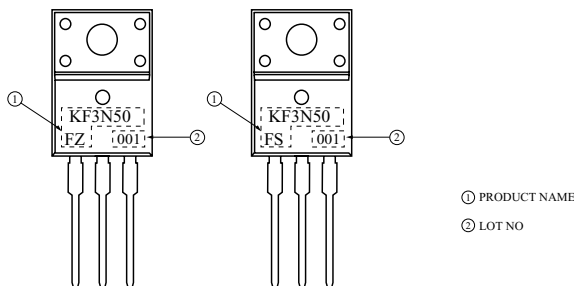
Note 2) $L=22mH$, $I_S=3A$, $V_{DD}=50V$, $R_G=25\Omega$, Starting $T_j=25^\circ C$.

Note 3) $I_S \leq 3A$, $dI/dt \leq 100A/\mu s$, $V_{DD} \leq BV_{DSS}$, Starting $T_j=25^\circ C$.

Note 4) Pulse Test : Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

Note 5) Essentially independent of operating temperature.

Marking



KF3N50FZ/FS

Fig1. $I_D - V_{DS}$

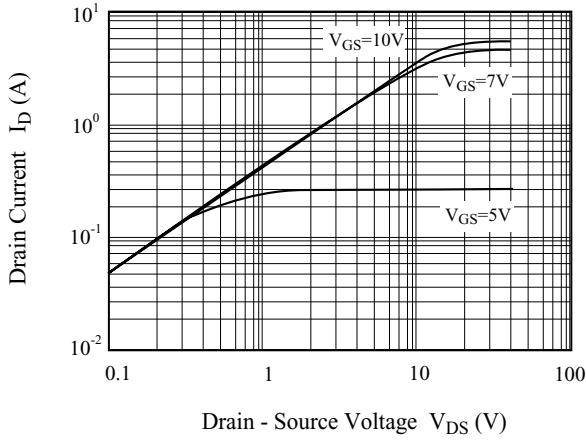


Fig2. $I_D - V_{GS}$

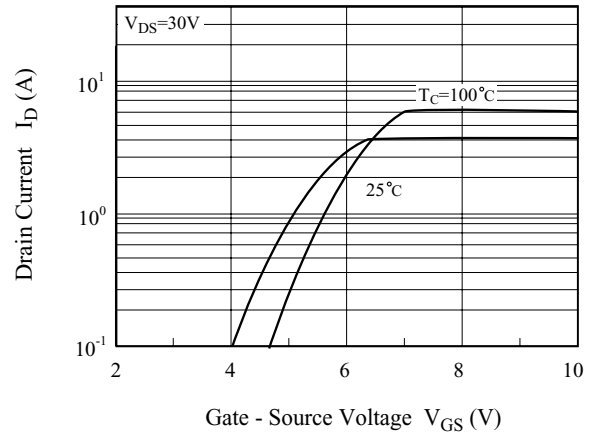


Fig3. $BV_{DSS} - T_j$

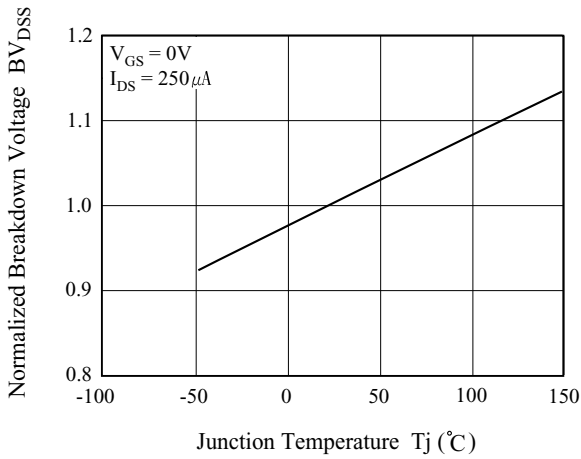


Fig4. $R_{DS(ON)} - I_D$

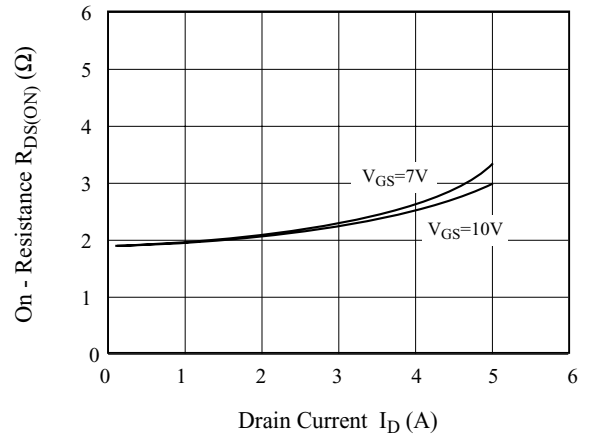


Fig5. $I_S - V_{SD}$

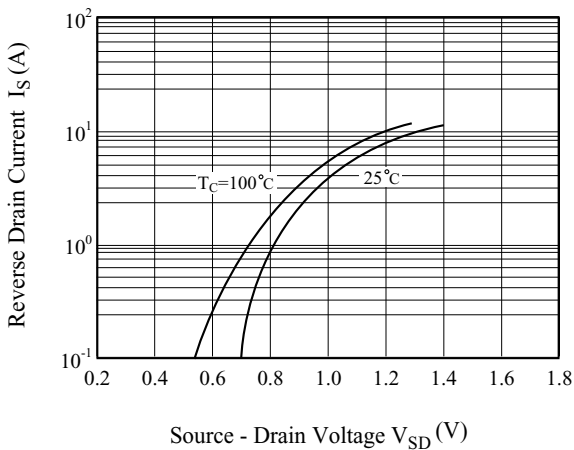
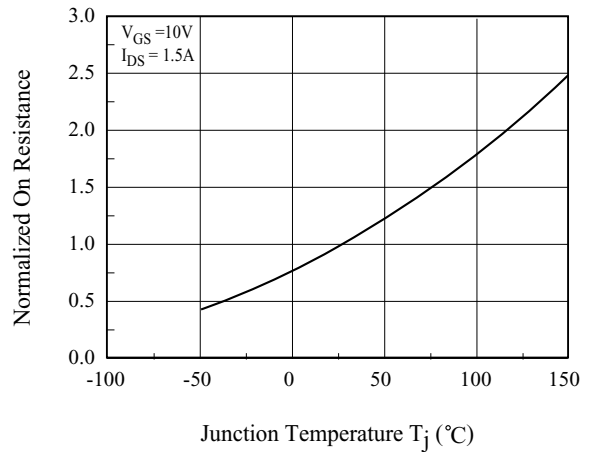


Fig6. $R_{DS(ON)} - T_j$



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Fig 7. C - V_{DS}

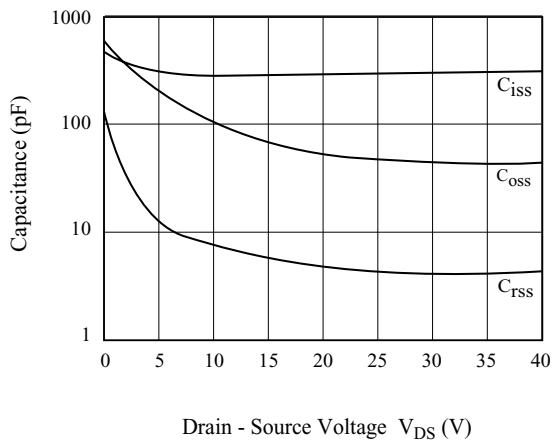


Fig8. Qg- V_{GS}

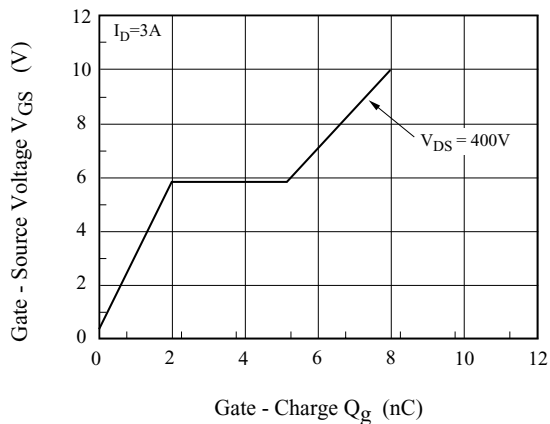


Fig9. Safe Operation Area

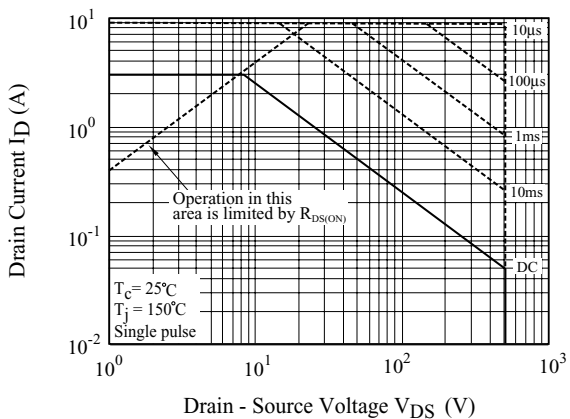


Fig10. I_D - T_j

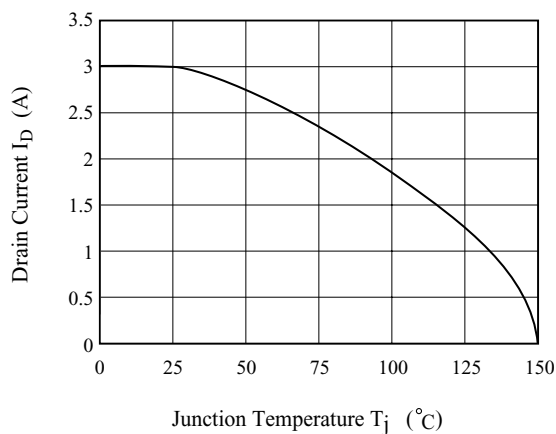
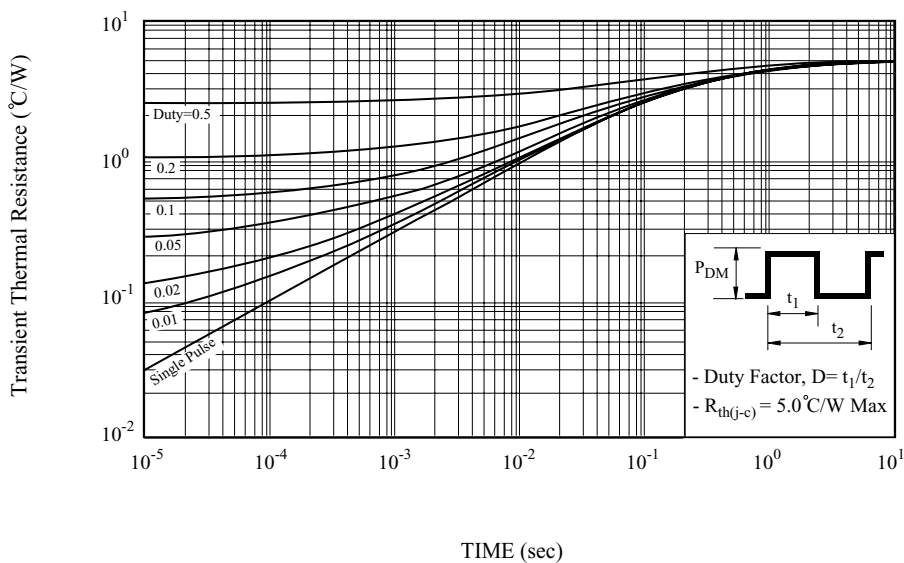


Fig11. Transient Thermal Response Curve



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Fig12. Gate Charge

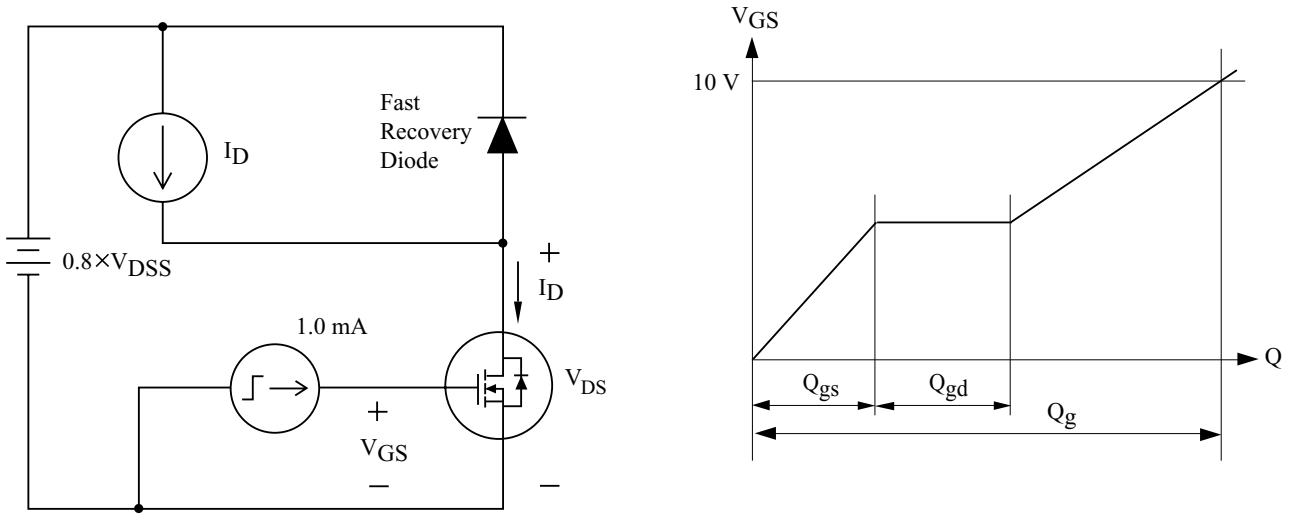


Fig13. Single Pulsed Avalanche Energy

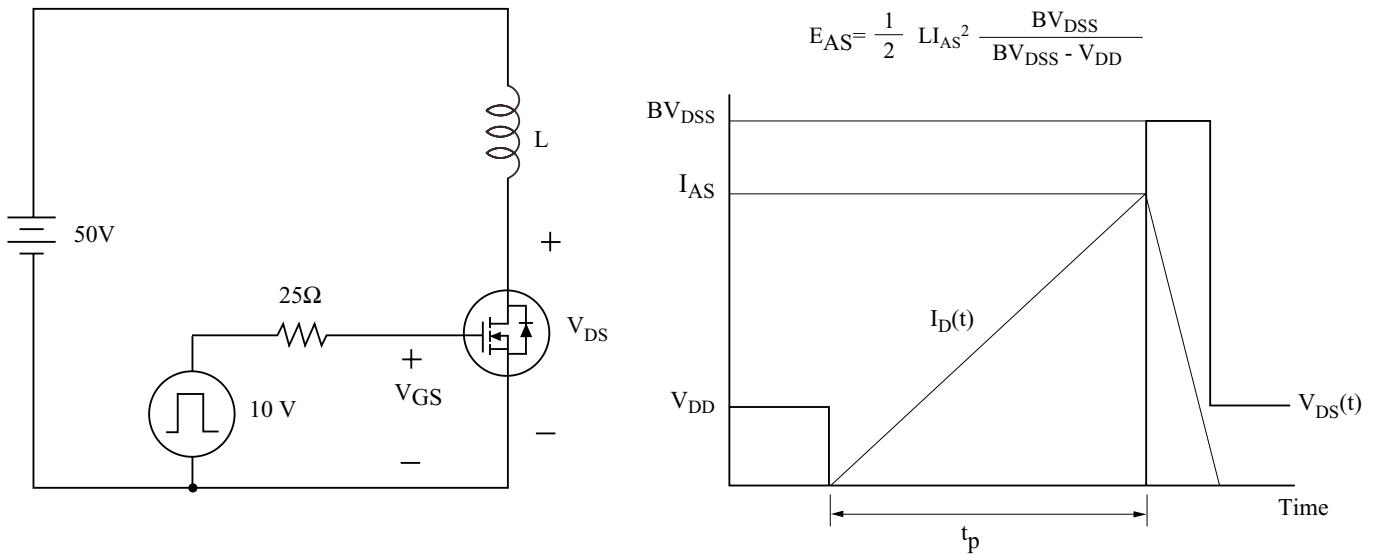


Fig14. Resistive Load Switching

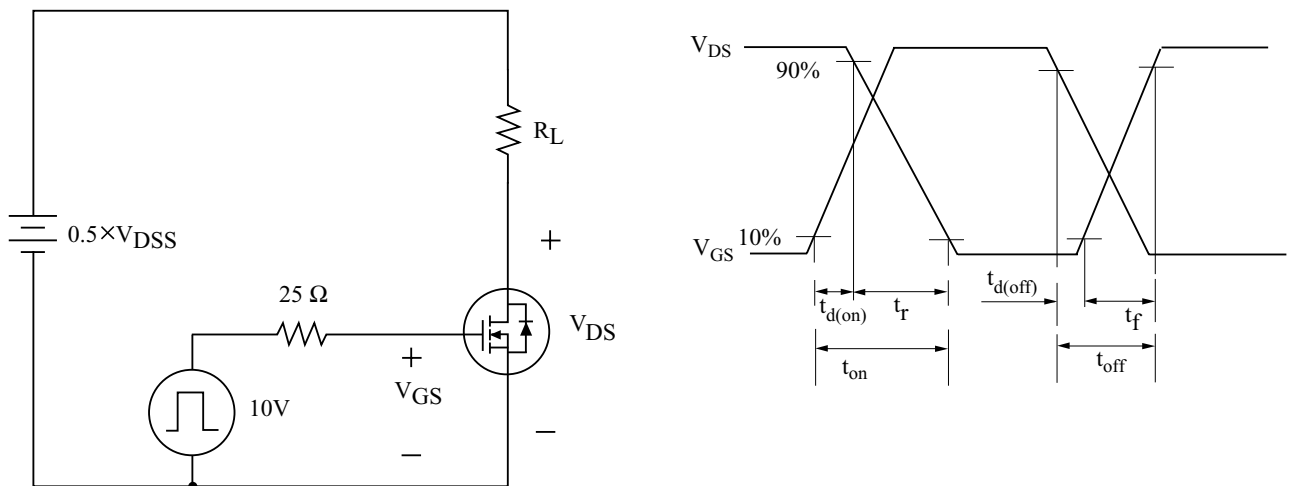


Fig15. Source - Drain Diode Reverse Recovery and dv/dt

