

PLASTIC SILICON RECTIFIER

VOLTAGE RANGE: 50 --- 1000 V
CURRENT: 8.0 A

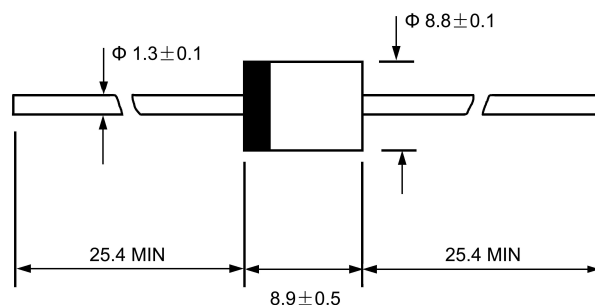
FEATURES

- ◇ Low cost
- ◇ Diffused junction
- ◇ Low leakage
- ◇ Low forward voltage drop
- ◇ High current capability
- ◇ Easily cleaned with Freon, Alcohol, Isopropanol and similar solvents
- ◇ The plastic material carries U/L recognition 94V-0

MECHANICAL DATA

- ◇ Case: JEDEC R-6, molded plastic
- ◇ Terminals: Axial lead, solderable per MIL-STD-202, Method 208
- ◇ Polarity: Color band denotes cathode
- ◇ Weight: 0.072 ounces, 2.04 grams
- ◇ Mounting position: Any

R - 6



Dimensions in millimeters

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate by 20%.

		8A05	8A1	8A2	8A4	8A6	8A8	8A10	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current 9.5mm lead length, @ $T_A=75^\circ\text{C}$	$I_{F(AV)}$	8.0							A
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load @ $T_J=125^\circ\text{C}$	I_{FSM}	400							A
Maximum instantaneous forward voltage @ 8.0 A	V_F	1.0							V
Maximum reverse current @ $T_A=25^\circ\text{C}$ at rated DC blocking voltage @ $T_A=100^\circ\text{C}$	I_R	10 100							μA
Typical junction capacitance (Note1)	C_J	120							pF
Typical thermal resistance (Note2)	$R_{\theta JA}$	10							$^\circ\text{C/W}$
Operating junction temperature range	T_J	- 55 ---- + 125							$^\circ\text{C}$
Storage temperature range	T_{STG}	- 55 ---- + 150							$^\circ\text{C}$

NOTE: 1. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

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2. Thermal resistance from junction to ambient.

FIG.1 – FORWARD DERATING CURVE

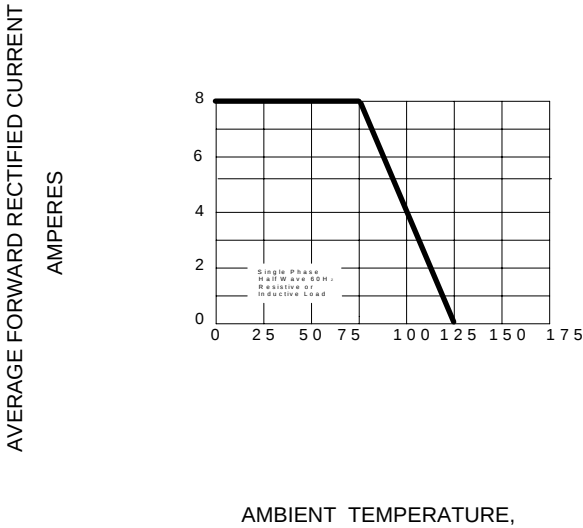


FIG.2 – TYPICAL FORWARD CHARACTERISTICS

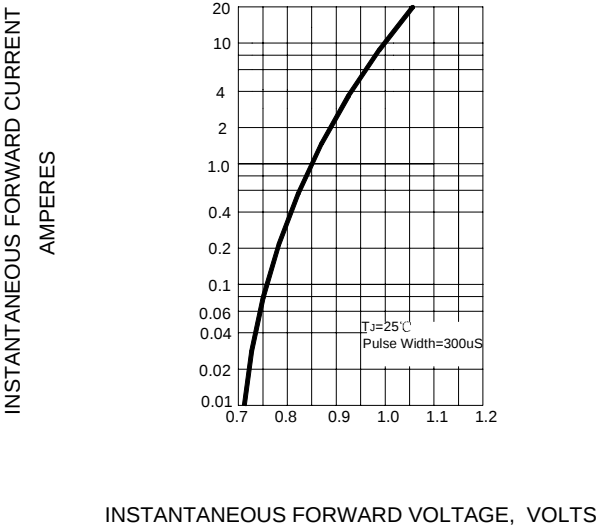


FIG.3 – MAXIMUM NON-REPETITIVE FORWARD
SURGE CURRENT

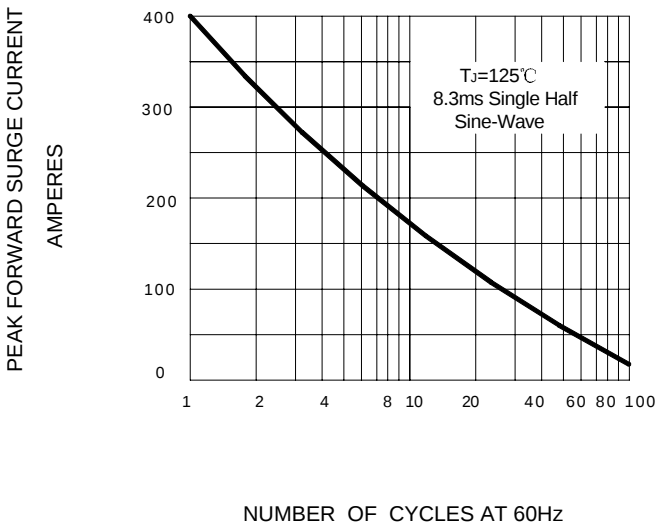


FIG.4 – TYPICAL JUNCTION CAPACITANCE

