Unit in mm

#### TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

# **MT6P06E**

## VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

• Two devices are built in to the super-thin and extreme super mini (6 pins) package: ES6

#### **MOUNTED DEVICES**

	Q1/Q2:SSM (TESM)
Three-pins (SSM/TESM) mold products are corresponded.	MT3S06S (MT3S06T)

#### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	Q1 / Q2	UNIT
Collector-Base Voltage	$v_{\mathrm{CBO}}$	10	V
Collector-Emitter Voltage	$v_{CEO}$	5	V
Emitter-Base Voltage	$ m v_{EBO}$	1.5	V
Collector Current	$I_{\mathbf{C}}$	15	mA
Base Current	$I_{\mathbf{B}}$	7	mA
Collector Power Dissipation	PC (Note 1)	100	mW
Junction Temperature	$\mathbf{T}_{\mathbf{j}}$	125	°C
Storage Temperature Range	$\mathbf{T_{stg}}$	-55~125	°C

\$0.01 = 0.05 \$0.01 = 0.05 \$0.01 = 0.05 \$0.01 = 0.05

1. EMITTER 1 4. COLLECTOR 2
2. EMITTER 2 5. BASE 1
3. BASE 2 6. COLLECTOR 1

JEDEC —

JEITA —

2-2N1A

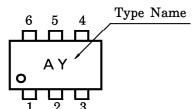
Weight: 0.003 g

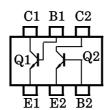
**TOSHIBA** 

(Note 1): Total power dissipation of Q1 and Q2.

### **MARKING**







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ELECTRICAL CHARACTERISTICS Q1/Q2 (Ta = 25°C)

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CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	ICBO	$V_{CB} = 5 \text{ V}, I_{E} = 0$	_	_	0.1	$\mu$ A
Emitter Cut-off Current	$I_{EBO}$	$V_{EB} = 1 V, I_{C} = 0$	_	_	1	$\mu$ A
DC Current Gain	$h_{ extbf{FE}}$	$V_{ m CE}=1 m V,\ I_{ m C}=5 m mA$	70	_	140	_
Transition Frequency	$\mathbf{f_{T}}$	$V_{ m CE}=3~{ m V},~{ m I}_{ m C}=5~{ m mA}$	7	10	_	GHz
Insertion Gain	$ S_{21e} ^2(1)$	$V_{CE} = 1 \text{ V}, \text{ I}_{C} = 5 \text{ mA},$ f = 2  GHz	_	7.5	_	dB
	$ S_{21e} ^2$ (2)	$V_{CE} = 3 \text{ V}, I_{C} = 7 \text{ mA},$ f = 2  GHz	4.5	8	_	dB
Noise Figure	NF (1)	$V_{CE} = 1 \text{ V}, I_{C} = 3 \text{ mA},$ f = 2  GHz	_	1.7	3	dB
	NF (2)	$V_{CE} = 3 \text{ V}, I_{C} = 3 \text{ mA},$ f = 2  GHz	_	1.6	3	dB
Reverse Transfer Capacitance	$\mathrm{C_{re}}$	$V_{CB} = 1 V, I_{E} = 0,$ f = 1 MHz (Note 2)	_	0.35	0.75	pF

(Note 2):  $C_{re}$  is measured by 3 terminal method with capacitance bridge.

#### HANDLING PRECAUTION

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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