

General Description

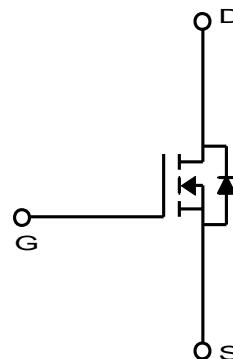
The MDP1930 uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDP1930 is suitable device for Synchronous Rectification For Server and general purpose applications.

Features

- $V_{DS} = 80V$
- $I_D = 120A @ V_{GS} = 10V$
- $R_{DS(ON)} < 2.5 \text{ m}\Omega @ V_{GS} = 10V$
- 100% UIL Tested
- 100% R_g Tested



TO-220



Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	80	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current ⁽¹⁾	$T_C=25^\circ\text{C}$ (Silicon Limited)	I_D	250	A
	$T_C=25^\circ\text{C}$ (Package Limited)		120	
	$T_C=100^\circ\text{C}$ (Silicon Limited)		158	
	$T_C=100^\circ\text{C}$ (Package Limited)		120	
Pulsed Drain Current		I_{DM}	480	
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	312.5	W
	$T_C=100^\circ\text{C}$		125	
Single Pulse Avalanche Energy ⁽²⁾		E_{AS}	612.5	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics		Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾	$R_{θJA}$		62.5	°C/W
Thermal Resistance, Junction-to-Case			0.4	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDP1930TH	-55~150°C	TO-220	Tube	Halogen Free

Electrical Characteristics ($T_J = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\mu\text{A}, V_{\text{GS}} = 0\text{V}$	80	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2.0	-	4.0	
Drain Cut-Off Current	I_{DSS}	$V_{\text{DS}} = 64\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	1.0	μA
Gate Leakage Current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$	-	-	± 0.1	
Drain-Source ON Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_D = 50\text{A}$	-	2.0	2.5	$\text{m}\Omega$
Forward Transconductance	g_{fs}	$V_{\text{DS}} = 10\text{V}, I_D = 50\text{A}$	-	115	-	S
Dynamic Characteristics						
Total Gate Charge	Q_g	$V_{\text{DS}} = 40\text{V}, I_D = 50\text{A}, V_{\text{GS}} = 10\text{V}$	-	186.3	-	nC
Gate-Source Charge	Q_{gs}		-	56.3	-	
Gate-Drain Charge	Q_{gd}		-	38.5	-	
Input Capacitance	C_{iss}	$V_{\text{DS}} = 40\text{V}, V_{\text{GS}} = 0\text{V}, f = 1.0\text{MHz}$	-	12,222	-	pF
Reverse Transfer Capacitance	C_{rss}		-	51	-	
Output Capacitance	C_{oss}		-	2,123	-	
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, V_{\text{DS}} = 40\text{V}, I_D = 50\text{A}, R_G = 3.0\Omega$	-	39.6	-	ns
Rise Time	t_r		-	24.2	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	141	-	
Fall Time	t_f		-	54.2	-	
Gate Resistance	R_g	$f=1\text{ MHz}$	-	3.0	-	Ω
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V_{SD}	$I_S = 50\text{A}, V_{\text{GS}} = 0\text{V}$	-	0.9	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 50\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	92.8	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	231.7	-	nC

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7). Continuous current at $T_c=25^\circ\text{C}$ is silicon limited
2. E_{AS} is tested at starting $T_J = 25^\circ\text{C}$, $L = 1.0\text{mH}$, $I_{\text{AS}} = 35.0\text{A}$, $V_{\text{GS}} = 10\text{V}$.

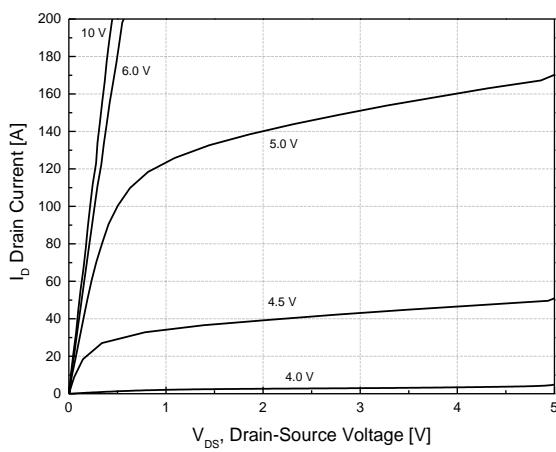


Fig.1 On-Region Characteristics

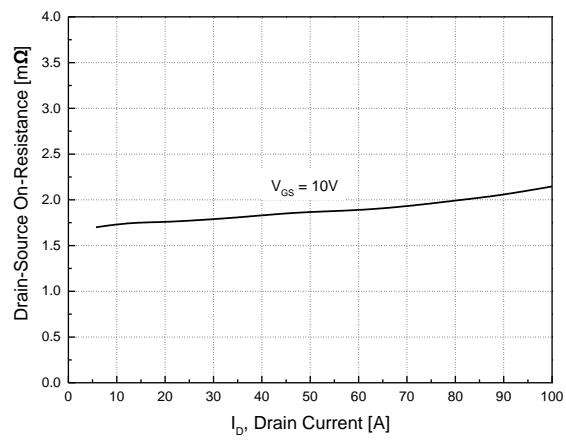


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

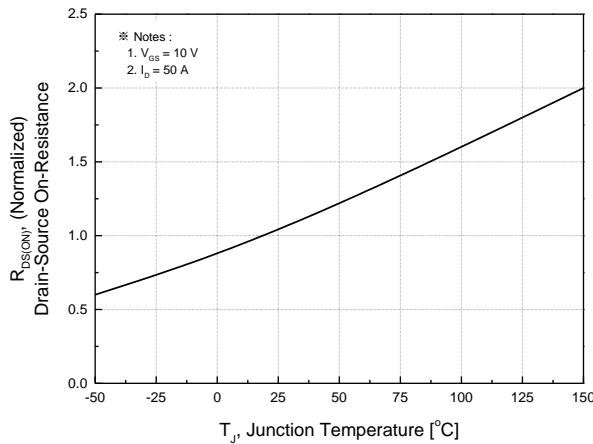


Fig.3 On-Resistance Variation with Temperature

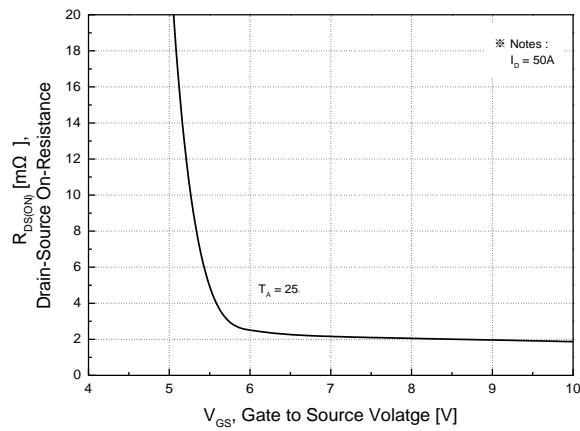


Fig.4 On-Resistance Variation with Gate to Source Voltage

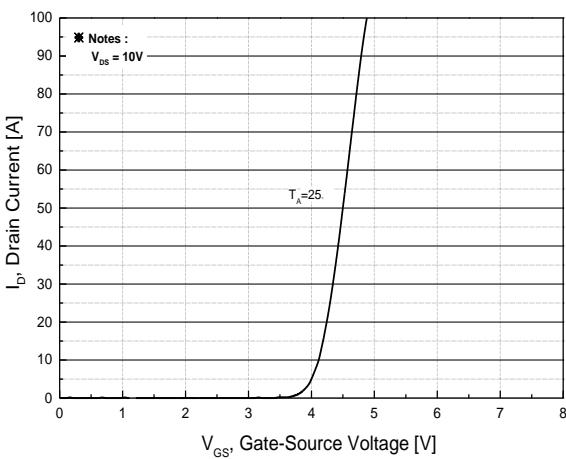


Fig.5 Transfer Characteristics

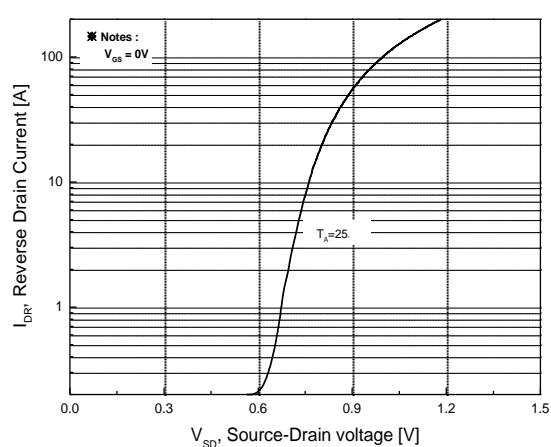


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

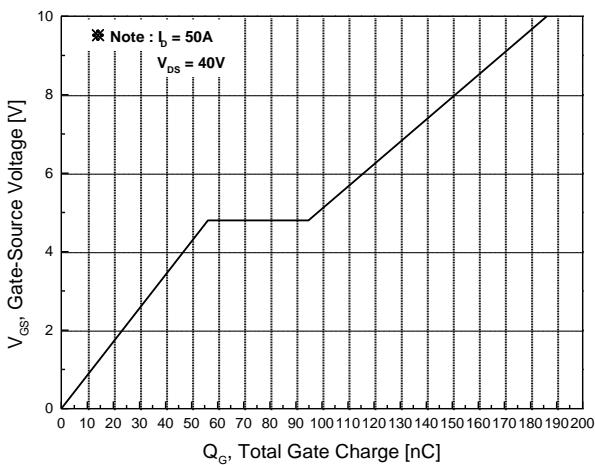


Fig.7 Gate Charge Characteristics

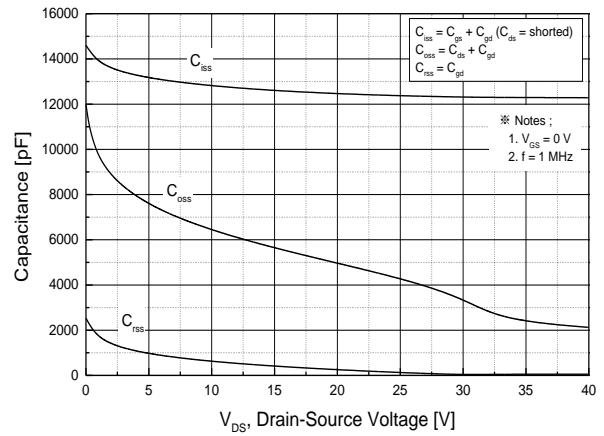


Fig.8 Capacitance Characteristics

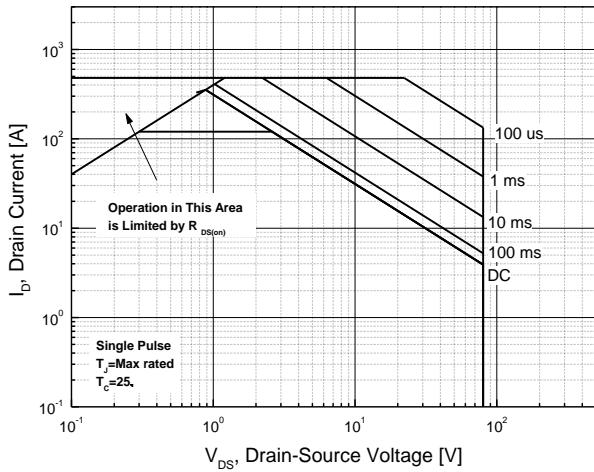


Fig.9 Maximum Safe Operating Area

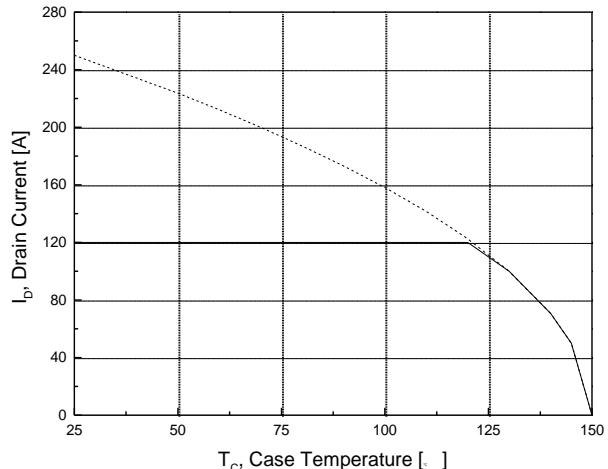


Fig.10 Maximum Drain Current vs. Case Temperature

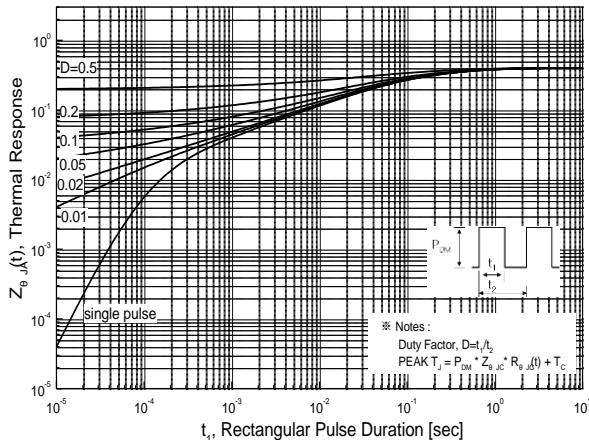
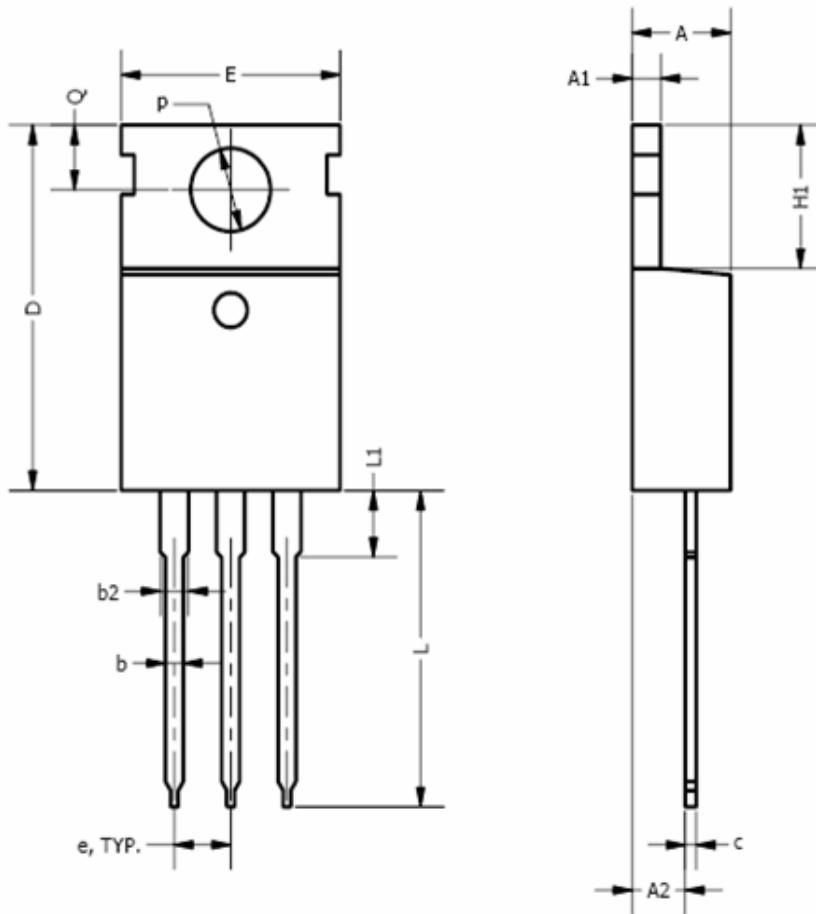


Fig.11 Transient Thermal Response Curve

Package Dimension

3 Leads, TO-220

Dimensions are in millimeters unless otherwise specified



Symbol	Min	Nom	Max
A	3.56		4.83
A1	0.50		1.40
A2	2.03		2.92
b	0.38	0.69	1.02
b2	1.14	1.45	1.78
c	0.36		0.61
D	14.22		16.51
e, TYP.	2.54 TYP		
E	9.65		10.67
H1	5.84		6.86
L	12.70		14.73
L1			6.35
φP	3.53		4.09
Q	2.54		3.43

DISCLAIMER:

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