

## isc N-Channel MOSFET Transistor

## IRFS4127, IIRFS4127

### • FEATURES

- Static drain-source on-resistance:  $R_{DS(on)} \leq 22m\Omega$
- Enhancement mode:
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • DESCRIPTION

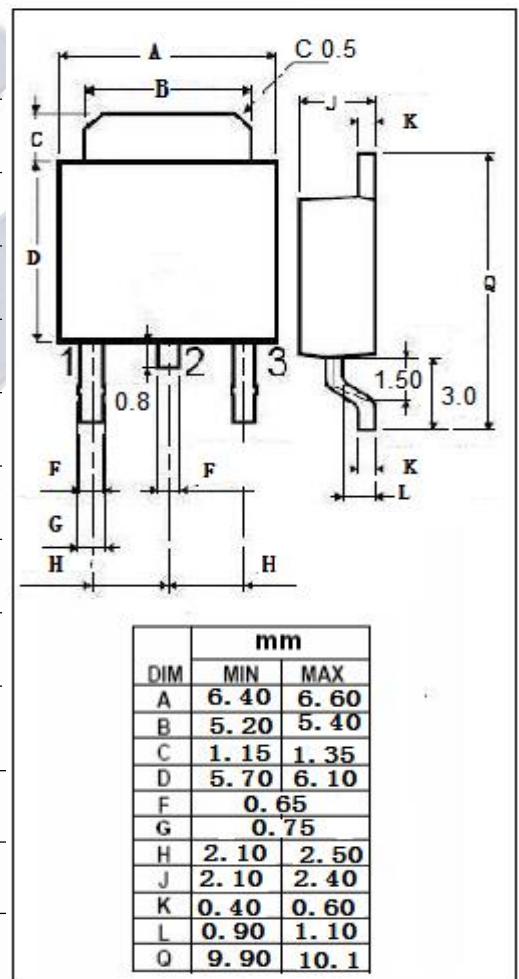
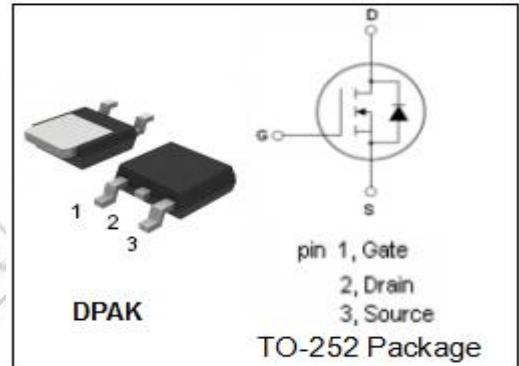
- High Speed Power Switching

### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ C$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	200	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous	72	A
$I_{DM}$	Drain Current-Single Pulsed	300	A
$P_D$	Total Dissipation @ $T_c=25^\circ C$	375	W
$T_j$	Max. Operating Junction Temperature	175	$^\circ C$
$T_{stg}$	Storage Temperature	-55~175	$^\circ C$

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	0.4	$^\circ C/W$
$R_{th(j-a)}$	Channel-to-ambient thermal resistance	40	$^\circ C/W$



**isc N-Channel MOSFET Transistor****IRFS4127, IIRFS4127****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D=250 \mu\text{A}$	200			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}; \text{I}_D=250 \mu\text{A}$	3		5	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=44\text{A}$			22	$\text{m}\Omega$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 20\text{V}$			$\pm 0.1$	$\mu\text{A}$
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=200\text{V}; \text{V}_{\text{GS}}= 0\text{V}$			20	$\mu\text{A}$
$\text{V}_{\text{SD}}$	Diode forward voltage	$\text{I}_s=44\text{A}, \text{V}_{\text{GS}} = 0\text{V}$			1.3	V