

UNISONIC TECHNOLOGIES CO., LTD

US206 Preliminary CMOS IC

POWER DISTRIBUTION SWITCH

DESCRIPTION

The UTC **US206** is a power distribution switch. It particularly designed for self-powered and bus-powered Universal Serial Bus (USB) applications.

The $R_{DS\;(ON)}$ of the MOSFET switch is as low as $85m\Omega$. \overline{OC} is open-drain output report over-current or over-temperature event which has deglitch typical 9ms timeout period typically 9ms.

The UTC **US206** incorporates protection circuits including current limiting circuit with foldback function, thermal shutdown circuit designed to prevent catastrophic switch failure due to increasing power dissipation when continuous heavy loads or short circuit occurs. Besides, a built-in charge pump is used to drive the N-channel MOSFET that is free of parasitic body diode to eliminate any reversed current flow across the switch when it is powered off.

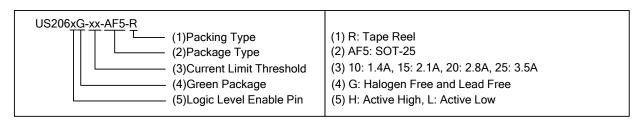
The UTC **US206** is applied in high-side power protection switch, USB power management, USB host and self-powered bubs, USB bus-powered hubs, hot plug-in power supplies, battery-charger circuits.

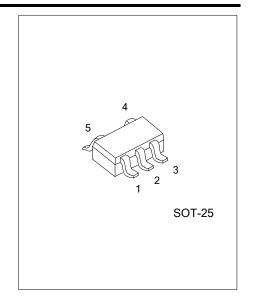


- * Operating on The Range of 3.0V to 5.5V
- * High-Side MOSFET with $85m\Omega$ R_{DS(ON)}
- * Quiescent Supply Current: 65µA
- * Available With 4 Versions of Current Limits with Foldback
- * Rise Time: 400µS (TYP.)
- * UVLO (Under Voltage Lockout)
- * Output Shutdown Pull-low Resister
- * Shutdown Supply Current: 1µA (MAX.)
- * Logic Level Enable Pin, Available with Active-High or Active-Low Version
- * Reverse Current is Not Generated when in Power Off State
- * Deglitched Open-Drain Over-Current Flag Output (OC)

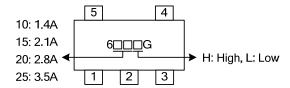
ORDERING INFORMATION

Ordering Number	Package	Packing
US206xG-xx-AF5-R	SOT-25	Tape Reel

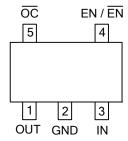




■ MARKING



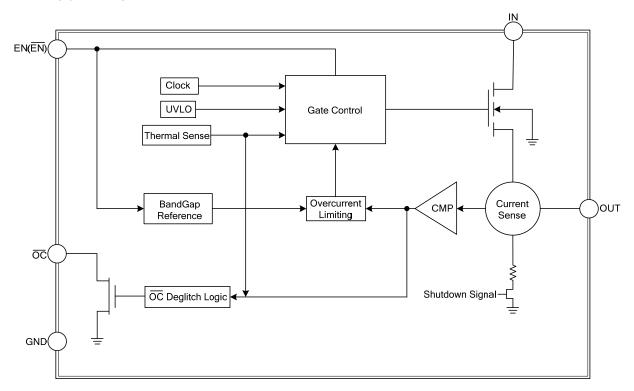
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	OUT	Switch Output: Output MOSFET Source. Typically connect to switched side of load.
2	GND	Ground
3	IN	Input Supply: Output MOSFET Drain, which also supplies IC's internal circuitry. Connect to positive supply.
4	EN(EN)	Enable: Logic level enable input. Make sure EN pin never floating.
5	<u>oc</u>	Overcurrent open-drain OC output

■ BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{IN}	6	V
Output Voltage	V_{OUT}	6	V
Output Current	I _{OUT}	Internally Limited	
Power Dissipation (T _A = 25°C)	P_{D}	0.4	W
Enable Input	V_{EN}	-0.3 ~ 6	V
Storage Temperature	Ts	-65 ~ +150	°C
Reflow Temperature (soldering, 10sec)		260	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

OPERATING RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{IN}	+3 ~ +5.5	V
Operating Temperature	TA	-40 ~ +85	°C

■ THERMAL DATA

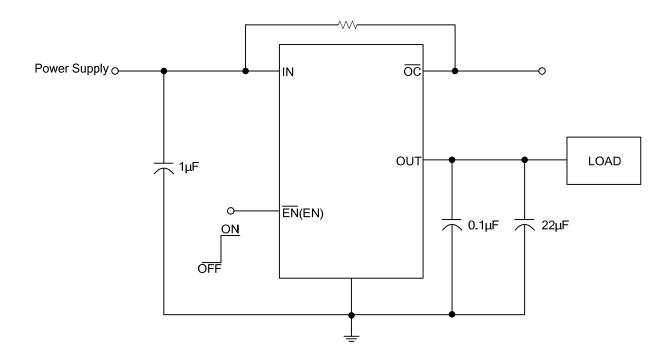
PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	250	°C/W

■ ELECTRICAL CHARACTERISTICS

 $(V_{IN}=5V, C_{IN}=1\mu F, C_{OUT}=1\mu F, R_L=10\Omega T_A=25^{\circ}C, unless otherwise noted.)$

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Input Voltage Rage	V_{IN}					5.5	V
Output MOS R _{DS(ON)}	R _{DS(ON)}	I _{OUT} =2A	US206-25	-	85	95	mΩ
		I _{OUT} =1.5A	US206-20				
Output MOS (NDS(ON)	TVDS(ON)	I _{OUT} =1A	US206-15				
		I _{OUT} =0.5A	US206-10				
Supply Current	I _{ON}				65		μA
Output Turn-on Rising Time	T _{ON}	R _L =10Ω, 90% Settling			400		μs
			US206-25	2.0	2.5	3.5	
Current Limit Threshold	loc	V _{OUT} =4V	US206-20	1.5	2.0	2.8	- A
Current Limit Threshold	IOC	V 001-4 V	US206-15	1.1	1.5	2.1	
			US206-10	0.7	1.0	1.4	
	I _{sc}	V _{OUT} =0V	US206-25	0.2	1.6	2.3	A
Short-circuit Current			US206-20	0.2	1.3	1.9	
Short-circuit Current			US206-15	0.2	1.0	1.4	
			US206-10	0.2	0.67	1.0	
EN Input Threshold	V_{EN}			1.4	1.6	1.8	V
Shutdown Supply Current	I _{OFF}				0.1	1	μΑ
Output Leakage Current	I _{LEAK}	EN="0", V _{OUT} =0V			0.5	1	μΑ
V _{IN} Under Voltage Lockout	V_{UVLO}			2.0	2.5	3.0	V
V _{IN} Under Voltage Hysteresis	$\triangle V_{UVLO}$				200		mV
Thermal Limit	T _{OT}				135		°C
Thermal Limit Hysteresis	$\triangle T_{OT}$				20		°C
OC Deglitch	T _{oc}	UTC US206, OC assertion	n or deassertion	4	9	15	ms
OC Output Low Voltage	V _{oc}	UTC US206, I oc = 5mA				0.4	V
OC Off-State Current	I _{OC-OFF}	UTC US206, V oc = 5V				1	μA

TYPICAL APPLICATION CIRCUIT



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