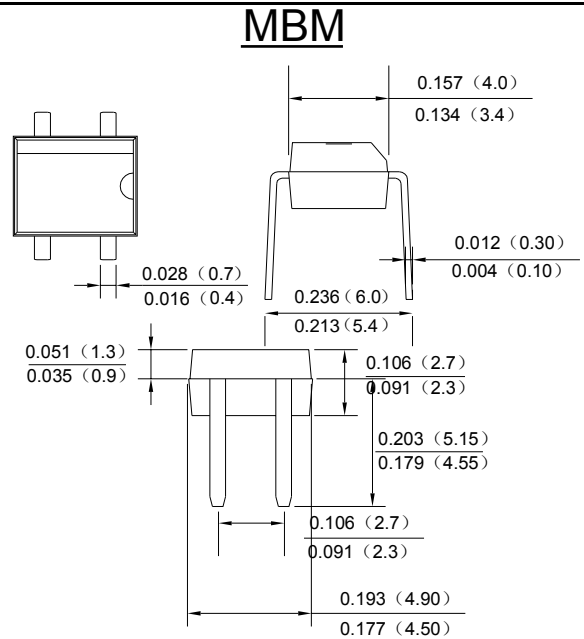


Features

- Glass Passivated Die Construction
- Low leakage
- Ideal for printed circuit board
- Surge overload rating-25A peak
- Designed for Surface Mount Application
- Plastic Material-UL Flammability 94V-0

Mechanical Data

- Case:Reliable low cost construction utilizing molded plastic technique
- Terminals:Plated Leads Solderable per MIL-STD-202,Method208
- Polarity:As Marked on Case
- Mounting Position:Any
- Marking:Type Number



Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.
 Single Phase, half wave, 60Hz, resistive or inductive load.
 For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	UMB1MN	UMB2MN	UMB4MN	UMB6MN	UMB8MN	UMB10MN	UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	100	200	400	600	800	1000	V
Working Peak Reverse Voltage	V_{RWM}							
DC Blocking Voltage	V_{DC}							
RMS Reverse Voltage	V_{RMS}	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1)@ $T_A=40^\circ C$	I_o	0.5						A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	25						A
Forward Voltage per element @ $I_F=0.5A$	V_{FM}	1.0	1.3	1.7			V	
Peak Reverse Current @ $T_A=25^\circ C$ At Rated DC Blocking Voltage @ $T_A=125^\circ C$	I_R	5.0			500			μA
Maximum reverse recovery time (Note 3)	T_{RR}	50			75			ns
Typical Junction Capacitance per leg	C_J	13						μF
	$R_{\theta JA}$	70						
Typical Thermal Resistance per leg (Note 4)	$R_{\theta JL}$	20						$^\circ C/W$
Operating and Storage Temperature Range	T_J, T_{STG}	-55to+150						$^\circ C$

- Note:1. Mounted on glass epoxy PC board with 1.3mm² solder pad.
 2. Reverse Recovery Test Conditions: $I_F=0.5A$, $I_R=1A$, $I_{rr}=0.25A$.
 3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

UMB1MN THRU UMB10MN

Fig. 1 Output Current Derating Curve

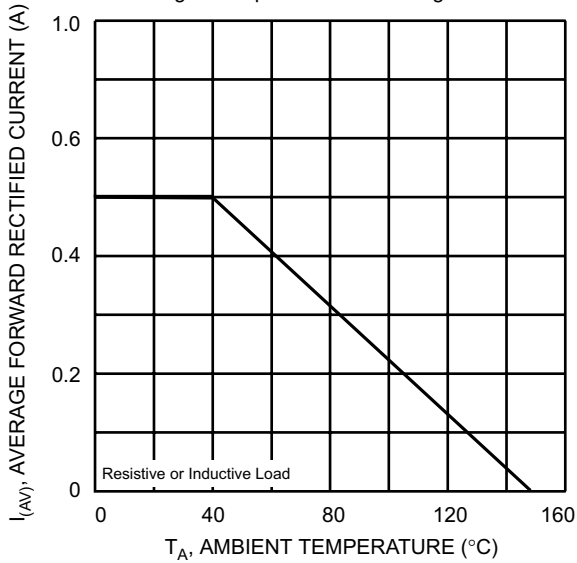


Fig. 2 Typical Forward Characteristics (per leg)

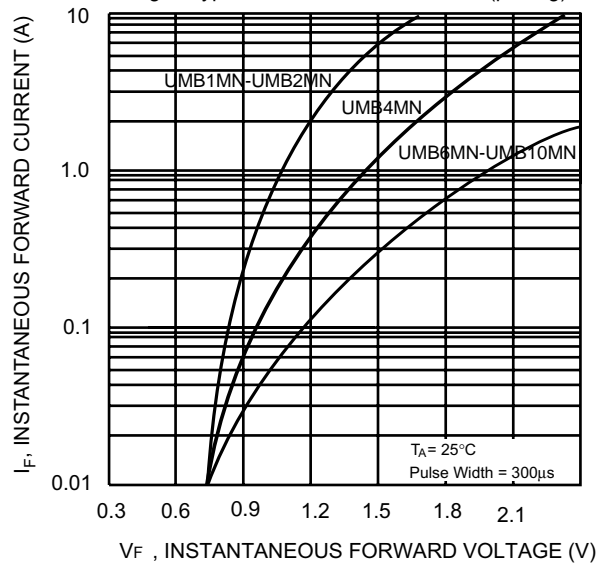


Fig. 3 Maximum Peak Forward Surge Current (per leg)

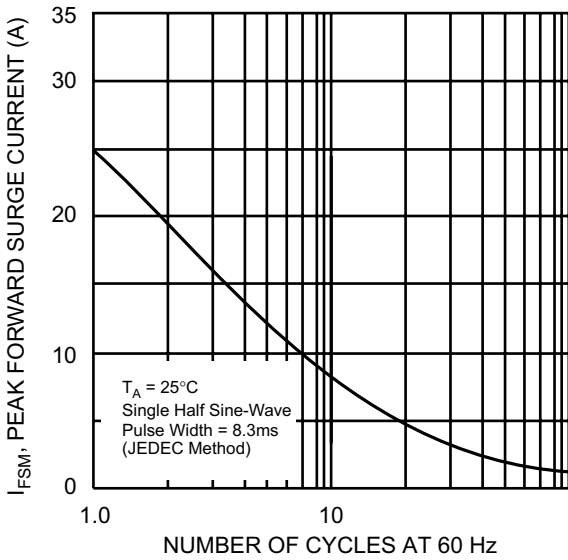


Fig. 4 Typical Junction Capacitance

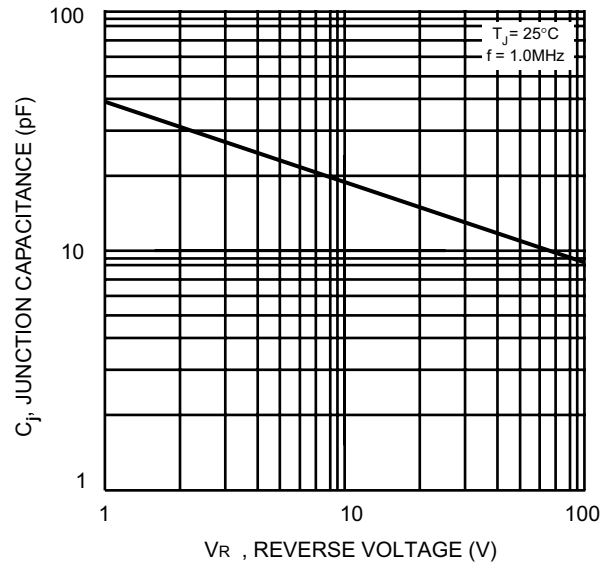


Fig. 5 Typical Reverse Characteristics (per element)

