

**DUAL SCHOTTKY RECTIFIERS**

**REVERSE VOLTAGE: 20 - 60 V**  
**FORWARD CURRENT: 10 A**

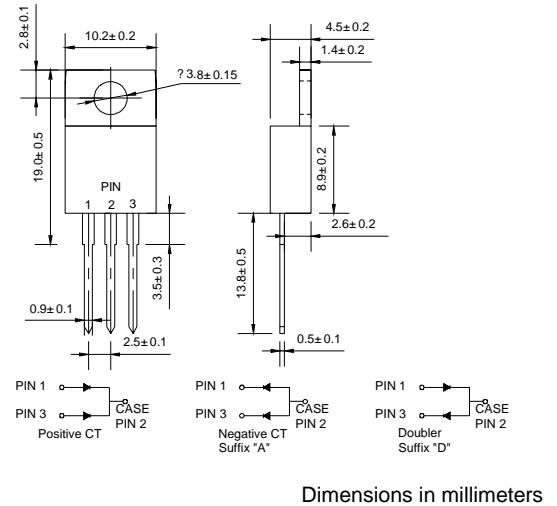
**FEATURES**

- ◇ Metal-semiconductor junction with guard ring
- ◇ Epitaxial construction
- ◇ For use in low voltage,high frequency inverters free wheeling,and polarity protection applications
- ◇ Low forward voltage drop,low switching losses
- ◇ High surge capability
- ◇ The plastic material carries U/L recognition 94V-0

**MECHANICAL DATA**

- ◇ Case:JEDEC TO--220AB,molded plastic
- ◇ Terminals: Plated leads, solderable per MIL-STD-750, Method 2026
- ◇ Polarity: As marked
- ◇ Weight: 0.08ounce, 2.24 grams
- ◇ Mounting position: Any

**TO-220AB**



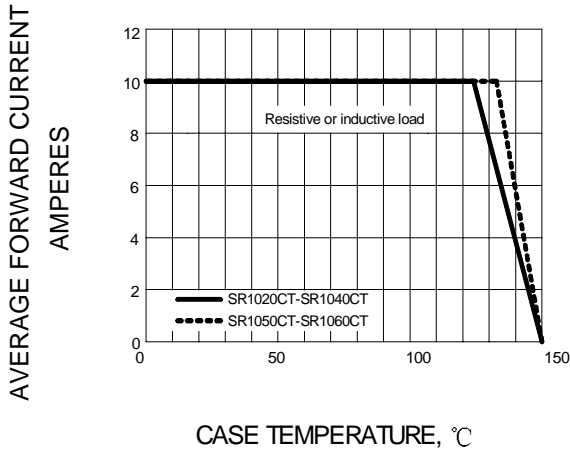
**MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS**

Ratings at 25°C ambient temperature unless otherwise specified.  
 Single phase,half wave,50 Hz,resistive or inductive load. For capacitive load,derate by 20%.

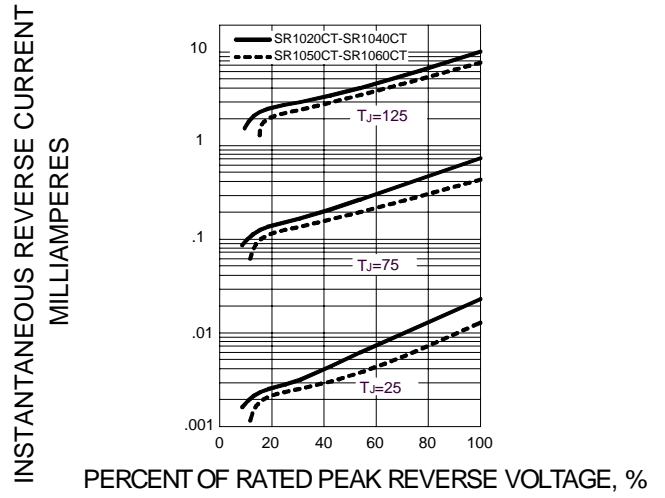
		SR 1020CT	SR 1030CT	SR 1040CT	SR 1050CT	SR 1060CT	UNITS
Maximum recurrent peak reverse voltage	V <sub>RRM</sub>	20	30	40	50	60	V
Working peak reverse voltage	V <sub>RMS</sub>	14	21	28	35	42	V
Maximum DC blocking voltage	V <sub>DC</sub>	20	30	40	50	60	V
Maximum average forward rectified current (See FIG.1)	I <sub>F(AV)</sub>	10.0					A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method) @T <sub>J</sub> =125°C	I <sub>FSM</sub>	175.0					A
Maximum instantaneous forward voltage per leg @ 5A (Note1)	V <sub>F</sub>	0.55		0.70			V
Maximum reverse current @T <sub>A</sub> =25°C at rated DC blocking voltage @T <sub>A</sub> =100°C	I <sub>R</sub>	0.5 50.0					mA
Operating junction temperature range	T <sub>J</sub>	-55 --- + 150					°C
Storage temperature range	T <sub>STG</sub>	-55 --- + 150					°C

Note: 1. Pulse test:300us pulse width,1% duty cycle.

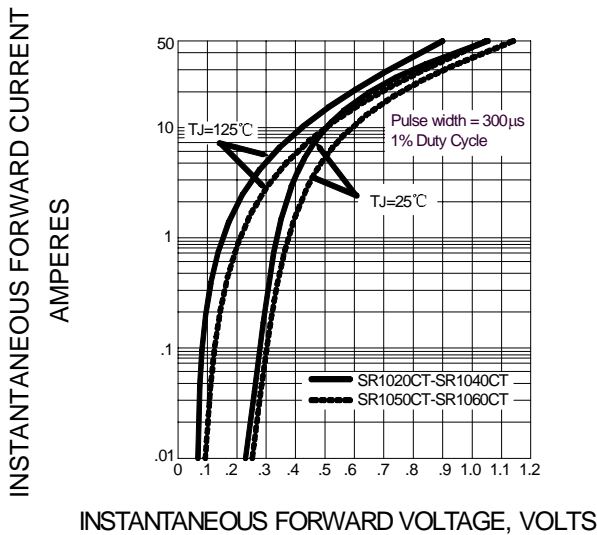
**FIG.1 – FORWARD CURRENT DERATING CURVE**



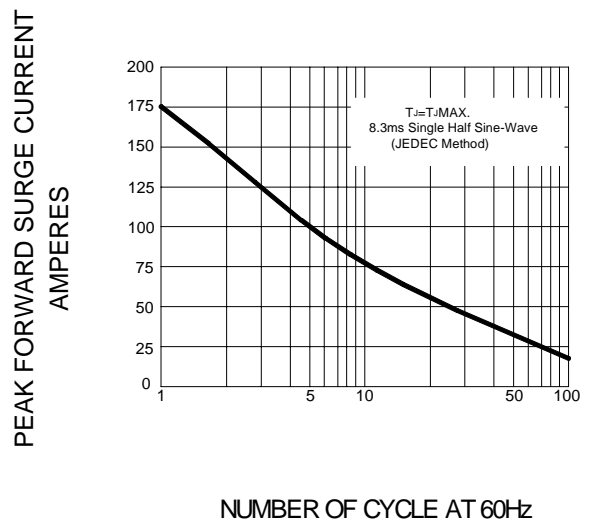
**FIG.2 – TYPICAL REVERSE CHARACTERISTICS PER LEG**



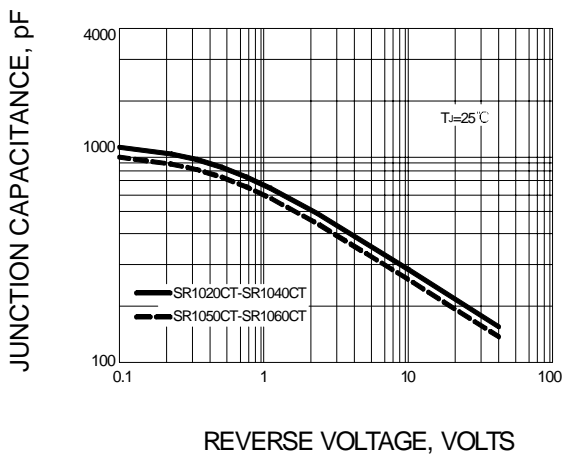
**FIG.3 – TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS PER LEG**



**FIG.4 – PEAK FORWARD SURGE CURRENT**



**FIG.5 – TYPICAL JUNCTION CAPACITANCE**



**FIG.6 – TYPICAL TRANSIENT THERMAL IMPEDANCE**

