2SB1651

Silicon PNP epitaxial planer type

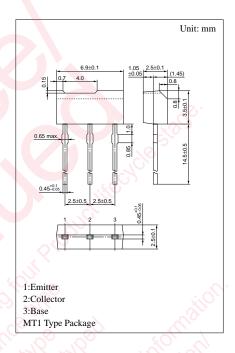
For low-frequency and low-noise amplification

Features

- Low noise voltage NV.
- High foward current transfer ratio h_{FE}.
- M type package allowing easy automatic and manual insertion as well as stand-alone fixing to the printed circuit board.

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	-55	V
Collector to emitter voltage	V_{CEO}	-55	V
Emitter to base voltage	$V_{\rm EBO}$	-5	V
Peak collector current	I_{CP}	-200	mA
Collector current	$I_{\rm C}$	-50	mA
Collector power dissipation	P_{C}	400	mW
Junction temperature	Tj	150	°C
Storage temperature	T_{stg}	−55 ~ +150	C.C.



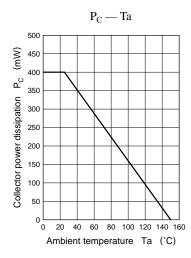
Electrical Characteristics (Ta=25°C)

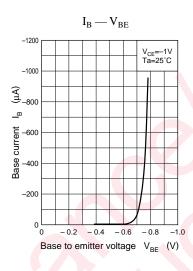
Parameter	Symbol	Conditions	min	typ	max	Unit
Collector cutoff current	I_{CBO}	$V_{CB} = -10V, I_E = 0$			-100	nA
	I _{CEO}	$V_{CE} = -10V, I_B = 0$			-1	μΑ
Collector to base voltage	V_{CBO}	$I_{\rm C} = -10\mu{\rm A}, I_{\rm E} = 0$	–55			V
Collector to emitter voltage	V _{CEO}	$I_C = -2mA$, $I_B = 0$	-55			V
Emitter to base voltage	V _{EBO}	$I_{\rm E} = -10\mu{\rm A}, I_{\rm C} = 0$	-5			V
Forward current transfer ratio	h _{FE} *1	$V_{CE} = -5V, I_{C} = -2mA$	180		700	
Collector to emitter saturation voltage	V _{CE(sat)}	$I_C = -100 \text{mA}, I_B = -10 \text{mA}$			-0.6	V
Base to emitter voltage	V _{BE}	$V_{CE} = -1V, I_{C} = -100 \text{mA}$			-1	V
Noise voltage	NV	$V_{CE} = -10V, I_{C} = -1mA, G_{V} = 80dB,$		110		mV
		$R_g = 100k\Omega$, Function = FLAT		110		111 V
Transition frequency	f_T	$V_{CB} = -5V, I_E = 2mA, f = 200MHz$ 150			MHz	

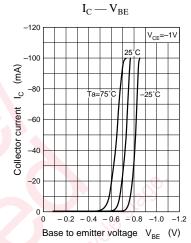
*1hFE Rank classification

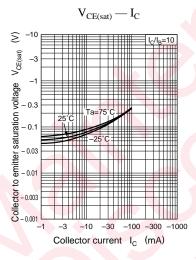
Rank	R	S	Т
h_{FE}	180 ~ 360	260 ~ 520	360 ~ 700

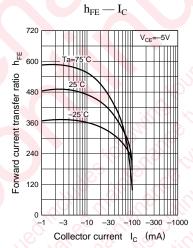
Transistor 2SB1651

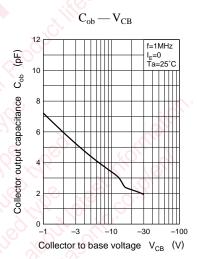












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