### NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE8651Q uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

V<sub>DS</sub> =20V,I<sub>D</sub> =10A

 $R_{DS(ON)} < 11 m\Omega @ V_{GS} = 4.5 V$ 

 $R_{DS(ON)}$  < 11.5m $\Omega$  @  $V_{GS}$ =4V

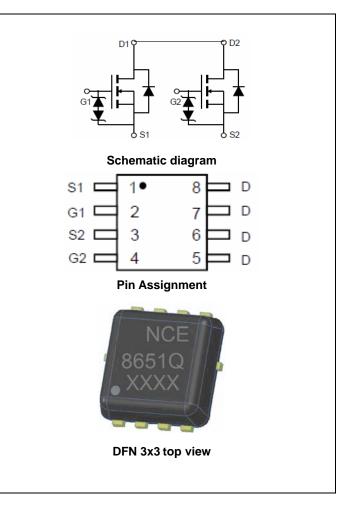
 $R_{DS(ON)} < 12.5 \text{m}\Omega$  @  $V_{GS} = 3.1 \text{V}$ 

 $R_{DS(ON)}$  < 15.5m $\Omega$  @  $V_{GS}$ =2.5V

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- 2.5V Drive
- Common-drain type

#### **Application**

- Battery protection switch
- Mobile device battery charging and discharging



#### **Package Marking and Ordering Information**

	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
	NCE8651Q	NCE8651Q	DFN 3x3	-	-	-

#### Absolute Maximum Ratings (T<sub>C</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Drain Current-Continuous	I <sub>D</sub>	10	А
Pulsed Drain Current	I <sub>DM</sub>	32	Α
Maximum Power Dissipation	P <sub>D</sub>	1.5	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}\mathbb{C}$

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	83	°C/W

NCE8651Q

# Electrical Characteristics (TC=25°C unless otherwise noted)

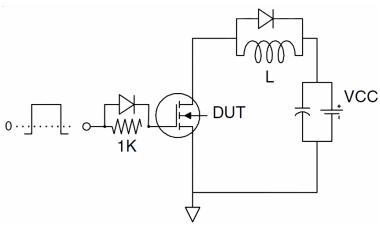
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	20		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V,V <sub>DS</sub> =0V	-	-	±10	μA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	0.5	0.7	1	V
		$V_{GS}$ =4.5V, $I_D$ =10A	I <sub>D</sub> =10A - 7.2	7.2	11	- mΩ
Drain Source On State Besistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4 V, I <sub>D</sub> =5A	-	7.4	11.5	
Drain-Source On-State Resistance		V <sub>GS</sub> =3.1V, I <sub>D</sub> =5A		7.8	12.5	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =2.5A		8.6	15.5	
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =10V,I <sub>D</sub> =5A	5	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\\ 40\\\\ 0\\	-	1255	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =10V, $V_{GS}$ =0V, F=1.0MHz	-	220	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVIHZ	-	168	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	300	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =10 $V$ , $I_{D}$ =5 $A$	-	1000	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{GEN}$ =50 $\Omega$	-	4000	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	2500	-	nS
Total Gate Charge	Qg	V 40VI 40A	-	29	-	nC
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =10V,I <sub>D</sub> =10A,	-	5.2	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	6.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =10A	-	0.85	1.2	V
Diode Forward Current (Note 2)	Is		-	-	10	Α

#### Notes:

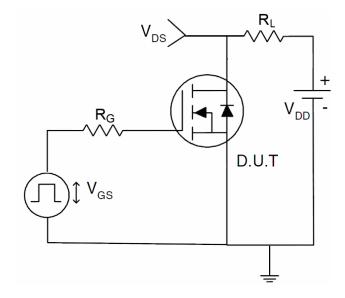
- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature}.$
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

# **Test Circuit**

# 1) Gate Charge Test Circuit

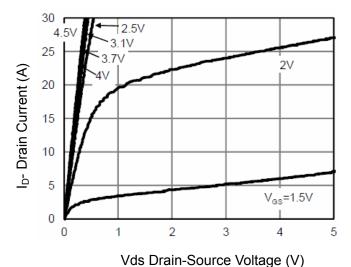


# 2) Switch Time Test Circuit

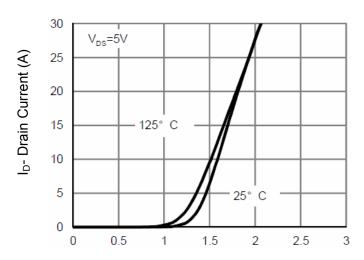




# **Typical Electrical and Thermal Characteristics (Curves)**



**Figure 1 Output Characteristics** 



Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics

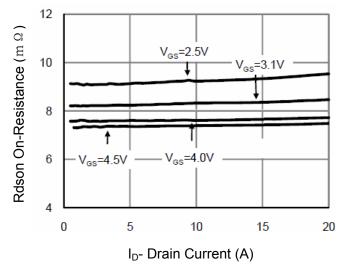
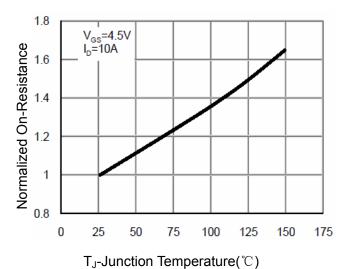
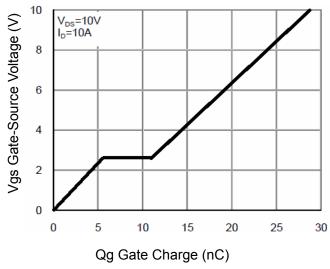


Figure 3 Rdson- Drain Current



**Figure 4 Rdson-Junction Temperature** 



**Figure 5 Gate Charge** 

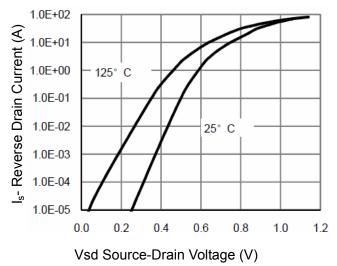
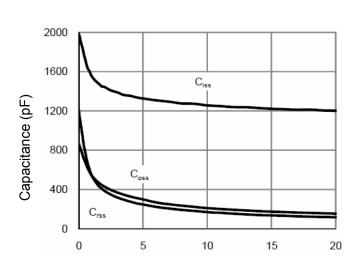
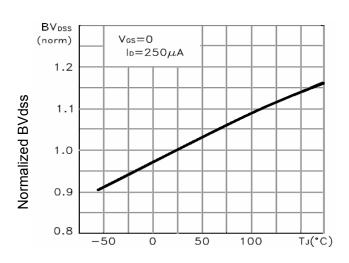


Figure 6 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)
Figure 7 Capacitance vs Vds



 $T_J$ -Junction Temperature (°C) Figure 9 BV<sub>DSS</sub> vs Junction Temperature

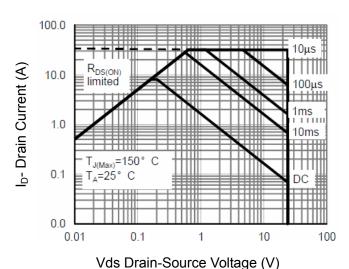
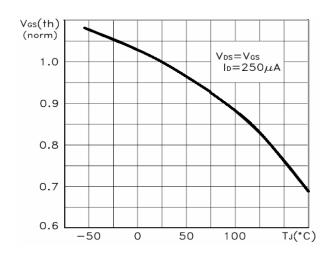
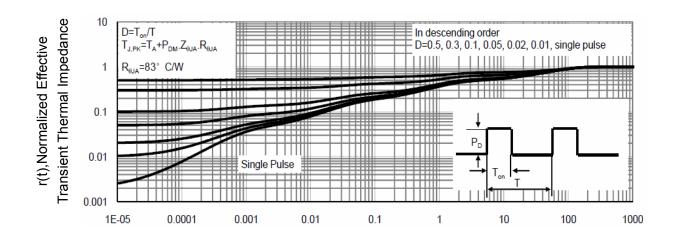


Figure 8 Safe Operation Area



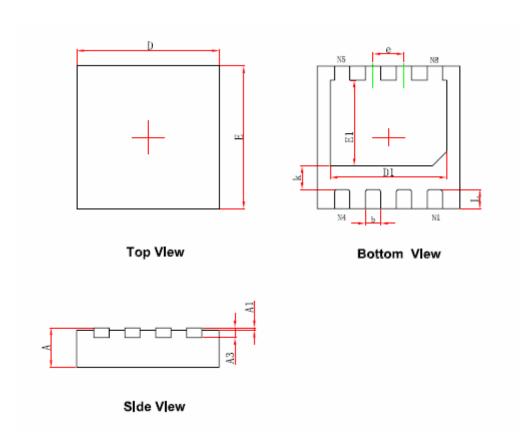
 $\label{eq:TJ-Junction} $T_{J}$-Junction Temperature(^{\circ}C)$ Figure 10 $V_{GS(th)}$ vs Junction Temperature$ 



Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance

# **DFN3X3 EP Package Information**



Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035	
A1	0.000	0.050	0.000	0.002	
A3	0.203REF.		0.008REF.		
D	2.924	3.076	0.115	0.121	
E	2.924	3.076	0.115	0.121	
D1	2.350	2.550	0.093	0.100	
E1	1.700	1.900	0.067	0.075	
k	0.450	0.550	0.018	0.022	
b	0.270	0.370	0.011	0.015	
е	0.650TYP.		0.026TYP.		
L	0.324	0.476	0.013	0.019	

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