

# RFMR57-CTRM-020SP-500A



#### **Product Features**

- GaN on SiC Technology
- High Efficiency
- 50 Ohm Input/Output Impedance
- 6BIT phase and attenuation control

### **Application**

• C-Band Radar



### **Description**

- RFMR57-CTRM-020SP-500A is T/R Module (Transmitter/Receiver Module) operating at 5.4-5.9GHz performing 20W output power.
- · Circulator provides duplexer function and has a built in Limiter Diode and switch which protects receiver.
- 6-Bit Phase Shifter and Attenuator are controlled by serial data.
- · Bias sequencing is required

### **Electrical Specifications** @ Ta=25°C, 50Ω System

PARAMETER	Unit	Min	Тур	Max	SYMBOL
Frequency	GHz	5.4	-	5.9	F
Peak Output Power	W	20	_	<u>_</u>	Pout
Transmit Gain Flatness	dBp-p	-		2.0	ΔGp
Transmit input Power	dBm	-	5	_	Pin
Pulse width	us	-	-	50	Pw
Duty Cycle	%	-		10	Dc
Pulse droop	dB	-	-	1	A Droop
Pulse Rise & Fall time	ns	L-: _	-	100	Tr / Tf
Transmit Harmonics 1 to N	dBc	20	CO	m-	Hn
TX Input & RX Input Return Loss	dB	-	-	-12	RL
Receiver Gain	dB	-	25		Gr
RX Gain Flatness	dBp-p	-	-	2	Gf
RX Noise Figure	dB	-	3.5	- /	N
Receiver output 1dB compression	dBm	+3	-	<i>y</i>	Rp1
Gain Variation with phase control	dB	-	-	2.5	Av
Digital attenuator RMS error	dB	-		2.0	AE
Digital Phase shifter RMS error	deg	-	-	5	PE
Tx/Rx switching time	us	-		1	Ts
Efficiency	%	30		-	Eff
Digital Attenuator		6 BIT, LSB=0.5dB, 0 ~ 31.5dB			AT
Digital Phase shifter		6 BIT, LSB=5.625°, 0 ~ 354.625°			PS
Operating Voltage		DC +45V, DC+5.0V, DC -2.7V			V

1/3

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### **Environmental Specifications**

Operating Flange Temperature -10 ℃ to +50 ℃
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### **Mechanical Specifications**

Physiacl Dimension ( W × D × H )	122mm X 26.5mm X 12.5mm		
DC & Control signal Connector	Pin Header ( 2 X 9 18pin, 2mm pitch)		
RF Connector	Ceramic pin / SMP Connector		

### Interface

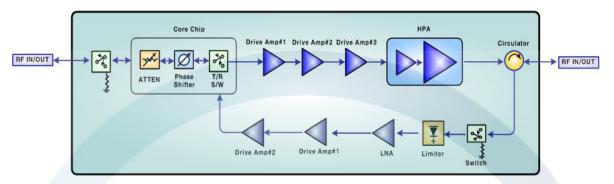
No	Function	Description	
1	Data	Core Chip Data (TTL)	
2	DC -2.7V	Core Chip Vcc	
3	Clock	Core Chip Clock(TTL)	
4	TX Path Enable	TX Path Control	
5	Enable	Core Chip Latch Enable(TTL)	
6	RX Path Enable	RX Path Control	
7	Termination S/W Control	Termination Switch Control (TTL)	
8	DC +5.0V	Drive Amp Voltage	
9	Data out	Core Chip Data Out	
10	Temp Sensor	0.4V ~ 1.2V (-15°C ~ +65°C)	
11	GND	Ground	
12	NC	NOT Connect	
13	GND	PA Gate Control	
14	HPA Gate Voltage	-3.4V : ON , -8V : OFF	
15	GND	Ground	
16	GND	Ground	
17	DC +45V	DC +45V HPA Drain Voltage	
18	DC +45V	HPA Drain Voltage	



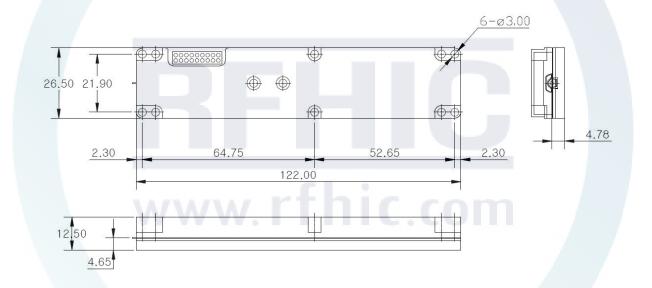
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### **Block Diagram**



### **TRM Drawing**



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