

flow2	1200V/50A
Features	
<ul style="list-style-type: none"> <li>• 3~rectifier,BRC,Inverter, NTC</li> <li>• Very Compact housing, easy to route</li> <li>• Mitsubishi IGBT and FWD</li> </ul>	
Target Applications	
<ul style="list-style-type: none"> <li>• Motor Drives</li> <li>• Power Generation</li> </ul>	
Types	
• V23990-P768-A50	

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>Input Rectifier Diode</b>				
Repetitive peak reverse voltage	$V_{RRM}$		1600	V
DC forward current	$I_{FAV}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	80 80	A
Surge forward current	$I_{FSM}$	$t_p=10\text{ms}$	490	A
$I^2t$ -value	$I^2t$	$T_j=150^\circ\text{C}$	1200	$\text{A}^2\text{s}$
Power dissipation per Diode	$P_{\text{tot}}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	95 144	W
Maximum Junction Temperature	$T_{j\max}$		150	$^\circ\text{C}$

## Inverter Transistor

Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	60 77	A
Pulsed collector current	$I_{Cpulse}$	$t_p$ limited by $T_{j\max}$	100	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$ , $T_j \leq T_{j\max}$	100	A
Power dissipation per IGBT	$P_{\text{tot}}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	155 235	W
Gate-emitter peak voltage	$V_{GE}$		$\pm 20$	V
Short circuit ratings	$t_{sc}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 850	$\mu\text{s}$ V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>Inverter Diode</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$	$T_j=25^\circ\text{C}$	1200	V
DC forward current	$I_F$	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	82 106	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\text{max}$	100	A
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	126 191	W
Maximum Junction Temperature	$T_j\text{max}$		175	°C
<b>Brake Transistor</b>				
Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	45 56	A
Pulsed collector current	$I_{Cpuls}$	$t_p$ limited by $T_j\text{max}$	135	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$ , $T_j \leq T_{top\ max}$	70	A
Power dissipation per IGBT	$P_{tot}$	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	137 208	W
Gate-emitter peak voltage	$V_{GE}$		±20	V
Short circuit ratings	$t_{sc}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_j\text{max}$		175	°C
<b>Brake Inverse Diode</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$	$T_c=25^\circ\text{C}$	1200	V
DC forward current	$I_F$	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	16 16	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\text{max}$	20	A
Brake Inverse Diode	$P_{tot}$	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	69 98	W
Maximum Junction Temperature	$T_j\text{max}$		175	°C
<b>Brake Diode</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$	$T_j=25^\circ\text{C}$	1200	V
DC forward current	$I_F$	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	28 35	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\text{max}$	50	A
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	72 109	W
Maximum Junction Temperature	$T_j\text{max}$		175	°C

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit

### Thermal Properties

Storage temperature	$T_{stg}$		-40...+125	°C
Operation temperature under switching condition	$T_{op}$		-40...+( $T_{jmax} - 25$ )	°C

### Insulation Properties

Insulation voltage	$V_{is}$	t=2s	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm
Comparative tracking index	CTI			>200	

**Characteristic Values**

Parameter	Symbol	Conditions				Value			Unit	
		$V_{GE}$ [V] or $V_{GS}$ [V]	$V_T$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_C$ [A] or $I_F$ [A] or $I_D$ [A]	$T_J$	Min	Typ	Max		
<b>Input Rectifier Diode</b>										
Forward voltage	$V_F$			50	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$	1	1,1 1,05	1,8	V	
Threshold voltage (for power loss calc. only)	$V_{to}$			50	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		0,89 0,78		V	
Slope resistance (for power loss calc. only)	$r_t$			50	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		4 5		$\text{m}\Omega$	
Reverse current	$I_r$		1500		$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$			0,1	$\text{mA}$	
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Phase-Change Material					0,74		K/W	
Thermal resistance chip to heatsink per chip	$R_{thJC}$						0,49			
<b>Inverter Transistor</b>										
Gate emitter threshold voltage	$V_{GE(\text{th})}$	$V_{CE}=V_{GE}$		10	0,005	$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	5,4	6	6,6	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$		15		50	$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	1,4	1,80 2,18	2,3	V
Collector-emitter cut-off current incl. Diode	$I_{CES}$		0	1200		$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$			300	$\mu\text{A}$
Gate-emitter leakage current	$I_{GES}$		20	0		$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$			500	$\text{nA}$
Integrated Gate resistor	$R_{gint}$							none		$\Omega$
Turn-on delay time	$t_{d(on)}$	$R_{goff}=8 \Omega$ $R_{gon}=8 \Omega$	$\pm 15$	600	50	$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	65,4 64,8		ns	
Rise time	$t_r$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	9,8 11,6			
Turn-off delay time	$t_{d(off)}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	137 189			
Fall time	$t_f$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	58,6 99,8			
Turn-on energy loss per pulse	$E_{on}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	1,994 3,311		mWs	
Turn-off energy loss per pulse	$E_{off}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	2,564 4,317			
Input capacitance	$C_{ies}$	$f=1\text{MHz}$	0	10		$T_J=25^\circ\text{C}$	5000		pF	
Output capacitance	$C_{oss}$						1000			
Reverse transfer capacitance	$C_{rss}$						80			
Gate charge	$Q_{\text{Gate}}$		15	600	50	$T_J=25^\circ\text{C}$	117			$\text{nC}$
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Phase-Change Material					0,61		K/W	
Thermal resistance chip to case per chip	$R_{thJC}$						0,41			
<b>Inverter Diode</b>										
Diode forward voltage	$V_F$				50	$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	1,4 1,2	1,21 2,2		V
Peak reverse recovery current	$I_{RRM}$	$R_{gon}=8 \Omega$	$\pm 15$	600	50	$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	80 81		A	
Reverse recovery time	$t_{rr}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	156 470			
Reverse recovered charge	$Q_{rr}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	6,95 12,53			
Peak rate of fall of recovery current	$d(i_{rec})/\text{max dt}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	4237 1162			
Reverse recovered energy	$E_{rec}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	3,314 6,025		mWs	
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Phase-Change Material					0,75			
Thermal resistance chip to case per chip	$R_{thJC}$						0,5			

**Characteristic Values**

Parameter	Symbol	Conditions					Value			Unit
			V <sub>GE</sub> [V] or V <sub>GS</sub> [V]	V <sub>I</sub> [V] or V <sub>CE</sub> [V] or V <sub>DS</sub> [V]	I <sub>C</sub> [A] or I <sub>F</sub> [A] or I <sub>D</sub> [A]	T <sub>J</sub>	Min	Typ	Max	
<b>Brake Transistor</b>										
Gate emitter threshold voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> =V <sub>GE</sub>			0,0012	T <sub>J</sub> =25°C T <sub>J</sub> =150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>		15		35	T <sub>J</sub> =25°C T <sub>J</sub> =150°C	1,5	1,92 2,37	2,3	V
Collector-emitter cut-off incl diode	I <sub>CES</sub>		0	1200		T <sub>J</sub> =25°C T <sub>J</sub> =150°C			250	μA
Gate-emitter leakage current	I <sub>GES</sub>		20	0		T <sub>J</sub> =25°C T <sub>J</sub> =150°C			120	nA
Integrated Gate resistor	R <sub>gint</sub>							none		Ω
Turn-on delay time	t <sub>d(on)</sub>	R <sub>goff</sub> =16 Ω R <sub>gon</sub> =16 Ω	±15	600	35	T <sub>J</sub> =25°C T <sub>J</sub> =150°C		82,8 89		ns
Rise time	t <sub>r</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		27 27		
Turn-off delay time	t <sub>d(off)</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		191,4 269		
Fall time	t <sub>f</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		54,3 124,9		
Turn-on energy loss per pulse	E <sub>on</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		2 2,92		mWs
Turn-off energy loss per pulse	E <sub>off</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		1,74 3,18		
Input capacitance	C <sub>ies</sub>							1950		pF
Output capacitance	C <sub>oss</sub>					T <sub>J</sub> =25°C		155		
Reverse transfer capacitance	C <sub>rss</sub>							115		
Gate charge	Q <sub>Gate</sub>		15	960	35	T <sub>J</sub> =25°C		160		nC
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Phase-Change Material						0,69		K/W
Thermal resistance chip to case per chip	R <sub>thJC</sub>							0,46		
<b>Brake Inverse Diode</b>										
Diode forward voltage	V <sub>F</sub>				10	T <sub>J</sub> =25°C T <sub>J</sub> =150°C	1,2	1,80 1,76	2,2	V
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Phase-Change Material						1,38		K/W
Thermal resistance chip to case per chip	R <sub>thJC</sub>							0,91		
<b>Brake Diode</b>										
Diode forward voltage	V <sub>F</sub>				25	T <sub>J</sub> =25°C T <sub>J</sub> =150°C	1	2,24 2,36	2,9	V
Reverse leakage current	I <sub>r</sub>			1200		T <sub>J</sub> =25°C T <sub>J</sub> =150°C			60	μA
Peak reverse recovery current	I <sub>RRM</sub>	R <sub>gon</sub> =16 Ω R <sub>goff</sub> =16 Ω	±15	600	35	T <sub>J</sub> =25°C T <sub>J</sub> =150°C		30,8 39,2		A
Reverse recovery time	t <sub>rr</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		146,4 423,1		ns
Reverse recovered charge	Q <sub>rr</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		2,32 4,84		
Peak rate of fall of recovery current	di(rec)max /dt					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		1749 917		A/μs
Reverse recovery energy	E <sub>rec</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		0,91 1,98		
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Phase-Change Material						1,32		K/W
Thermal resistance chip to case per chip	R <sub>thJC</sub>							0,87		

**Characteristic Values**

Parameter	Symbol	Conditions				Value			Unit
		$V_{GE}$ [V] or $V_{GS}$ [V]	$V_r$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_c$ [A] or $I_F$ [A] or $I_D$ [A]	$T_j$	Min	Typ	Max	

**Thermistor**

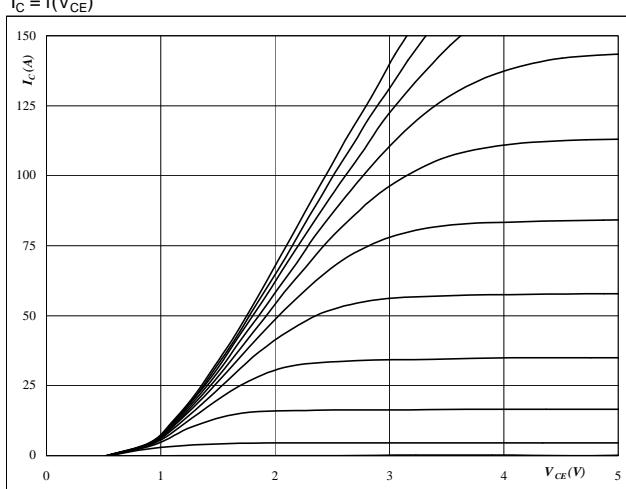
Rated resistance	R				T=25°C		21511		Ω
Deviation of R100	ΔR/R	R100=1486 Ω			T=25°C	-4,5		+4,5	%
Power dissipation	P				T=25°C		210		mW
Power dissipation constant					T=25°C		3,5		mW/K
B-value	$B_{(25/50)}$				T=25°C		3884		K
B-value	$B_{(25/100)}$				T=25°C		3964		K
Vincotech NTC Reference								F	

**Module Properties**

Thermal resistance, case to heatsink	$R_{thCH}$					tbd.			K/W
Module stray inductance	$L_{sCE}$					5			nH
Chip module lead resistance, terminals -chip	$R_{cc'1+EE'}$					tbd.			mΩ
Mounting torque	M					3,8	4	4,2	Nm
Terminal connection torque	M					6,7	7	7,4	Nm
Weight	G					tbd.			g

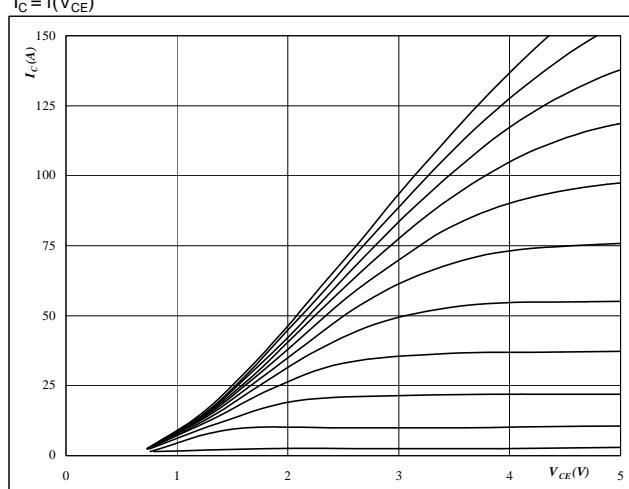
## Output Inverter

**Figure 1**  
Typical output characteristics  
 $I_C = f(V_{CE})$



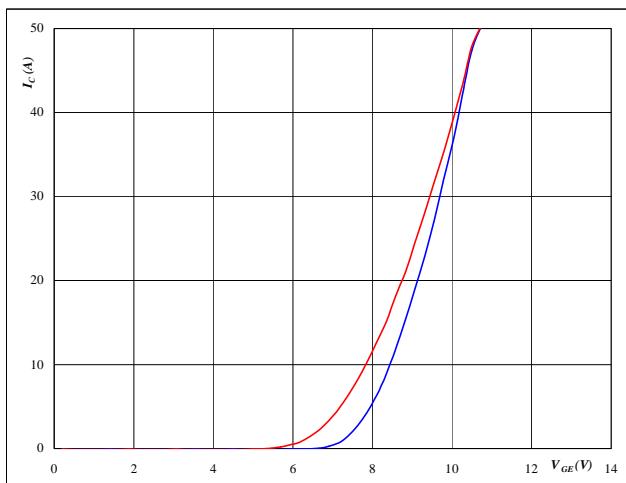
**At**  
 $t_p = 250 \mu s$   
 $T_j = 25^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 2**  
Typical output characteristics  
 $I_C = f(V_{CE})$



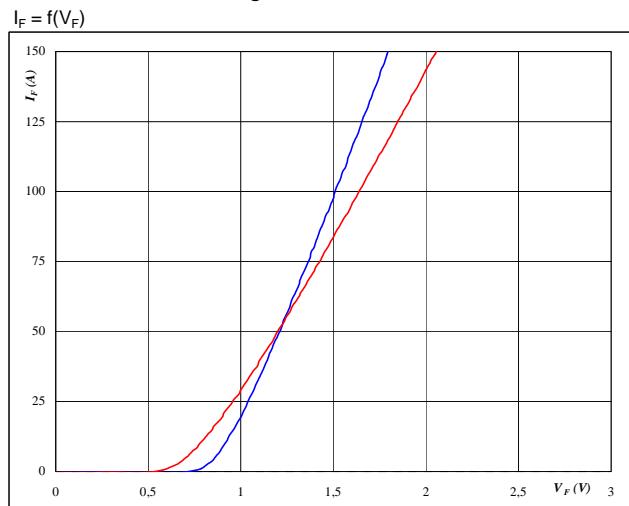
**At**  
 $t_p = 250 \mu s$   
 $T_j = 150^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**  
Typical transfer characteristics  
 $I_C = f(V_{GE})$



**At**  
 $T_j = 25/150^\circ C$   
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

**Figure 4**  
Typical diode forward current as a function of forward voltage  
 $I_F = f(V_F)$



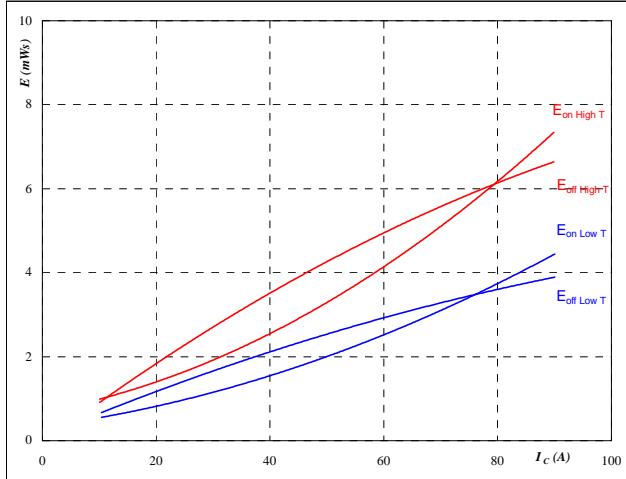
**At**  
 $t_p = 250 \mu s$

## Output Inverter

**Figure 5** Output inverter IGBT

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



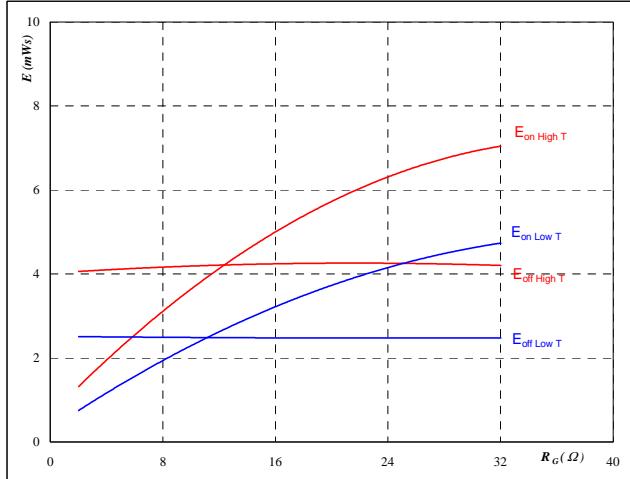
With an inductive load at

$$\begin{aligned} T_j &= \textcolor{red}{25/150} \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \\ R_{goff} &= 8 \quad \Omega \end{aligned}$$

**Figure 6** Output inverter IGBT

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



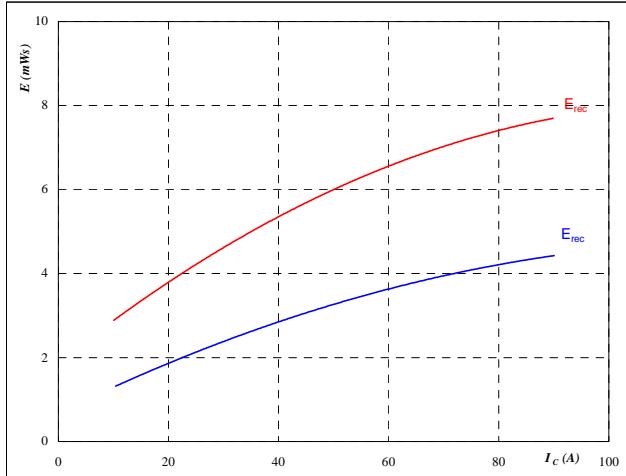
With an inductive load at

$$\begin{aligned} T_j &= \textcolor{red}{25/150} \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 50 \quad \text{A} \end{aligned}$$

**Figure 7** Output inverter FWD

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



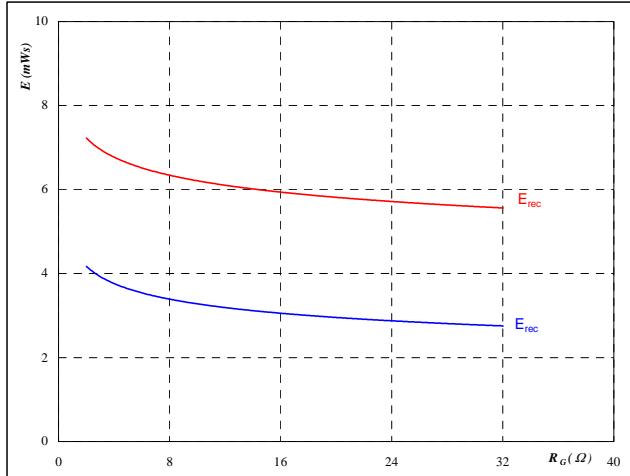
With an inductive load at

$$\begin{aligned} T_j &= \textcolor{red}{25/150} \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

**Figure 8** Output inverter FWD

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



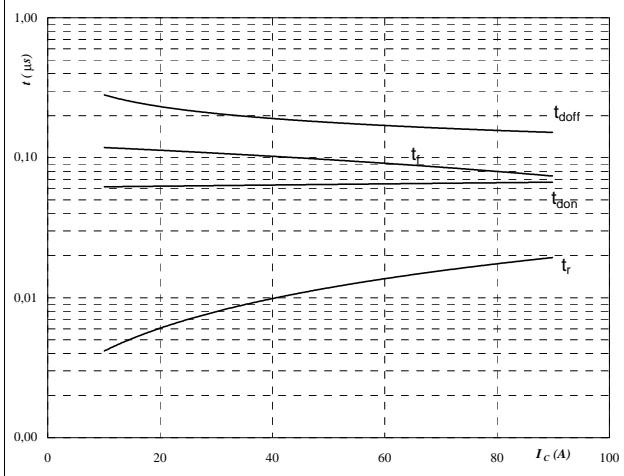
With an inductive load at

$$\begin{aligned} T_j &= \textcolor{red}{25/150} \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 50 \quad \text{A} \end{aligned}$$

## Output Inverter

**Figure 9** Output inverter IGBT

Typical switching times as a function of collector current  
 $t = f(I_C)$

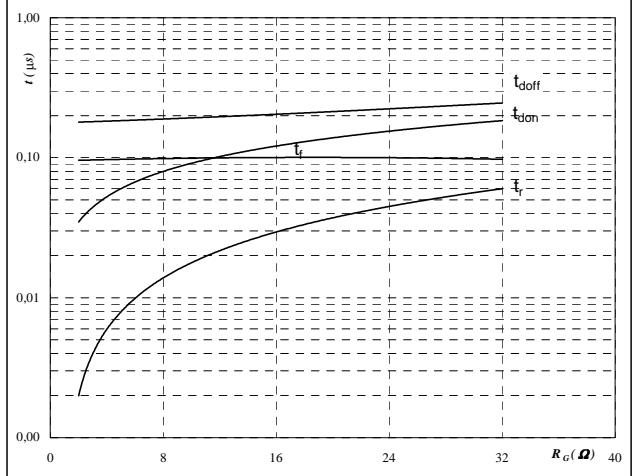


With an inductive load at

$T_j = 150 \text{ }^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 8 \Omega$   
 $R_{goff} = 8 \Omega$

**Figure 10** Output inverter IGBT

Typical switching times as a function of gate resistor  
 $t = f(R_G)$

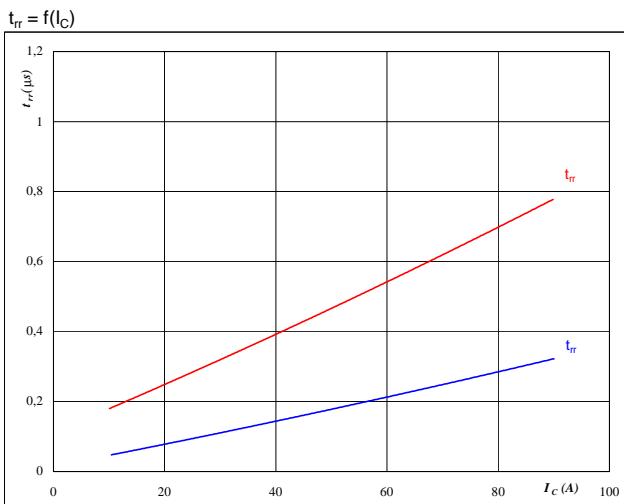


With an inductive load at

$T_j = 150 \text{ }^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_C = 50 \text{ A}$

**Figure 11** Output inverter FWD

Typical reverse recovery time as a function of collector current  
 $t_{rr} = f(I_C)$

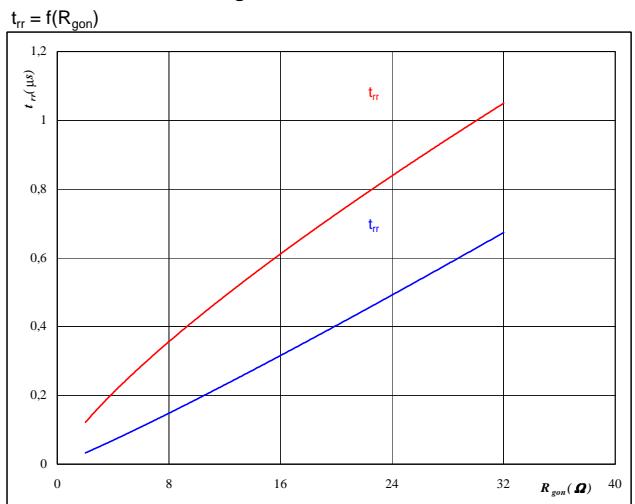


At

$T_j = 25/150 \text{ }^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 8 \Omega$

**Figure 12** Output inverter FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor  
 $t_{rr} = f(R_{gon})$



At

$T_j = 25/150 \text{ }^\circ\text{C}$   
 $V_R = 600 \text{ V}$   
 $I_F = 50 \text{ A}$   
 $V_{GE} = \pm 15 \text{ V}$

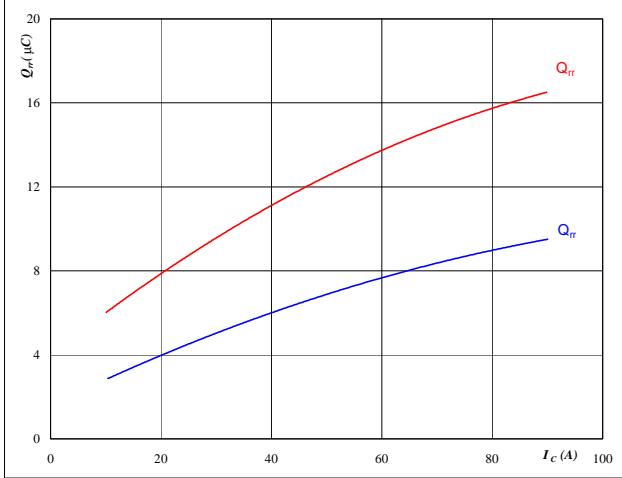
## Output Inverter

**Figure 13**

Output inverter FWD

**Typical reverse recovery charge as a function of collector current**

$$Q_{rr} = f(I_C)$$


**At**

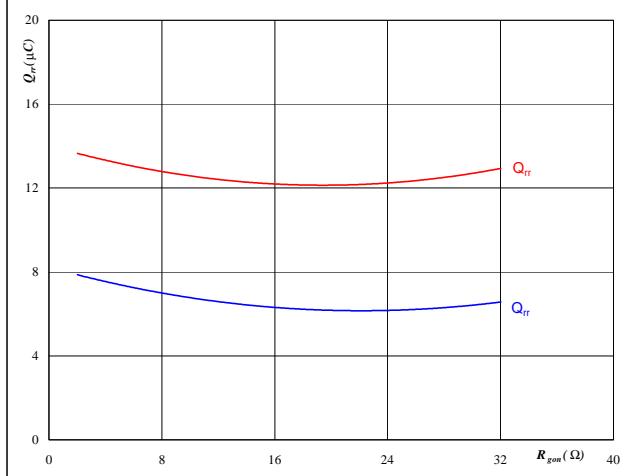
$$\begin{aligned} T_j &= \textcolor{blue}{25/150} \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

**Figure 14**

Output inverter FWD

**Typical reverse recovery charge as a function of IGBT turn on gate resistor**

$$Q_{rr} = f(R_{gon})$$


**At**

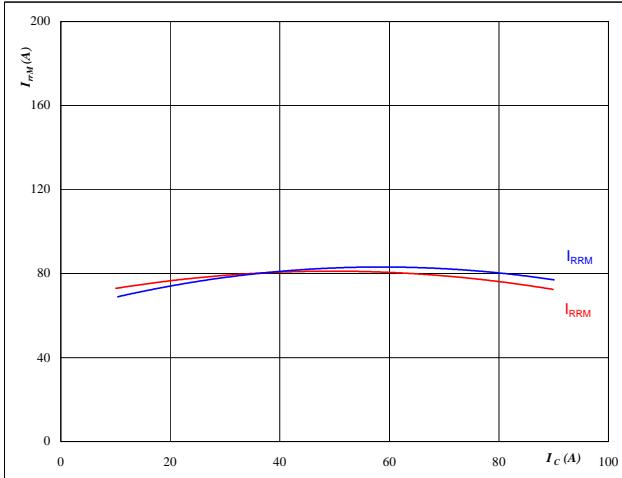
$$\begin{aligned} T_j &= \textcolor{blue}{25/150} \quad ^\circ\text{C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 50 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

**Figure 15**

Output inverter FWD

**Typical reverse recovery current as a function of collector current**

$$I_{RRM} = f(I_C)$$


**At**

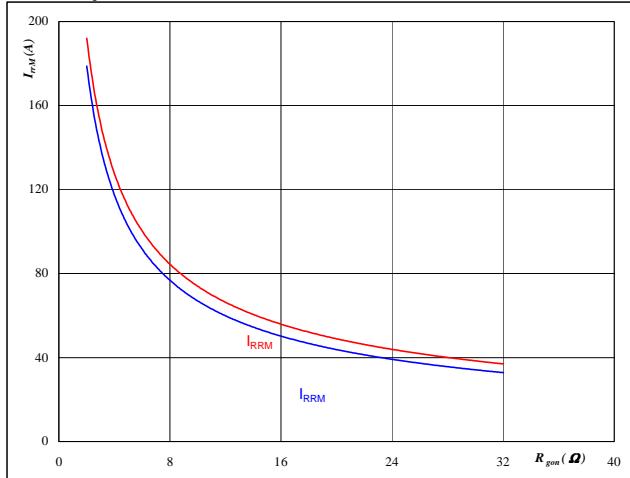
$$\begin{aligned} T_j &= \textcolor{blue}{25/150} \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

**Figure 16**

Output inverter FWD

**Typical reverse recovery current as a function of IGBT turn on gate resistor**

$$I_{RRM} = f(R_{gon})$$


**At**

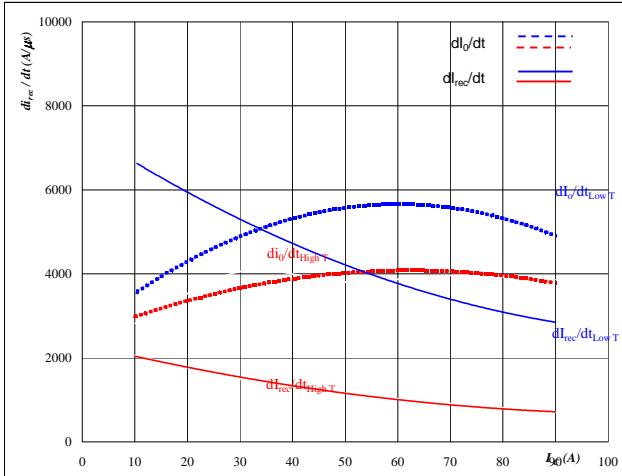
$$\begin{aligned} T_j &= \textcolor{blue}{25/150} \quad ^\circ\text{C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 50 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

## Output Inverter

**Figure 17**

Output inverter FWD

**Typical rate of fall of forward and reverse recovery current as a function of collector current**  
 $dI_0/dt, dI_{rec}/dt = f(I_C)$


**At**

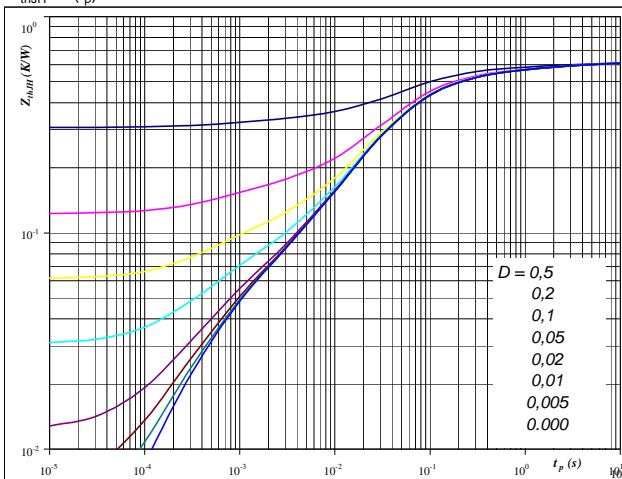
$T_j = 25/150 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 8 \Omega$

**Figure 19**

Output inverter IGBT

**IGBT transient thermal impedance as a function of pulse width**

$Z_{thJH} = f(t_p)$


**At**

$D = t_p / T$   
 $R_{thJH} = 0,61 \text{ K/W}$        $R_{thJH} = 0,60 \text{ K/W}$

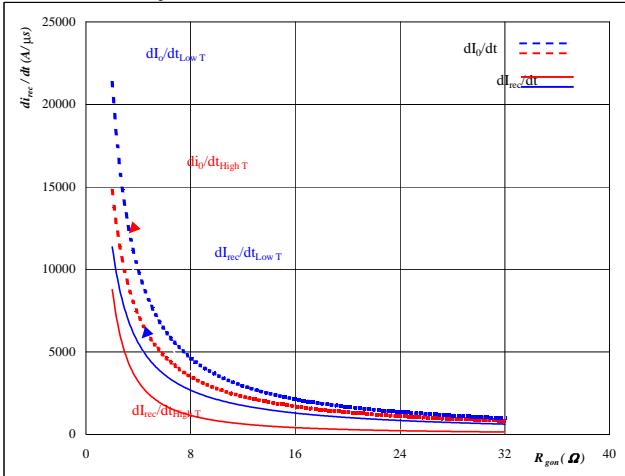
**IGBT thermal model values**

Psx7p	Phase change interface		
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,04	4,0E+00	0,04	3,9E+00
0,05	7,8E-01	0,05	7,6E-01
0,13	1,5E-01	0,12	1,5E-01
0,26	4,5E-02	0,25	4,4E-02
0,08	1,3E-02	0,08	1,2E-02
0,03	1,4E-03	0,03	1,3E-03
0,02	3,8E-04	0,00	0,0E+00

**Figure 18**

Output inverter FWD

**Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor**  
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$


**At**

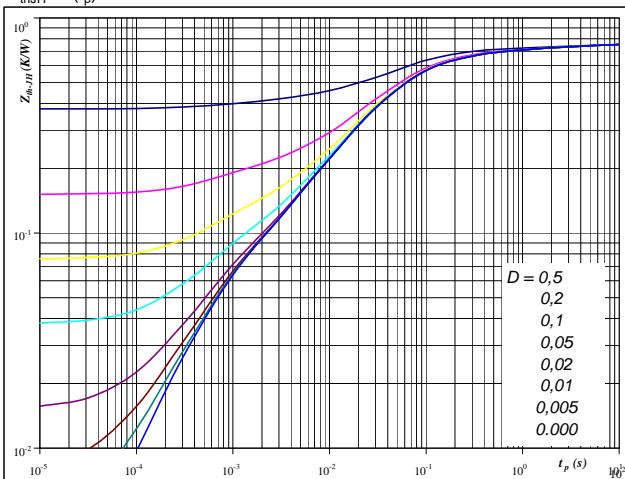
$T_j = 25/150 \text{ } ^\circ\text{C}$   
 $V_R = 600 \text{ V}$   
 $I_F = 50 \text{ A}$   
 $V_{GE} = \pm 15 \text{ V}$

**Figure 20**

Output inverter FWD

**FWD transient thermal impedance as a function of pulse width**

$Z_{thJH} = f(t_p)$


**At**

$D = t_p / T$   
 $R_{thJH} = 0,75 \text{ K/W}$        $R_{thJH} = 0,73 \text{ K/W}$

**FWD thermal model values**

Psx7p	Phase change interface		
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,04	3,7E+00	0,04	3,6E+00
0,07	5,6E-01	0,06	5,5E-01
0,21	9,7E-02	0,21	9,4E-02
0,31	2,9E-02	0,30	2,8E-02
0,07	6,0E-03	0,07	5,8E-03
0,05	6,6E-04	0,05	6,4E-04

## Output Inverter

**Figure 21**

Output inverter IGBT

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$


**At**

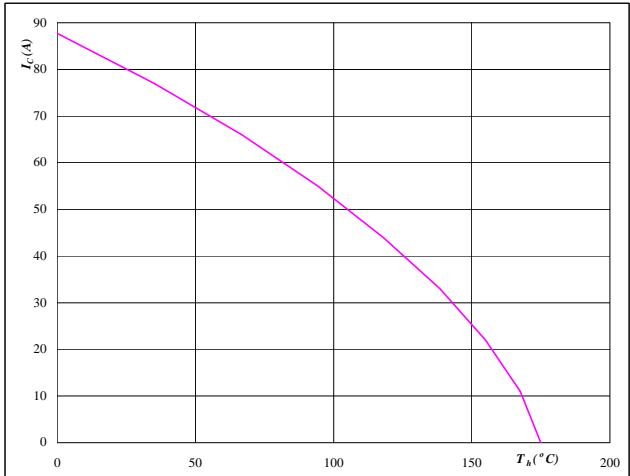
$$T_j = 175 \quad ^\circ\text{C}$$

**Figure 22**

Output inverter IGBT

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


**At**

$$T_j = 175 \quad ^\circ\text{C}$$

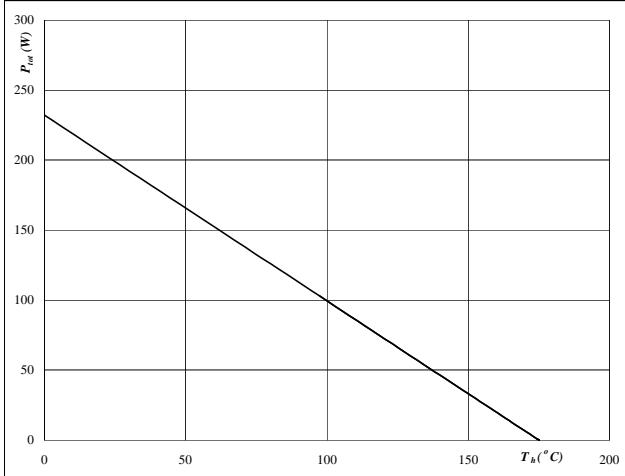
$$V_{GE} = 15 \quad \text{V}$$

**Figure 23**

Output inverter FWD

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$


**At**

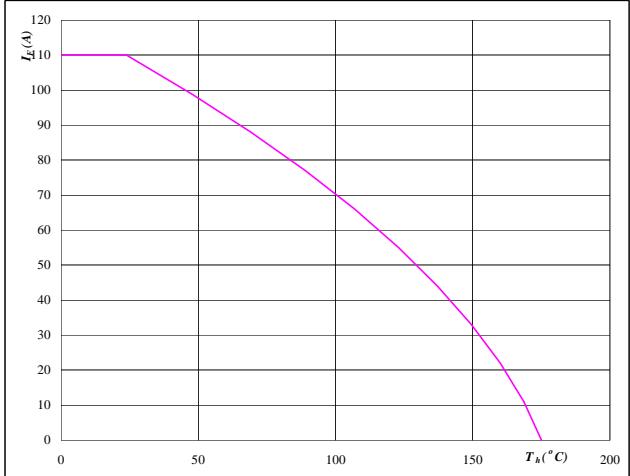
$$T_j = 175 \quad ^\circ\text{C}$$

**Figure 24**

Output inverter FWD

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$

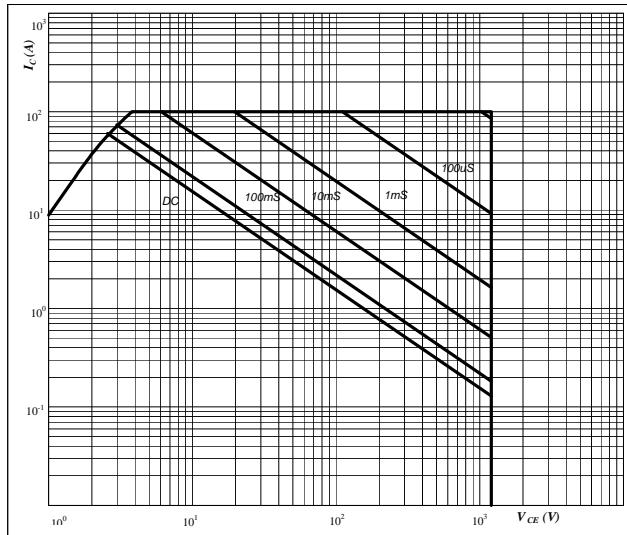

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

## Output Inverter

**Figure 25**  
Safe operating area as a function  
of collector-emitter voltage

$I_C = f(V_{CE})$



At

D = single pulse

$T_h = 80 \text{ } ^\circ\text{C}$

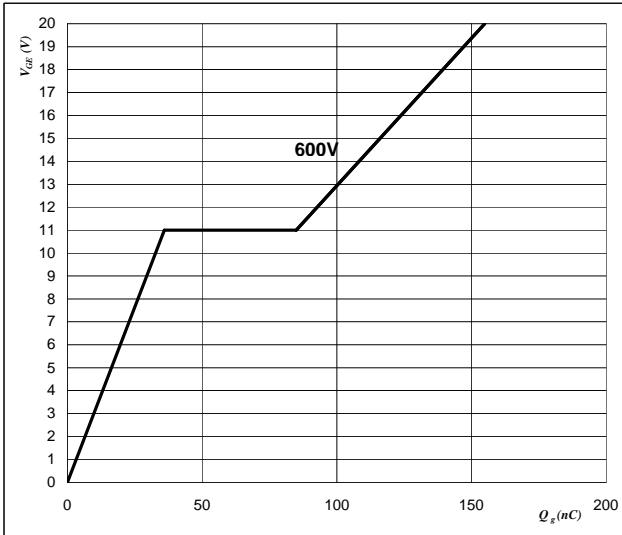
$V_{GE} = \pm 15 \text{ V}$

$T_j = T_{jmax} \text{ } ^\circ\text{C}$

Output inverter IGBT

**Figure 26**  
Gate voltage vs Gate charge

$V_{GE} = f(Q_{GE})$



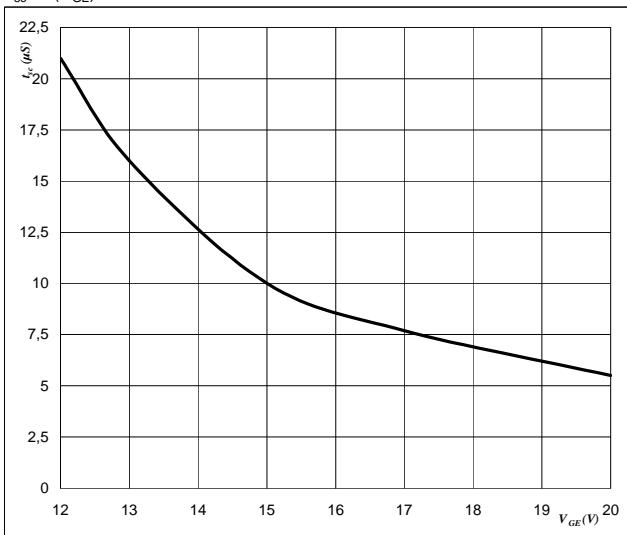
At

$I_C = 50 \text{ A}$

$T_j = 25 \text{ } ^\circ\text{C}$

**Figure 27**  
Short circuit withstand time as a function of  
gate-emitter voltage

$t_{sc} = f(V_{GE})$



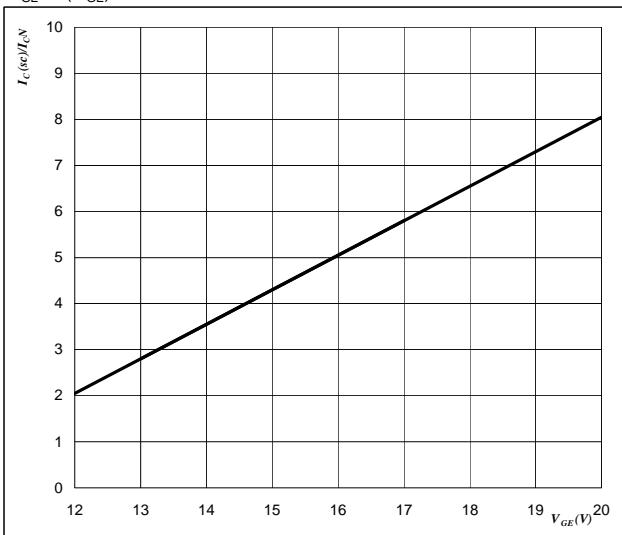
At

$V_{CE} = 1200 \text{ V}$

$T_j \leq 175 \text{ } ^\circ\text{C}$

**Figure 28**  
Typical short circuit collector current as a function of  
gate-emitter voltage

$I_{sc} = f(V_{GE})$



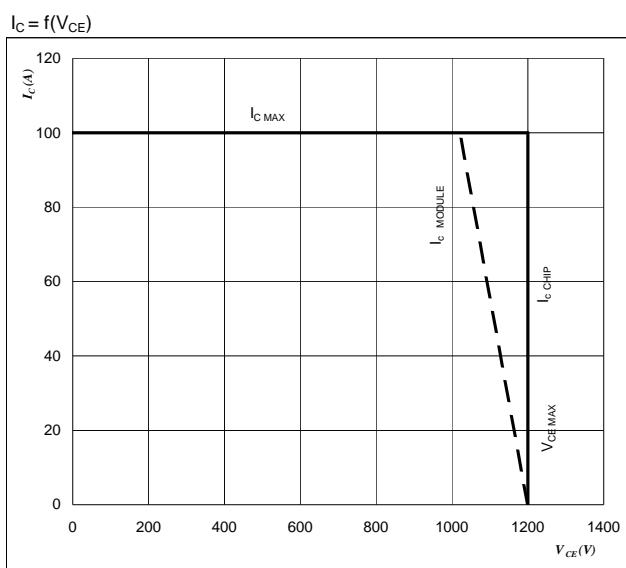
At

$V_{CE} \leq 800 \text{ V}$

$T_j = 150 \text{ } ^\circ\text{C}$

**Figure 29**  
**Reverse bias safe operating area**

IGBT

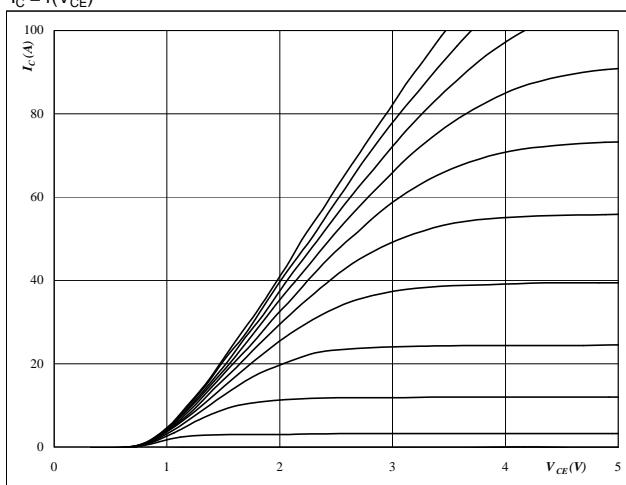


**At**

$T_j = 150^\circ\text{C}$   
 $R_{on} = 8 \Omega$   
 $R_{off} = 8 \Omega$

## Brake

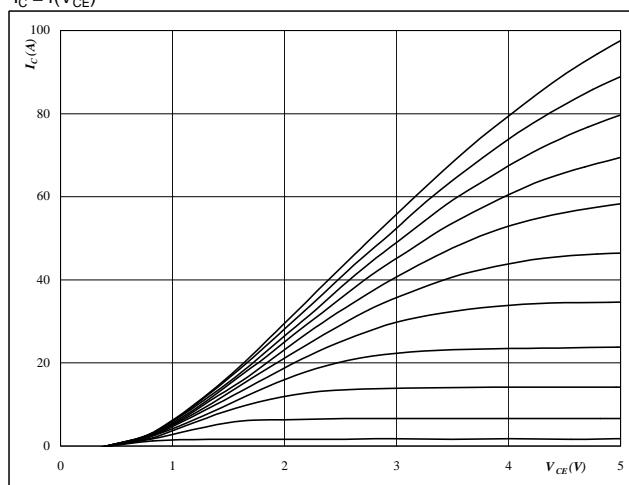
**Figure 1**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



**At**  
 $t_p = 250 \mu s$   
 $T_j = 25^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

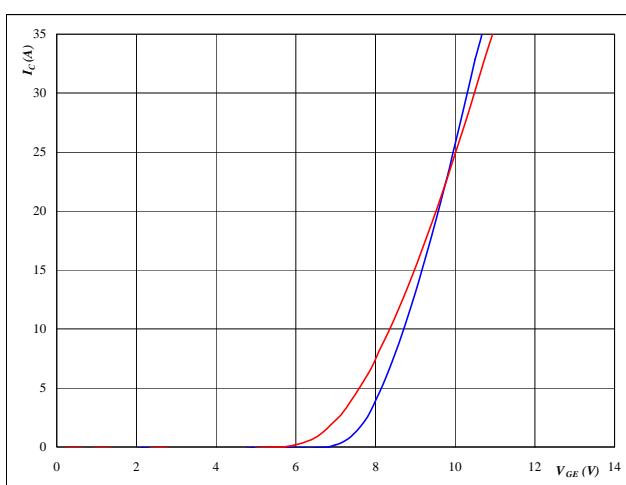
Brake IGBT

**Figure 2**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



**At**  
 $t_p = 250 \mu s$   
 $T_j = 150^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

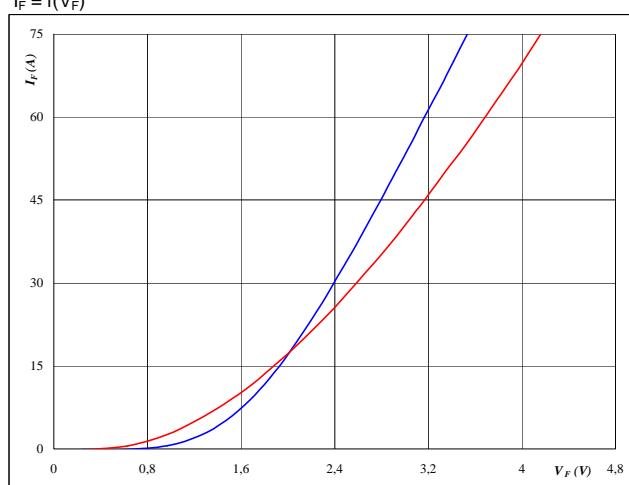
**Figure 3**  
**Typical transfer characteristics**  
 $I_C = f(V_{GE})$



**At**  
 $T_j = 25/150^\circ C$   
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

Brake IGBT

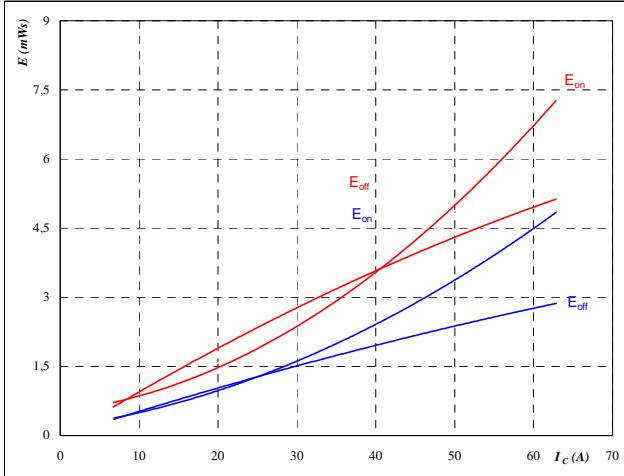
**Figure 4**  
**Typical diode forward current as a function of forward voltage**  
 $I_F = f(V_F)$



**At**  
 $t_p = 250 \mu s$   
 $T_j = 25^\circ C$

## Brake

**Figure 5**  
**Typical switching energy losses  
as a function of collector current**  
 $E = f(I_C)$

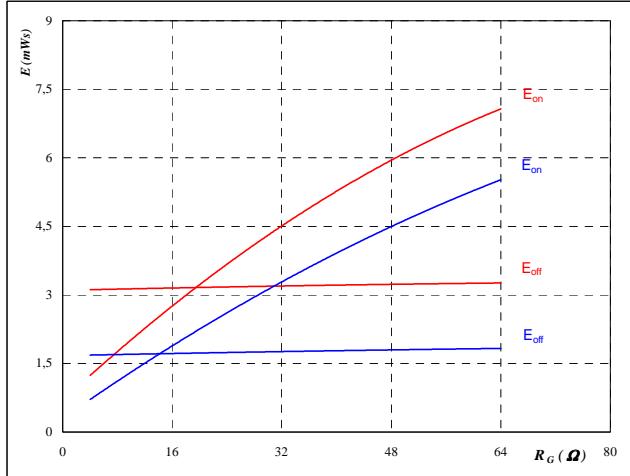


With an inductive load at

T<sub>j</sub> = **25/150** °C  
 V<sub>CE</sub> = 600 V  
 V<sub>GE</sub> = ±15 V  
 R<sub>gon</sub> = 16 Ω  
 R<sub>goff</sub> = 16 Ω

Brake IGBT

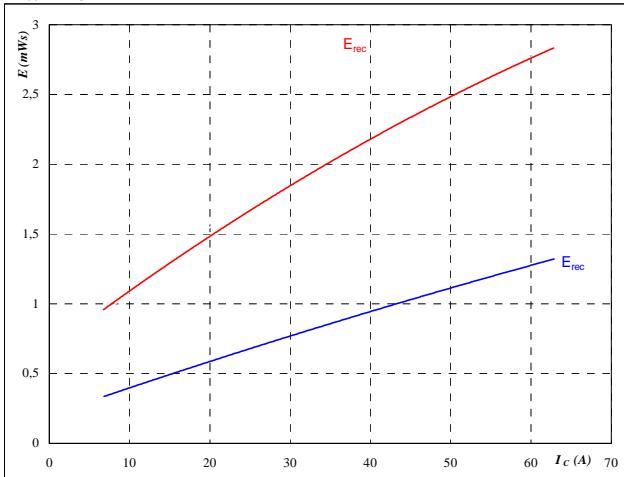
**Figure 6**  
**Typical switching energy losses  
as a function of gate resistor**  
 $E = f(R_G)$



With an inductive load at

T<sub>j</sub> = **25/150** °C  
 V<sub>CE</sub> = 600 V  
 V<sub>GE</sub> = ±15 V  
 I<sub>C</sub> = 35 A

**Figure 7**  
**Typical reverse recovery energy loss  
as a function of collector current**  
 $E_{rec} = f(I_C)$

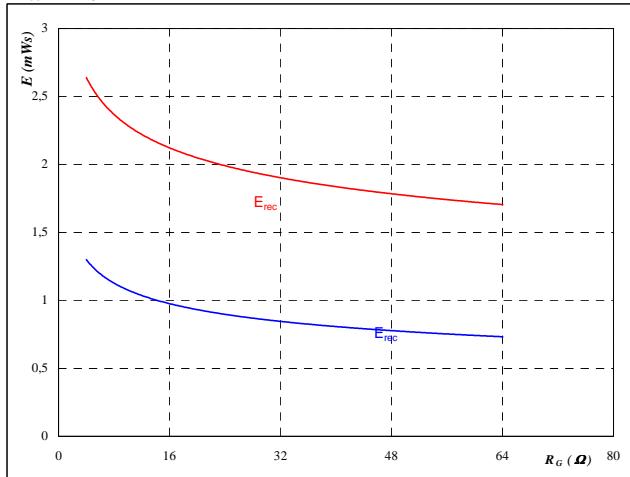


With an inductive load at

T<sub>j</sub> = **25/150** °C  
 V<sub>CE</sub> = 600 V  
 V<sub>GE</sub> = ±15 V  
 R<sub>gon</sub> = 16 Ω

Brake FWD

**Figure 8**  
**Typical reverse recovery energy loss  
as a function of gate resistor**  
 $E_{rec} = f(R_G)$



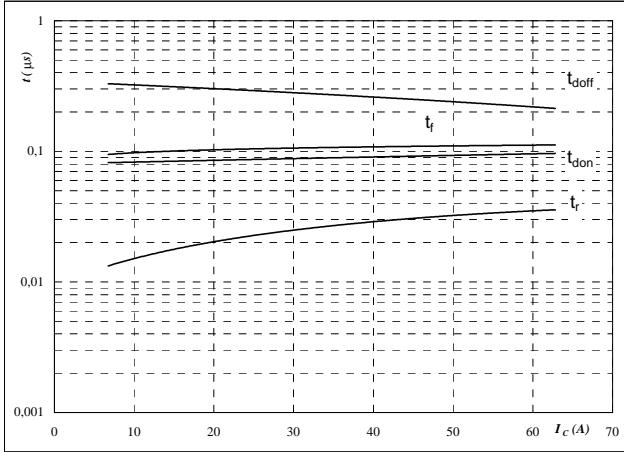
With an inductive load at

T<sub>j</sub> = **25/150** °C  
 V<sub>CE</sub> = 600 V  
 V<sub>GE</sub> = ±15 V  
 I<sub>C</sub> = 35 A

## Brake

**Figure 9**

Typical switching times as a function of collector current  
 $t = f(I_C)$

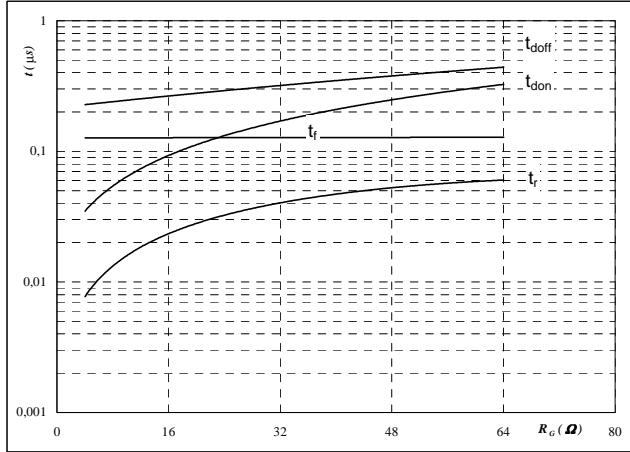


With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	16	Ω
$R_{goff} =$	16	Ω

**Brake IGBT**
**Figure 10**

Typical switching times as a function of gate resistor  
 $t = f(R_G)$



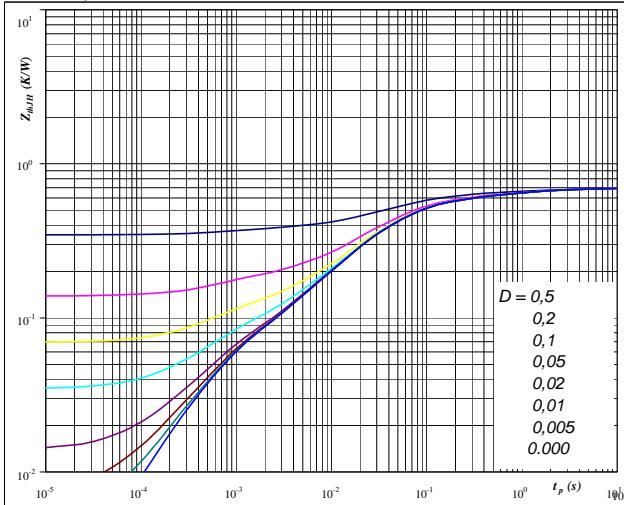
With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_C =$	35	A

**Figure 11**

IGBT transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$

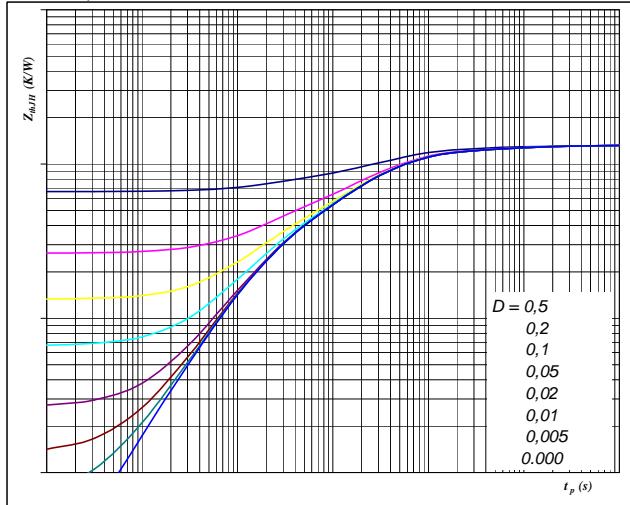


At	D =	$t_p / T$	Phase change interface
Psx7p	0,692	K/W	
$R_{thJH} =$	0,67	K/W	

**Brake IGBT**
**Figure 12**

FWD transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



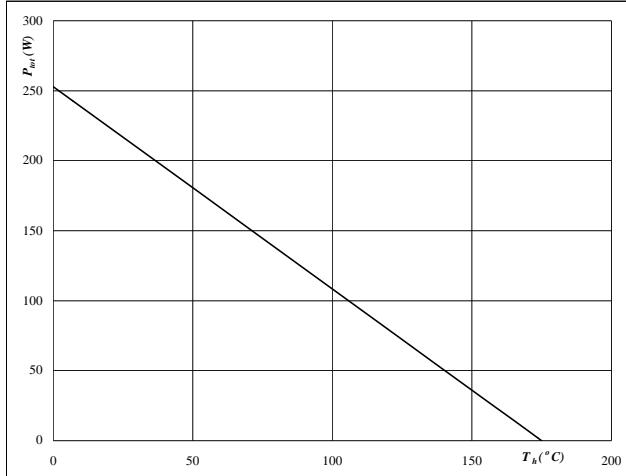
At	D =	$t_p / T$	Phase change interface
Psx7p	1,32	K/W	
$R_{thJH} =$	1,28	K/W	

## Brake

**Figure 13**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

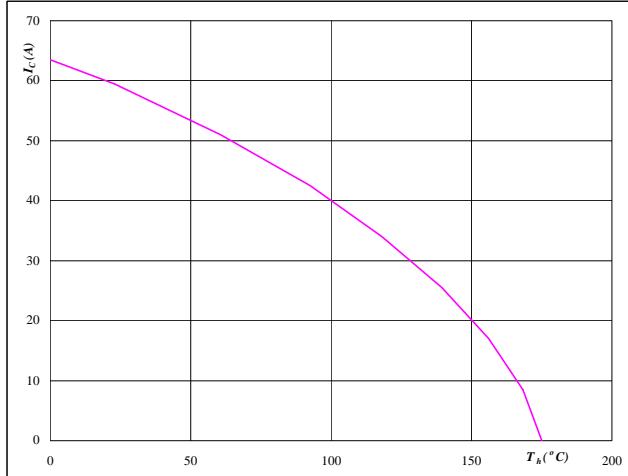

**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

**Brake IGBT**
**Figure 14**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


**At**

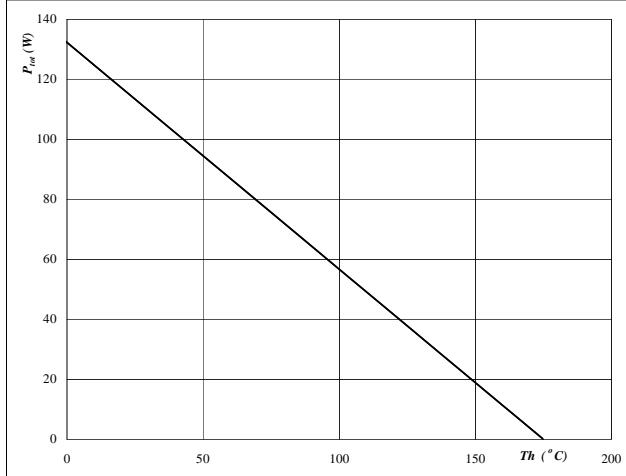
$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

**Figure 15**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

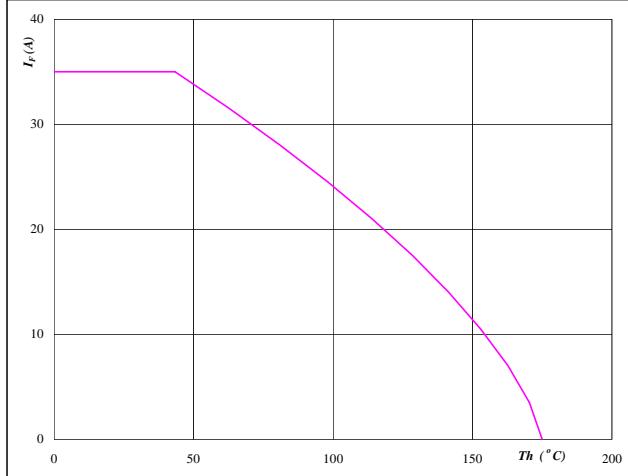

**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

**Brake FWD**
**Figure 16**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

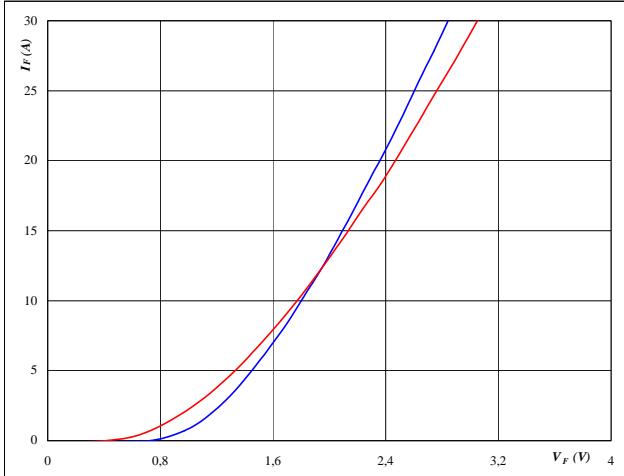
## Brake Inverse Diode

**Figure 1**

Brake inverse diode

**Typical diode forward current as a function of forward voltage**

$$I_F = f(V_F)$$


**At**

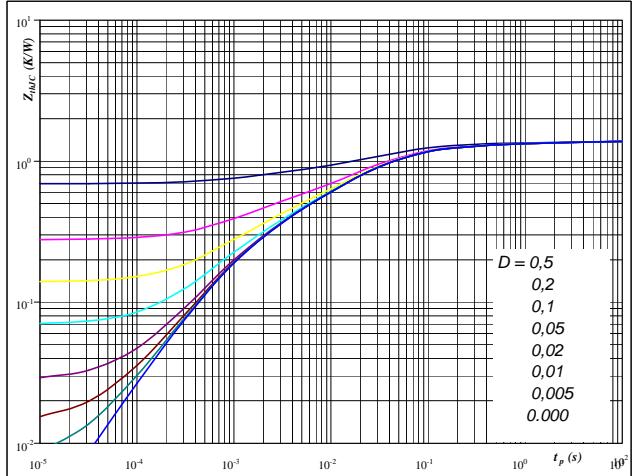
$$\begin{aligned} T_j &= \textcolor{blue}{25/150} \quad ^\circ\text{C} \\ t_p &= 250 \quad \mu\text{s} \end{aligned}$$

**Figure 2**

Brake inverse diode

**Diode transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$


**At**

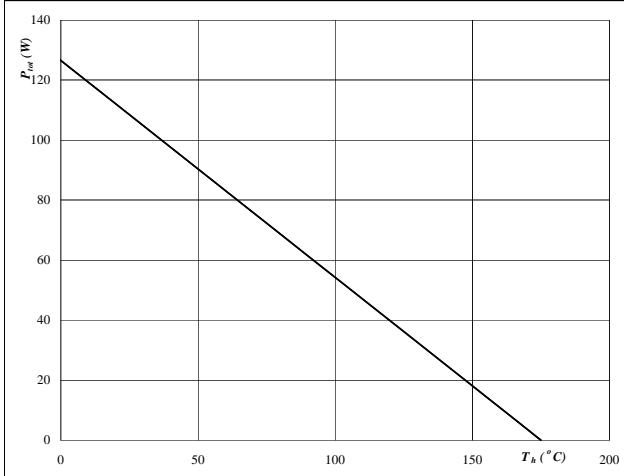
$$\begin{aligned} Psx7p & \quad D = tp / T \\ R_{thJH} &= 1.38 \quad \text{K/W} & R_{thJH} &= 1.34 \quad \text{K/W} \end{aligned}$$

**Figure 3**

Brake inverse diode

**Power dissipation as a function of heatsink temperature**

$$P_{tot} = f(T_h)$$


**At**

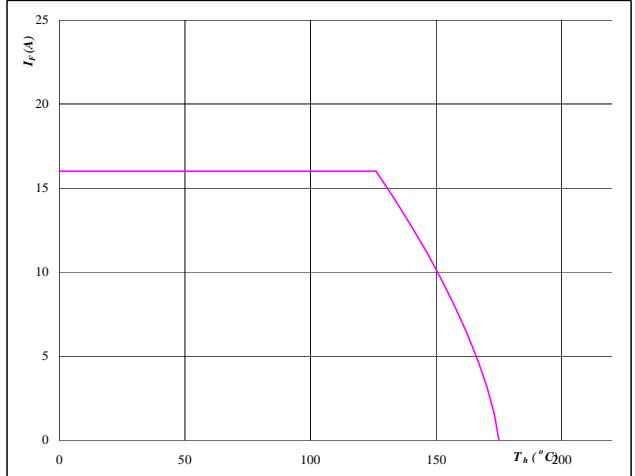
$$T_j = 150 \quad ^\circ\text{C}$$

**Figure 4**

Brake inverse diode

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

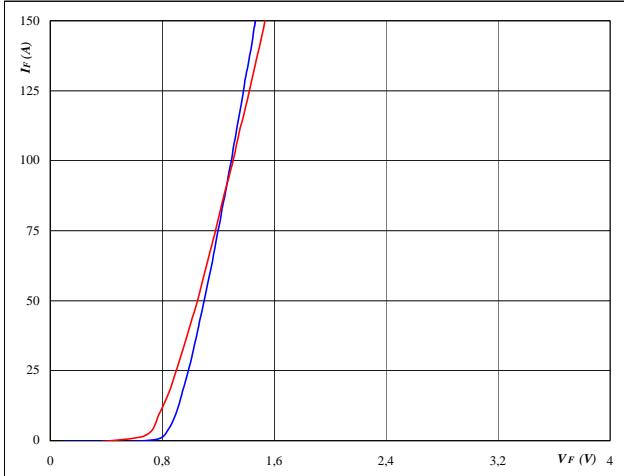
$$T_j = 150 \quad ^\circ\text{C}$$

## Input Rectifier Bridge

**Figure 1**

**Typical diode forward current as a function of forward voltage**

$$I_F = f(V_F)$$

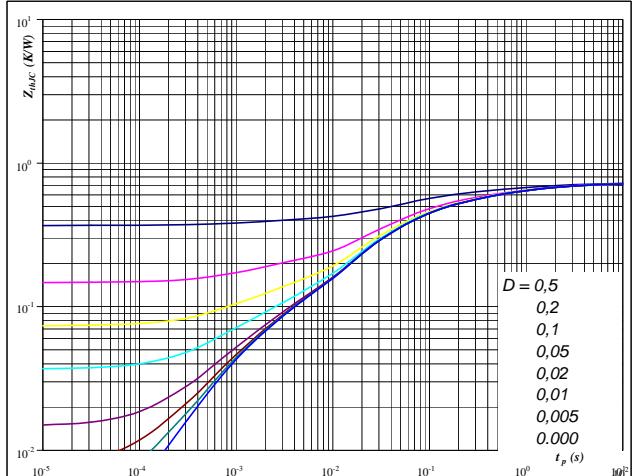

**At**

$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ t_p &= 250 \quad \mu\text{s} \end{aligned}$$

**Rectifier diode**
**Figure 2**

**Diode transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$

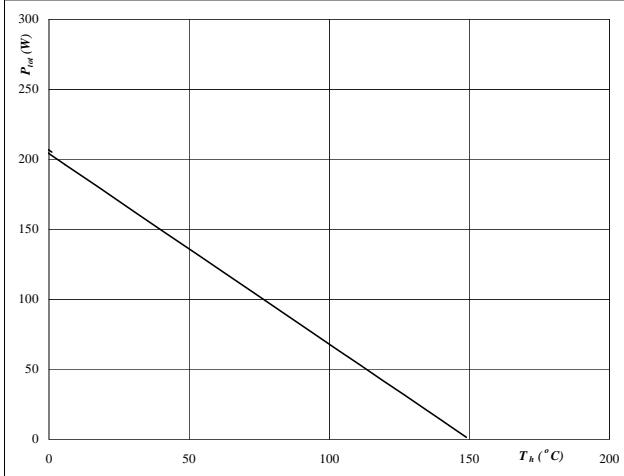

**At**

$$\begin{aligned} D &= t_p / T \\ R_{thJH} &= 0.74 \quad \text{K/W} \end{aligned}$$

**Figure 3**

**Power dissipation as a function of heatsink temperature**

$$P_{tot} = f(T_h)$$

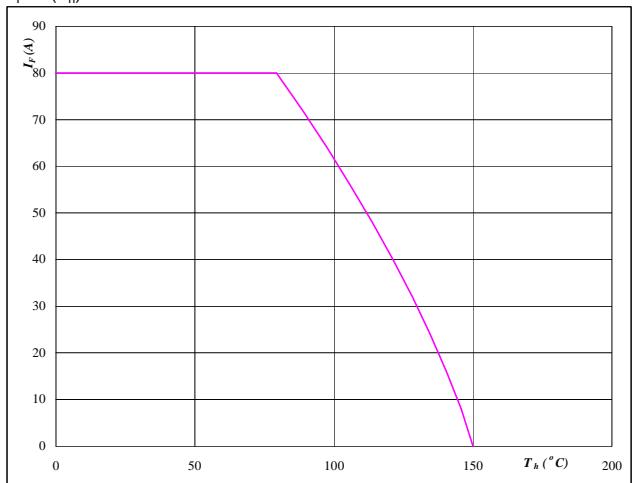

**At**

$$T_j = 150 \quad ^\circ\text{C}$$

**Rectifier diode**
**Figure 4**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

$$T_j = 150 \quad ^\circ\text{C}$$

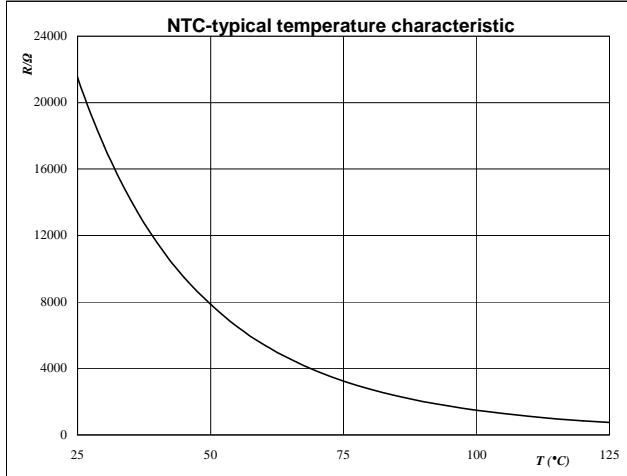
## Thermistor

**Figure 1**

Thermistor

**Typical NTC characteristic  
as a function of temperature**

$$R_T = f(T)$$


**Figure 2**

Thermistor

**Typical NTC resistance values**

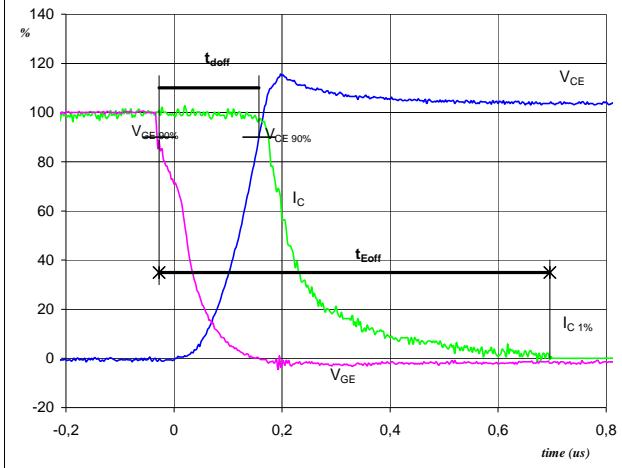
$$R(T) = R_{25} \cdot e^{\left( B_{25/100} \left( \frac{1}{T} - \frac{1}{T_{25}} \right) \right)} \quad [\Omega]$$

## Switching Definitions Output Inverter

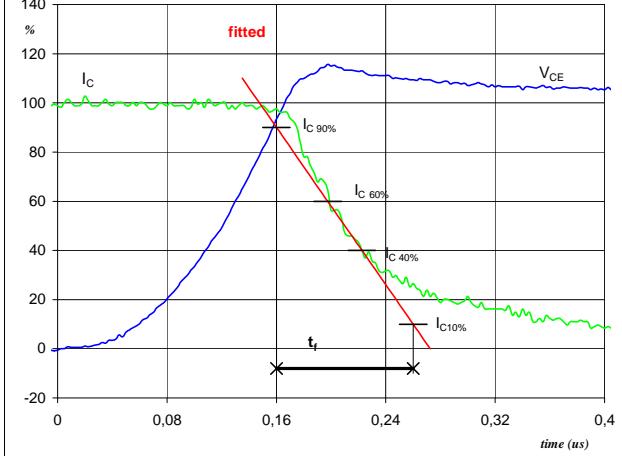
**General conditions**

$T_j$	=	150 °C
$R_{gon}$	=	8 Ω
$R_{goff}$	=	8 Ω

**Figure 1**
**Output inverter IGBT**
**Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$** 

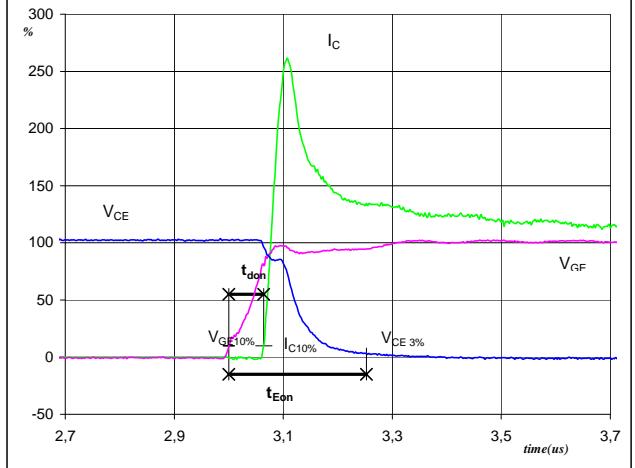
( $t_{Eoff}$  = integrating time for  $E_{off}$ )


$V_{GE}(0\%) = -15 \text{ V}$   
 $V_{GE}(100\%) = 15 \text{ V}$   
 $V_C(100\%) = 600 \text{ V}$   
 $I_C(100\%) = 50 \text{ A}$   
 $t_{doff} = 0,19 \mu\text{s}$   
 $t_{Eoff} = 0,72 \mu\text{s}$

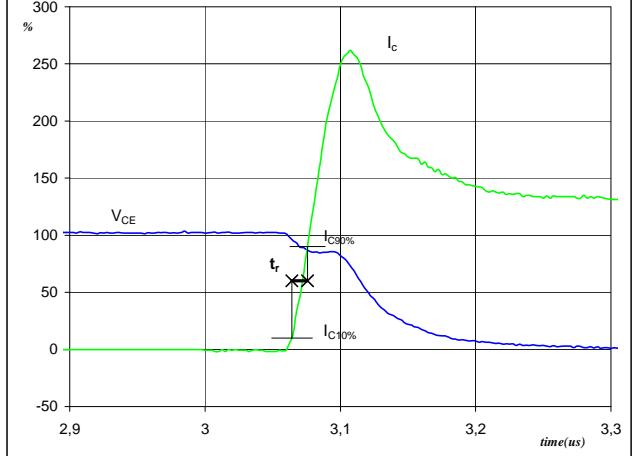
**Figure 3**
**Output inverter IGBT**
**Turn-off Switching Waveforms & definition of  $t_f$** 


$V_C(100\%) = 600 \text{ V}$   
 $I_C(100\%) = 50 \text{ A}$   
 $t_f = 0,10 \mu\text{s}$

**Figure 2**
**Output inverter IGBT**
**Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$** 

( $t_{Eon}$  = integrating time for  $E_{on}$ )


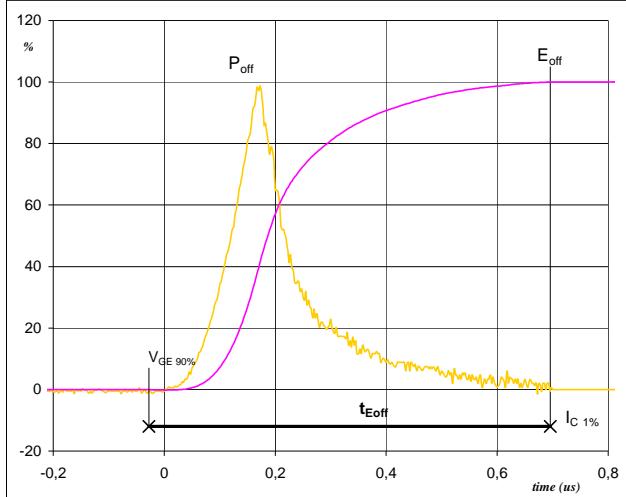
$V_{GE}(0\%) = -15 \text{ V}$   
 $V_{GE}(100\%) = 15 \text{ V}$   
 $V_C(100\%) = 600 \text{ V}$   
 $I_C(100\%) = 50 \text{ A}$   
 $t_{don} = 0,07 \mu\text{s}$   
 $t_{Eon} = 0,25 \mu\text{s}$

**Figure 4**
**Output inverter IGBT**
**Turn-on Switching Waveforms & definition of  $t_r$** 


$V_C(100\%) = 600 \text{ V}$   
 $I_C(100\%) = 50 \text{ A}$   
 $t_r = 0,01 \mu\text{s}$

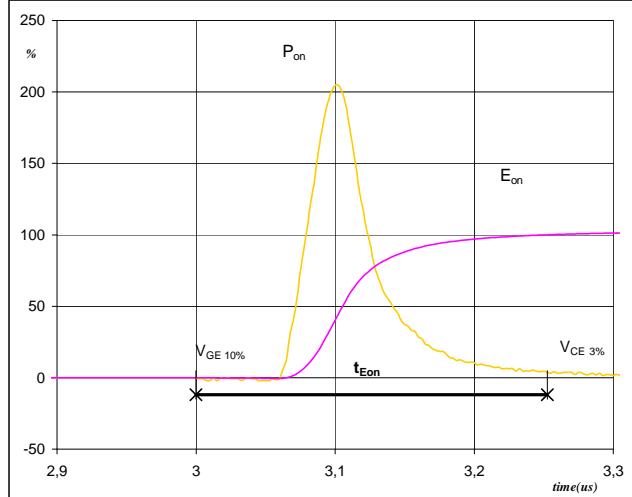
## Switching Definitions Output Inverter

**Figure 5** Output inverter IGBT  
Turn-off Switching Waveforms & definition of  $t_{Eoff}$



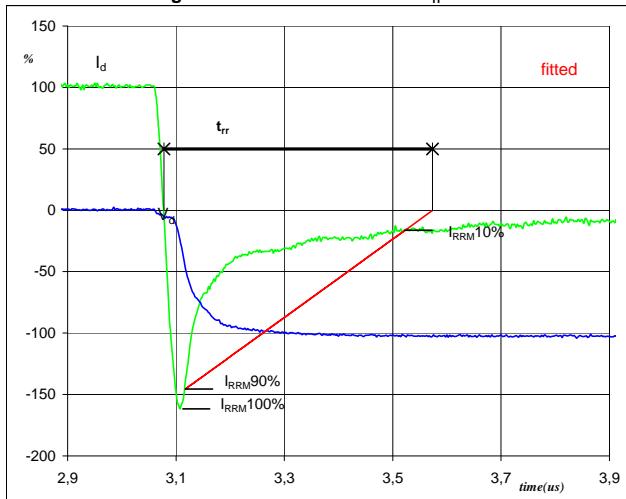
$P_{off} (100\%) = 30,08 \text{ kW}$   
 $E_{off} (100\%) = 4,32 \text{ mJ}$   
 $t_{Eoff} = 0,72 \mu\text{s}$

**Figure 6** Output inverter IGBT  
Turn-on Switching Waveforms & definition of  $t_{Eon}$



$P_{on} (100\%) = 30,08 \text{ kW}$   
 $E_{on} (100\%) = 3,31 \text{ mJ}$   
 $t_{Eon} = 0,25 \mu\text{s}$

**Figure 7** Output inverter FWD  
Turn-off Switching Waveforms & definition of  $t_{rr}$



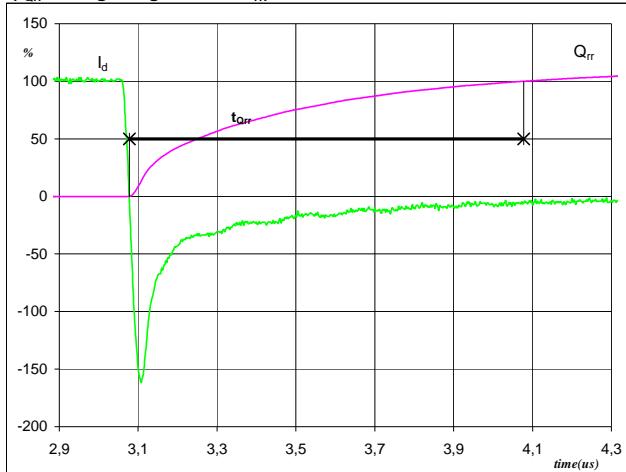
$V_d (100\%) = 600 \text{ V}$   
 $I_d (100\%) = 50 \text{ A}$   
 $I_{RRM} (100\%) = -81 \text{ A}$   
 $t_{rr} = 0,47 \mu\text{s}$

## Switching Definitions Output Inverter

**Figure 8**

Output inverter FWD

Turn-on Switching Waveforms & definition of  $t_{Qrr}$   
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

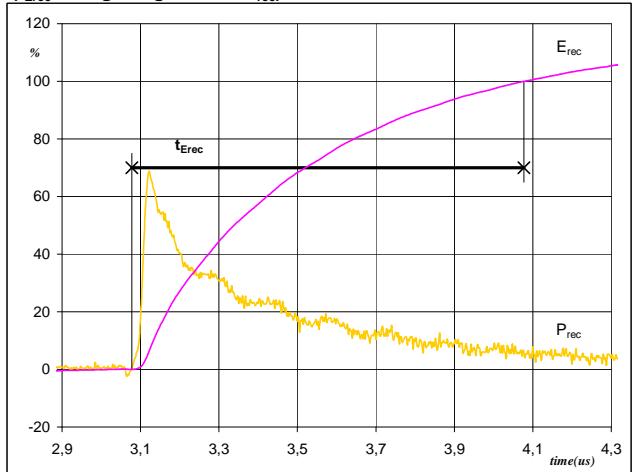


$I_d(100\%) = 50 \text{ A}$   
 $Q_{rr}(100\%) = 12,53 \mu\text{C}$   
 $t_{Qrr} = 1,00 \mu\text{s}$

**Figure 9**

Output inverter FWD

Turn-on Switching Waveforms & definition of  $t_{Erec}$   
 $(t_{Erec} = \text{integrating time for } E_{rec})$



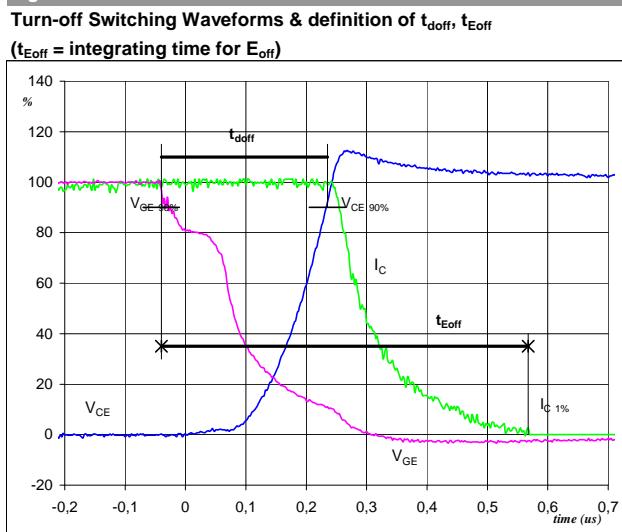
$P_{rec}(100\%) = 30,08 \text{ kW}$   
 $E_{rec}(100\%) = 6,03 \text{ mJ}$   
 $t_{Erec} = 1,00 \mu\text{s}$

## Switching Definitions Brake

General conditions

$T_j$	= 150 °C
$R_{gon}$	= 16 Ω
$R_{goff}$	= 16 Ω

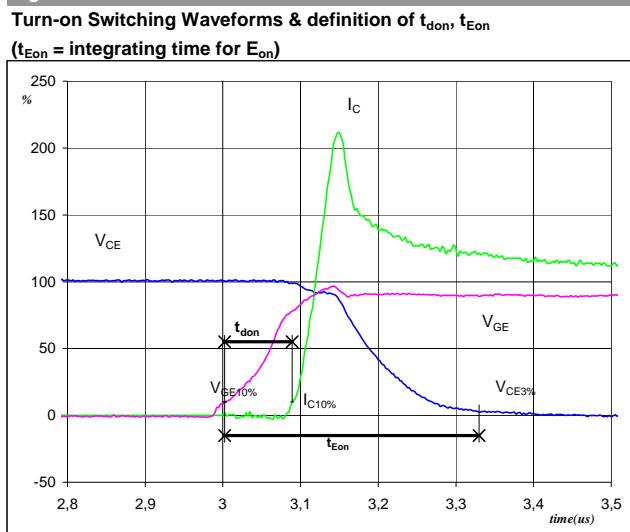
Figure 1



Parameter values:

$V_{GE\text{ (0\%)}}$ =	-15	V
$V_{GE\text{ (100\%)}}$ =	15	V
$V_C\text{ (100\%)}$ =	600	V
$I_c\text{ (100\%)}$ =	35	A
$t_{doff}$ =	0,27	μs
$t_{Eoff}$ =	0,61	μs

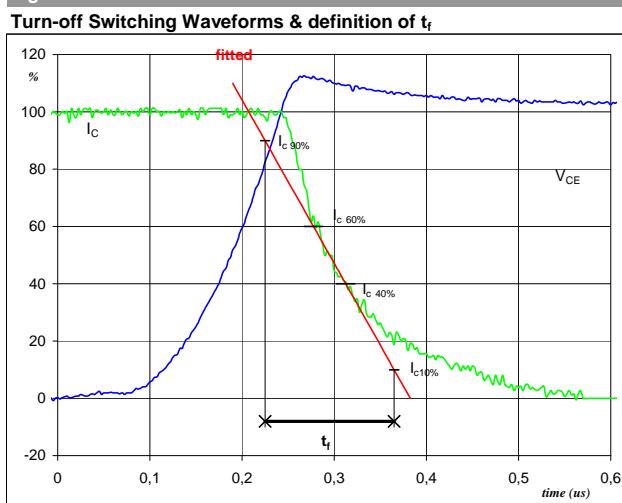
Figure 2



Parameter values:

$V_{GE\text{ (0\%)}}$ =	-15	V
$V_{GE\text{ (100\%)}}$ =	15	V
$V_C\text{ (100\%)}$ =	600	V
$I_c\text{ (100\%)}$ =	35	A
$t_{don}$ =	0,09	μs
$t_{Eon}$ =	0,33	μs

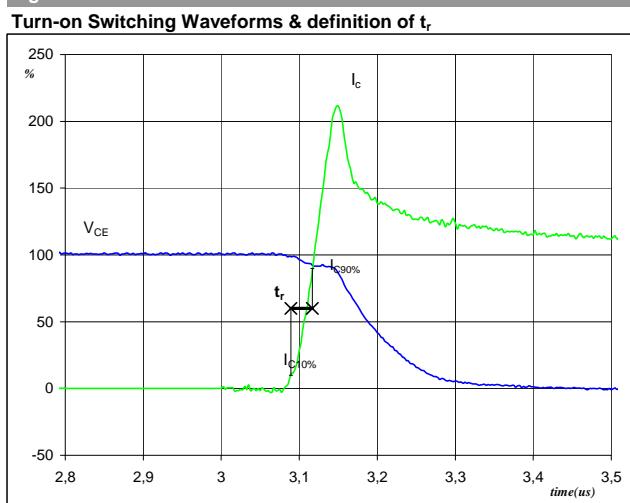
Figure 3



Parameter values:

$V_C\text{ (100\%)}$ =	600	V
$I_c\text{ (100\%)}$ =	35	A
$t_f$ =	0,13	μs

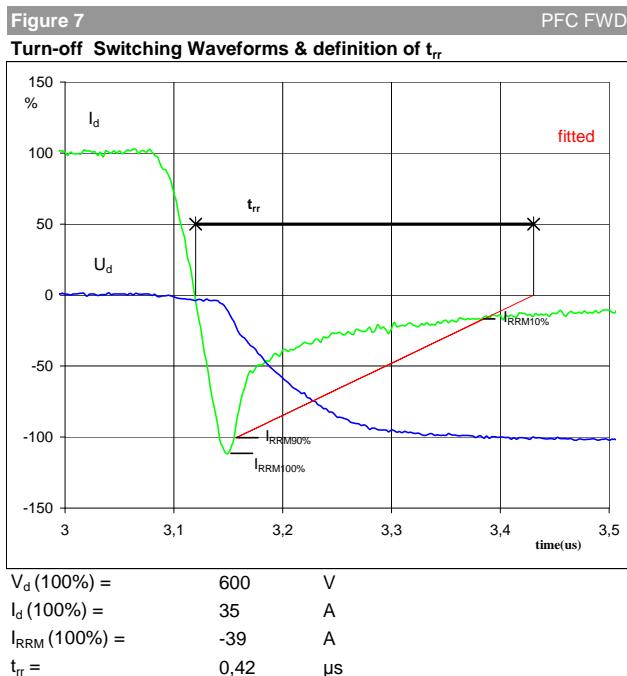
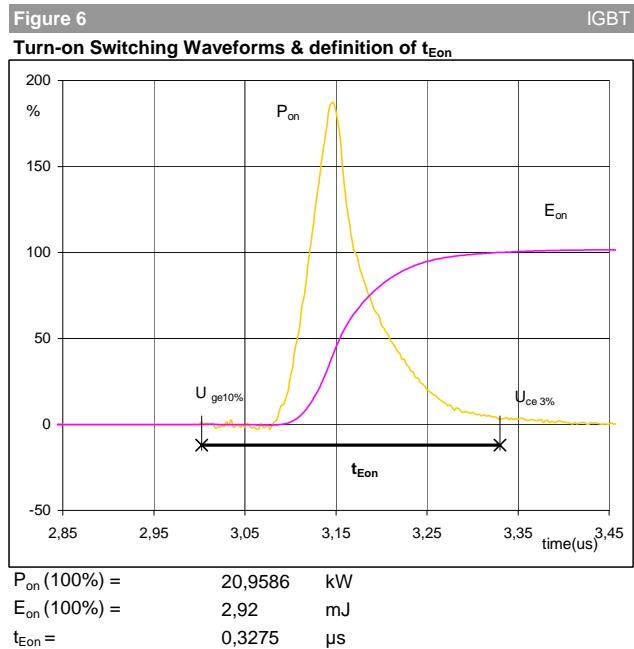
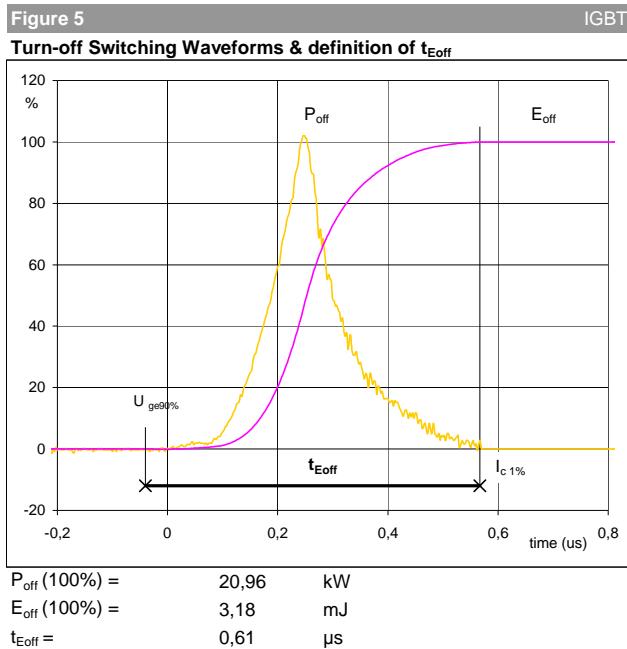
Figure 4



Parameter values:

$V_C\text{ (100\%)}$ =	600	V
$I_c\text{ (100\%)}$ =	35	A
$t_r$ =	0,03	μs

## Switching Definitions Brake

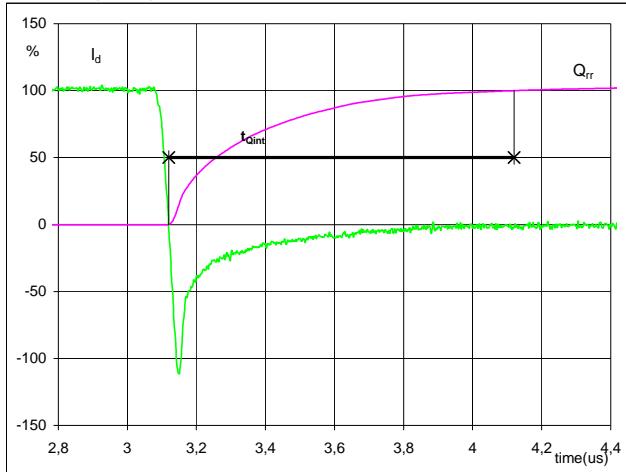


## Switching Definitions Brake

**Figure 8**

PFC FWD

**Turn-on Switching Waveforms & definition of  $t_{Qrr}$**   
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

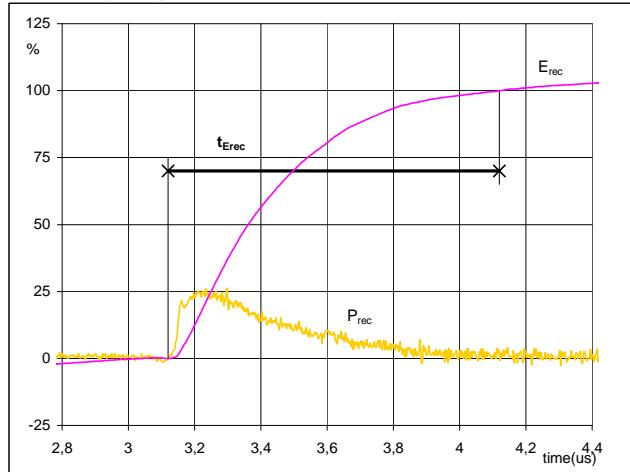


$I_d(100\%) = 35 \text{ A}$   
 $Q_{rr}(100\%) = 4,84 \mu\text{C}$   
 $t_{Qint} = 1,00 \mu\text{s}$

**Figure 9**

PFC FWD

**Turn-on Switching Waveforms & definition of  $t_{Erec}$**   
 $(t_{Erec} = \text{integrating time for } E_{rec})$



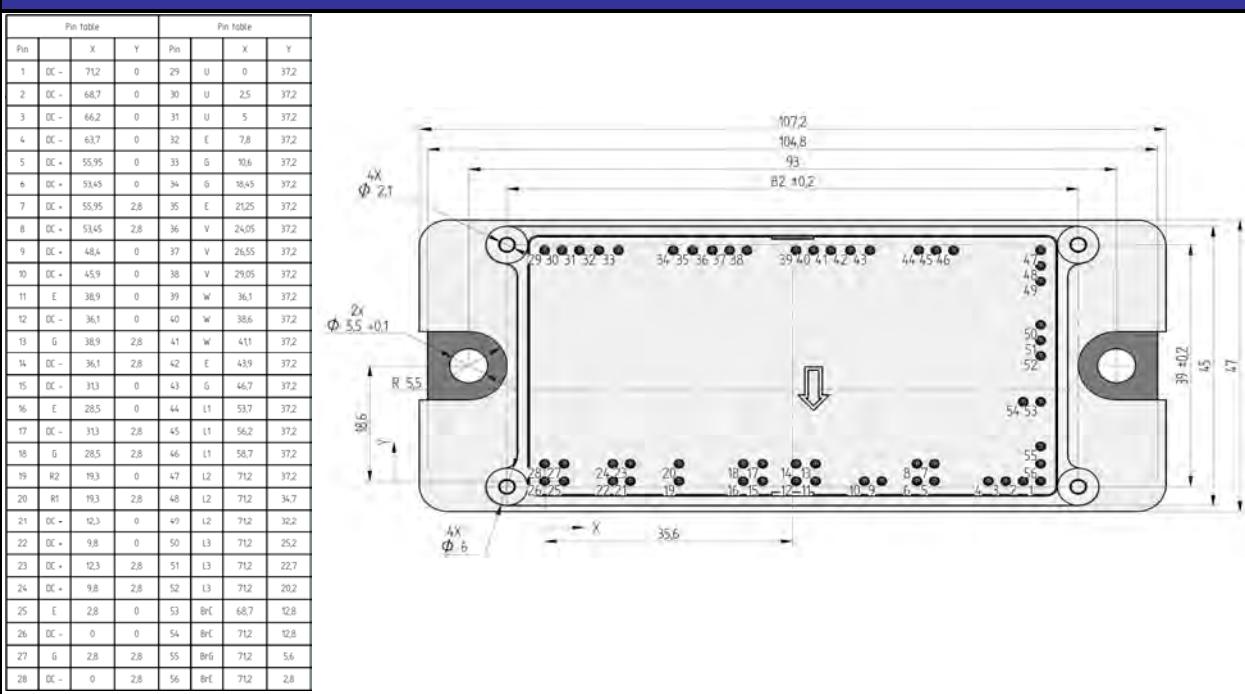
$P_{rec}(100\%) = 20,96 \text{ kW}$   
 $E_{rec}(100\%) = 1,98 \text{ mJ}$   
 $t_{Erec} = 1,00 \mu\text{s}$

## **Ordering Code and Marking - Outline - Pinout**

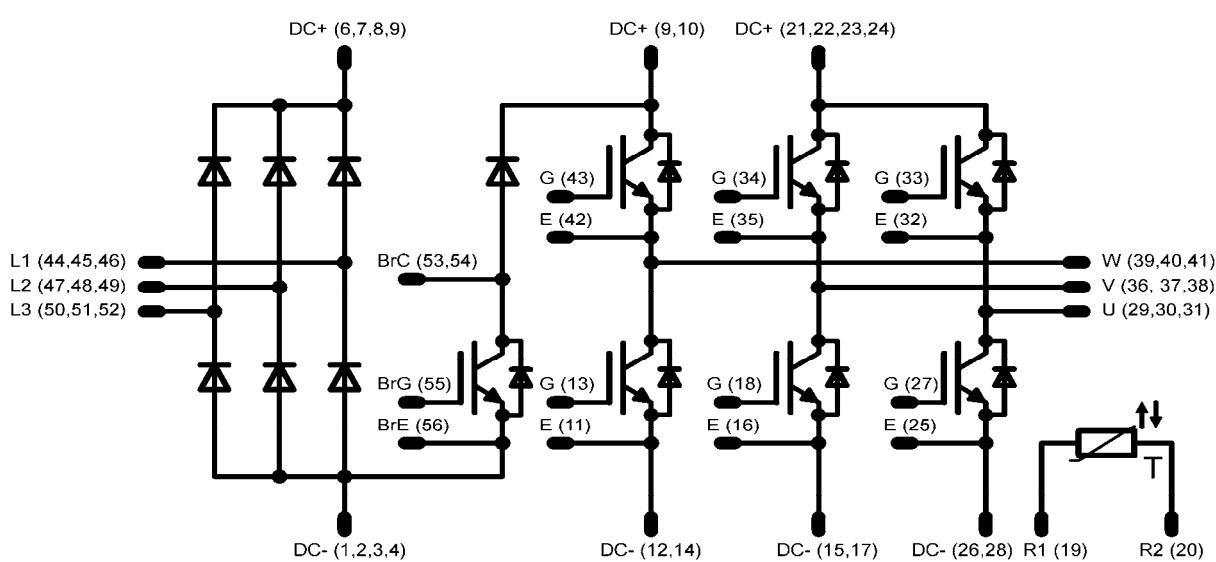
## Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste 17mm housing	V23990-P768-A50	P768-A50	P768-A50

## Outline



## Pinout



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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.