

650V(D-S) N-Channel Enhancement Mode Power MOS FET

General Features

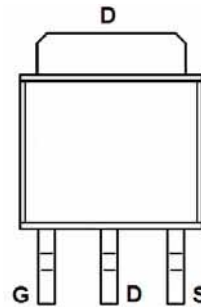
- $V_{DS} = 650V, I_D = 2A$
 $R_{DS(ON)} < 5.0 \Omega @ V_{GS} = 10V$
- High density cell design for ultra low $R_{DS(on)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability



Lead Free

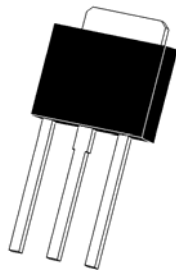
Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

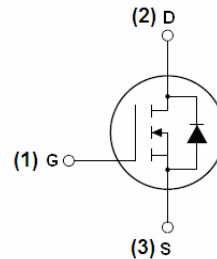


Marking and pin assignment

PIN Configuration



TO-251 top view



Schematic diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MSN6502Z	MSN6502Z	TO-251	-	-	-

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	± 30	V
Drain Current-Continuous	I_D	2	A
Drain Current-Continuous($T_C = 100^\circ C$)	$I_D(100^\circ C)$	1.3	A
Pulsed Drain Current	I_{DM}	6	A
Maximum Power Dissipation	P_D	44	W
Derating factor		0.28	W/ $^\circ C$
Single pulse avalanche energy ^(Note 5)	E_{AS}	240	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{\theta JC}$	5.5	°C/W
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Electrical Characteristics ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	650	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =650V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±30V,V _{DS} =0V	-	-	±100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	2	-	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =1.0A	-	-	5.0	Ω
Forward Transconductance	g _{FS}	V _{DS} =40V,I _D =1.0A	-	2.5	-	S
Dynamic Characteristics ^(Note4)						
Input Capacitance	C _{ISS}	V _{DS} =25V,V _{GS} =0V, F=1.0MHz	-	280	-	PF
Output Capacitance	C _{OSS}		-	37	-	PF
Reverse Transfer Capacitance	C _{RSS}		-	6.0	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =325V,I _D =1.8A,R _L =25Ω V _{GS} =10V,R _G =2.5Ω	-	9	-	nS
Turn-on Rise Time	t _r		-	25	-	nS
Turn-Off Delay Time	t _{d(off)}		-	24	-	nS
Turn-Off Fall Time	t _f		-	28	-	nS
Total Gate Charge	Q _g	V _{DS} =520V,I _D =1.8A V _{GS} =10V	-	6.0		nC
Gate-Source Charge	Q _{gs}		-	1.3		nC
Gate-Drain Charge	Q _{gd}		-	2.6		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V,I _S =2.0A	-		1.4	V
Diode Forward Current ^(Note 2)	I _S		-	-	2.0	A
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = 2.0A di/dt = 100A/μs ^(Note3)	-	190		nS
Reverse Recovery Charge	Q _{rr}		-	0.8		μC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. E_{AS} condition: $j=25^{\circ}\text{C}, V_{DD}=50V, V_G=10V, L=0.5mH, R_g=25\Omega$

Typical Electrical and Thermal Characteristics (Curves)

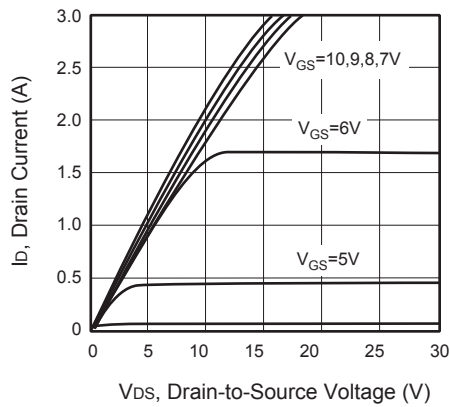


Figure 1. Output Characteristics

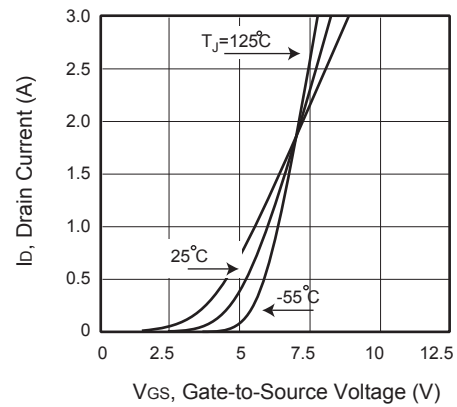


Figure 2. Transfer Characteristics

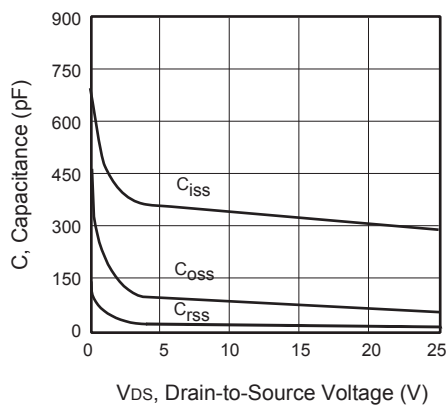


Figure 3. Capacitance

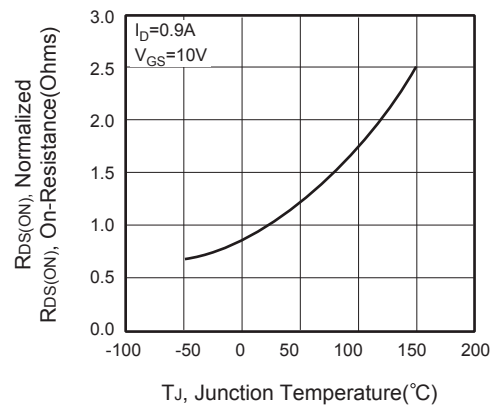


Figure 4. On-Resistance Variation with Temperature

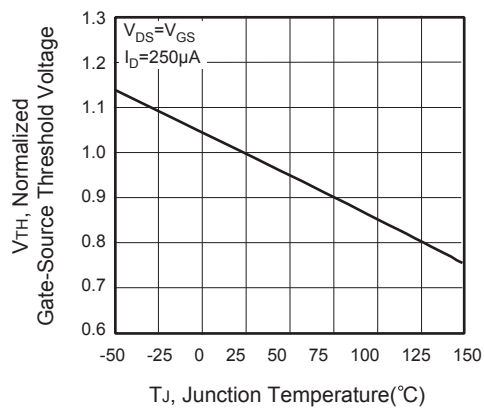


Figure 5. Gate Threshold Variation with Temperature

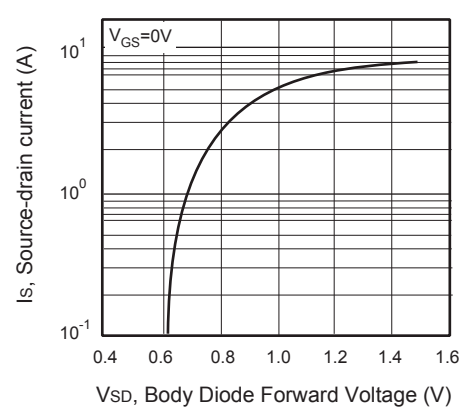


Figure 6. Body Diode Forward Voltage Variation with Source Current

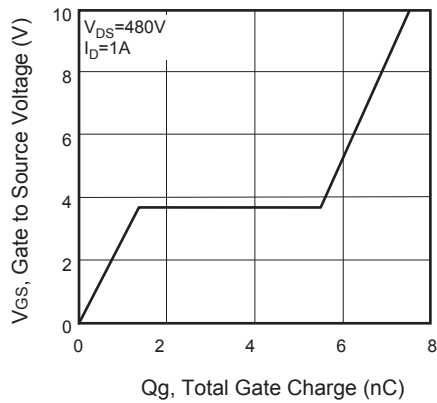


Figure 7. Gate Charge

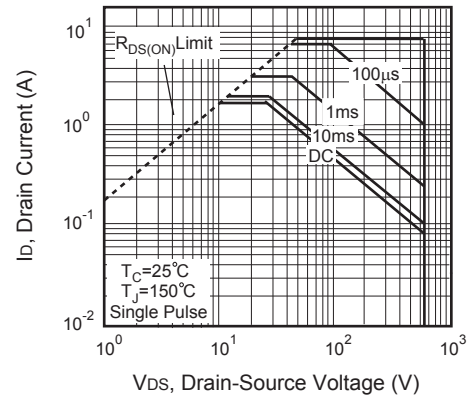


Figure 8. Maximum Safe Operating Area

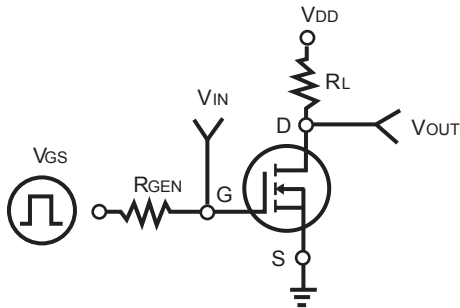


Figure 9. Switching Test Circuit

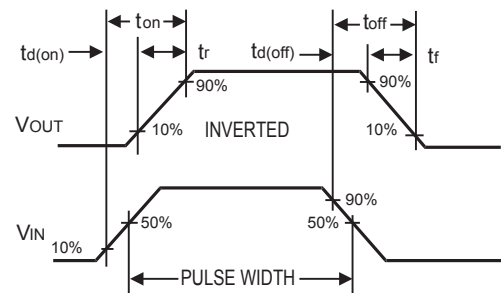


Figure 10. Switching Waveforms

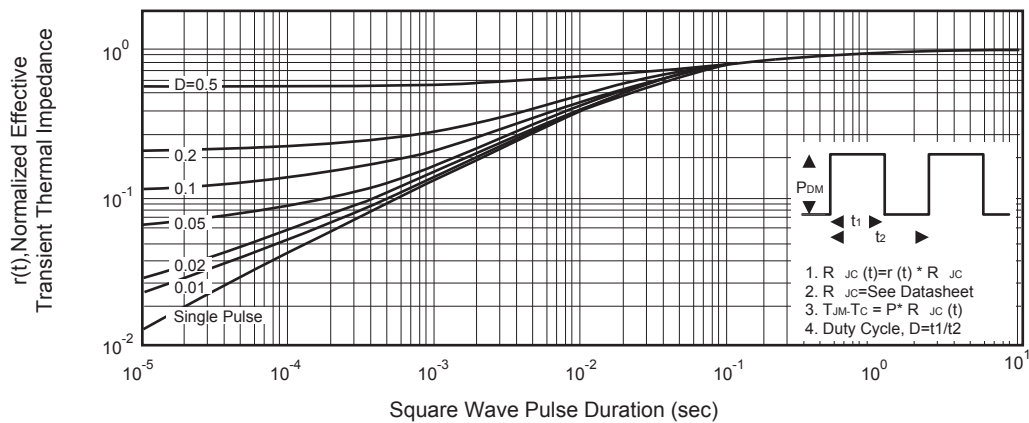
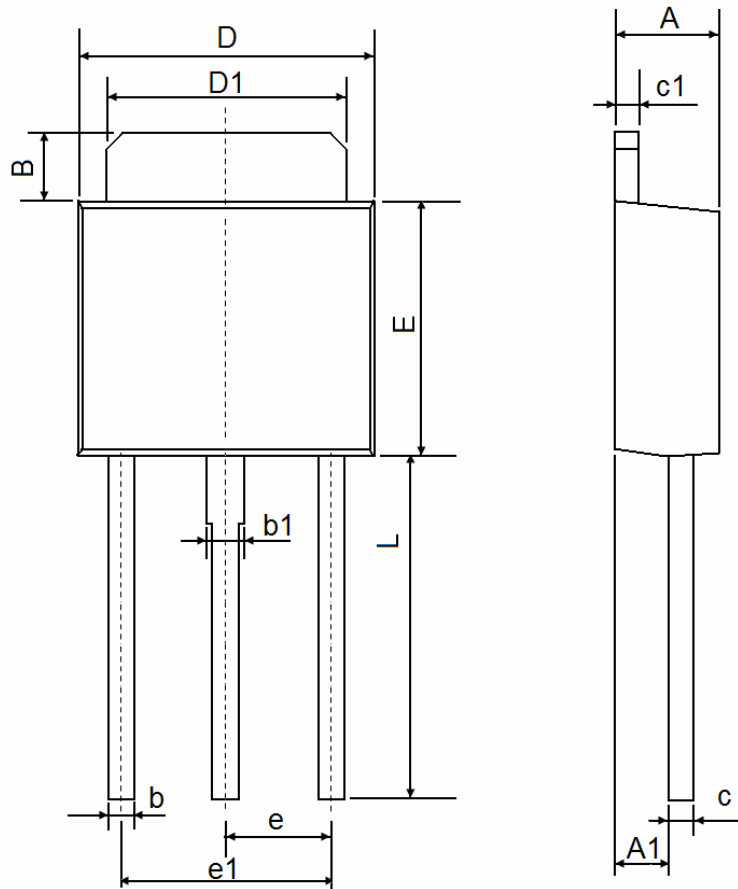


Figure 11. Normalized Thermal Transient Impedance Curve

TO-251 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	1.050	1.350	0.042	0.054
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP		0.091 TYP	
e1	4.500	4.700	0.177	0.185
L	7.500	7.900	0.295	0.311