

UNISONIC TECHNOLOGIES CO., LTD

4N60-E Power MOSFET

4A, 600V N-CHANNEL POWER MOSFET

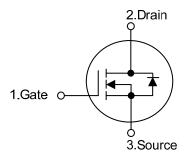
■ DESCRIPTION

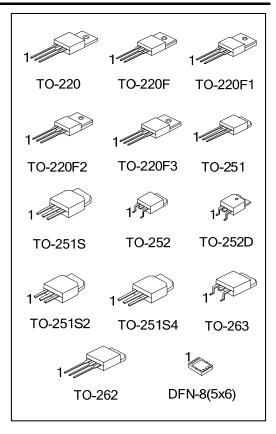
The UTC **4N60-E** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(ON)}$ < 2.5 Ω @ V_{GS} = 10 V, I_D = 2.2A
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, high RuggednessA

■ SYMBOL

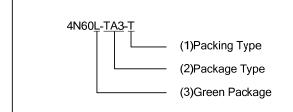




ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment							Daakina	
Lead Free	Halogen Free	Package	1	2	3	4	5	6	7	8	Packing
4N60L-TA3-T	4N60G-TA3-T	TO-220	G	D	S	-	ı	-	-	-	Tube
4N60L-TF3-T	4N60G-TF3-T	TO-220F	G	D	S	-	ı	-	-	-	Tube
4N60L-TF1-T	4N60G-TF1-T	TO-220F1	G	D	S	-	ı	-	-	-	Tube
4N60L-TF2-T	4N60G-TF2-T	TO-220F2	G	D	S	-	ı	-	-	-	Tube
4N60L-TF3T-T	4N60G-TF3T-T	TO-220F3	G	D	S	-	ı	-	-	-	Tube
4N60L-TM3-T	4N60G-TM3-T	TO-251	G	D	S	-	ı	-	-	-	Tube
4N60L-TMS-T	4N60G-TMS-T	TO-251S	G	D	S	-	ı	-	-	-	Tube
4N60L-TMS2-T	4N60G-TMS2-T	TO-251S2	G	D	S	-	-	-	-	-	Tube
4N60L-TMS4-T	4N60G-TMS4-T	TO-251S4	G	D	S	-	ı	-	-	-	Tube
4N60L-TN3-R	4N60G-TN3-R	TO-252	G	D	S	-	ı	-	-	-	Tape Reel
4N60L-TND-R	4N60G-TND-R	TO-252D	G	D	S	-	ı	-	-	-	Tape Reel
4N60L-T2Q-T	4N60G-T2Q-T	TO-262	G	D	S	-	ı	-	-	-	Tube
4N60L-TQ2-R	4N60G-TQ2-R	TO-263	G	D	S	-	ı	-	-	-	Tape Reel
4N60L-TQ2-T	4N60G-TQ2-T	TO-263	G	D	S	-	•	-	-	-	Tube
-	4N60G-K08-5060-R	DFN-8(5×6)	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



- (1) T: Tube, R: Tape Reel
- (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, TF3T: TO-220F3, TM3: TO-251, TMS: TO-251S, TN3: TO-252, TND: TO-252D TMS2: TO-251S2, TMS4: TO-251S4, T2Q: TO-262, TQ2: TO-263, K08-3030: DFN-8(5×6)
- (3) L: Lead Free, G: Halogen Free and Lead Free

■ MARKING

PACKAGE		MARKING				
TO-220 TO-220F TO-220F1 TO-220F2 TO-220F3 TO-251 TO-251S	TO-251S2 TO-251S4 TO-252 TO-252D TO-262 TO-263	UTC 4N60 □ C: Lead Free G: Halogen Free Data Code 1				
DFN-8(5×6)		UTC 4N60 □□□□□□□□ Lot Code Date Code				

■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±30	V
Avalanche Current (Note 2)		I_{AR}	4.4	Α
Drain Current	Continuous	I_{D}	4.0	Α
	Pulsed (Note 2)	I_{DM}	16	Α
Avalancha Energy	Single Pulsed (Note 3)	E _{AS}	200	mJ
Avalanche Energy	Repetitive (Note 2)	E _{AR}	10.6	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220/TO-262/TO-263		106	
	TO-220F/TO-220F1 TO-220F3		36	
	TO-220F2	Б	38	١,,,
	TO-251/TO-252/TO-252D TO-251S/TO-251S2 TO-251S4	P_D	50	- W
	DFN-8(5×6)		30	
Junction Temperature		T_J	+150	°C
Operating Temperature		T_{OPR}	-55 ~ + 150	°C
Storage Temperature		T_{STG}	-55 ~ + 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 30mH, I_{AS} = 3.65A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 4.4A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220/TO-262/TO-263 TO-220F/TO-220F1 TO-220F2/TO-220F3		62.5		
	TO-251/TO-252/TO-252D TO-251S/TO-251S2 TO-251S4	$ heta_{JA}$	110	°C/W	
	DFN-8(5×6)		75		
	TO-220/TO-262/TO-263		1.18		
Junction to Case	TO-220F/TO-220F1 TO-220F3		3.47		
	TO-220F2	0	3.28	°C/W	
	TO-251/TO-252/TO-252D TO-251S/TO-251S2 TO-251S4	$ heta_{ extsf{JC}}$	2.5	C/VV	
	DFN-8(5×6)		4.17		

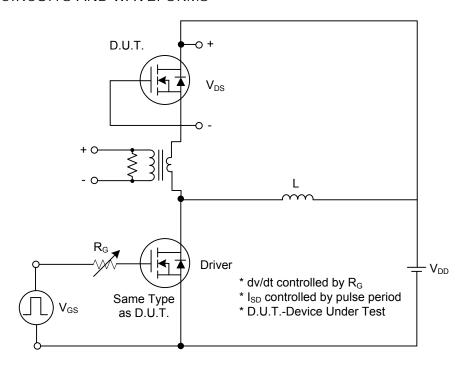
■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	600			V		
Drain-Source Leakage Current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V$			10	μΑ		
Cata Source Leakage Current Forward	CSS	$V_{GS} = 30V, V_{DS} = 0V$			100	nA		
Gate-Source Leakage Current Reverse		$V_{GS} = -30V, V_{DS} = 0V$			-100	nA		
Breakdown Voltage Temperature Coefficier	t $\triangle BV_{DSS}/\triangle T_{J}$	I _D =250μA,Referenced to 25°C		0.6		V/°C		
ON CHARACTERISTICS								
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V		
Static Drain-Source On-State Resistance	R _{DS(ON)}	$V_{GS} = 10 \text{ V}, I_D = 2.2 \text{A}$		2.3	2.5	Ω		
DYNAMIC CHARACTERISTICS								
Input Capacitance	C _{ISS}	V 05V V 0V		520	620	pF		
Output Capacitance	Coss	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz		55	75	pF		
Reverse Transfer Capacitance	C _{RSS}	1 = 11VIH2		11	15	pF		
SWITCHING CHARACTERISTICS								
Turn-On Delay Time	t _{D(ON)}			60	130	ns		
Turn-On Rise Time	t _R	$V_{DD} = 300V, I_D = 4.0A,$		60	100	ns		
Turn-Off Delay Time	t _{D(OFF)}	$R_G = 25\Omega \text{ (Note 1, 2)}$		220	260	ns		
Turn-Off Fall Time	t _F			70	100	ns		
Total Gate Charge	Q_{G}	V 400V/I 4.0A		65		nC		
Gate-Source Charge	Q_GS	V _{DS} = 480V,I _D = 4.0A, V _{GS} = 10V (Note 1, 2)		6		nC		
Gate-Drain Charge	Q_GD	V _{GS} = 10V (Note 1, 2)		8		nC		
SOURCE- DRAIN DIODE RATINGS AND	CHARACTERIS	TICS			_			
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS} = 0V, I_{S} = 4.4A$			1.4	V		
Maximum Continuous Drain-Source Diode					1.1	۸		
Forward Current	I _S				4.4	Α		
Maximum Pulsed Drain-Source Diode	la				17.6	Α		
Forward Current	I _{SM}				17.0	Α		
Reverse Recovery Time	t _{rr}	$V_{GS} = 0 \text{ V}, I_{S} = 4.4\text{A},$		250		ns		
Reverse Recovery Charge	Q_{RR}	dI _F /dt = 100 A/µs (Note 1)		1.5		μC		

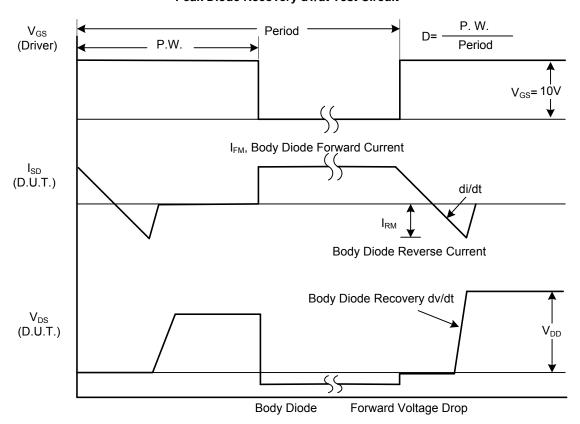
Notes: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%

^{2.} Essentially independent of operating temperature

TEST CIRCUITS AND WAVEFORMS

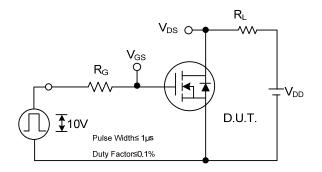


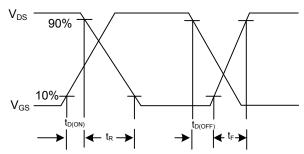
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

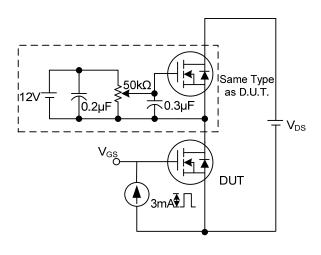
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

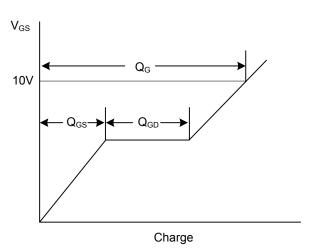




Switching Test Circuit

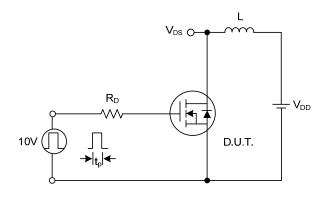
Switching Waveforms

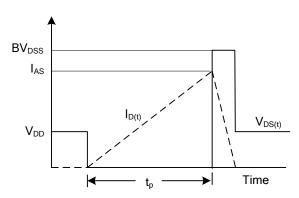




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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