TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7MA541FK

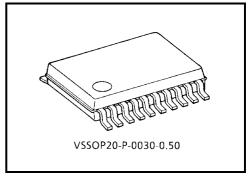
#### Low-Voltage Octal Bus Buffer with 3.6 V Tolerant Inputs and Outputs

The TC7MA541FK is a high performance CMOS octal bus buffer. Designed for use in  $1.8~\rm{V},\,2.5~\rm{V}$  or  $3.3~\rm{V}$  systems, it achieves high speed operation while maintaing the CMOS low power dissipation.

It is also designed with over voltage tolerant inputs and outputs up to  $3.6\ V.$ 

The device is a non-inverting 3-state buffer having two active-low output enables. When either  $\overline{OE}1$  or  $\overline{OE}2$  are high, the terminal outputs are in the high-impedance state. This device is designed to be used with 3-state memory address drivers, etc.

All inputs are equipped with protection circuits against static discharge.



Weight: 0.03 g (typ.)

#### **Features**

- Low voltage operation: VCC = 1.8~3.6 V
- High speed operation:  $t_{pd} = 3.5 \text{ ns (max) (VCC} = 3.0 \sim 3.6 \text{ V)}$

 $t_{pd} = 4.2 \text{ ns (max) (V}_{CC} = 2.3 \sim 2.7 \text{ V})$ 

 $t_{pd} = 8.4 \text{ ns (max) (VCC} = 1.8 \text{ V)}$ 

- 3.6 V tolerant inputs and outputs.
- Output current:  $I_{OH}/I_{OL} = \pm 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$

 $IOH/IOL = \pm 18 \text{ mA (min) (VCC} = 2.3 \text{ V)}$ 

 $I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.8 \text{ V)}$ 

- Latch-up performance: ±300 mA
- ESD performance: Machine model > ±200 V

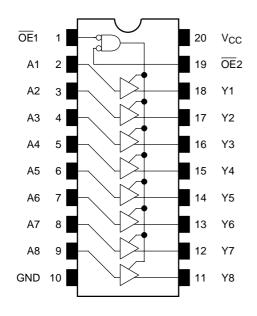
Human body model  $> \pm 2000 \text{ V}$ 

- Package: VSSOP (US20)
- Power down protection is provided on all inputs and outputs.
- Supports live insertion/withdrawal (\*)

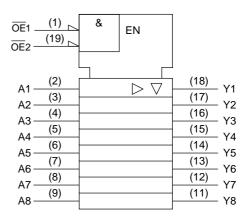
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<sup>\*:</sup> To ensure the high-impedance state during power up or power down,  $\overline{\sf OE}$  should be tied to V<sub>CC</sub> through a pullup resistor; the minimum value of the resistor is determined by the current-sourcing capability of the driver.

## Pin Assignment (top view)



## **IEC Logic Level**



### **Truth Table**

	Inputs					
OE1	OE2	An	Outputs			
Н	X	Х	Z			
Х	Н	Х	Z			
L	L	Н	Н			
L	L	L	L			

X: Don't care

Z: High impedance

### **Maximum Ratings**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V
DC input voltage	V <sub>IN</sub>	-0.5~4.6	V
		-0.5~4.6 (Note 1)	V
DC output voltage	Vout	-0.5~V <sub>CC</sub> + 0.5 (Note 2)	
Input diode current	I <sub>IK</sub>	-50	mA
Output diode current	lok	±50 (Note 3)	mA
DC output current	lout	±50	mA
Power dissipation	P <sub>D</sub>	180	mW
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	T <sub>stg</sub>	-65~150	°C

Note 1: Off-state

Note 2: High or low state. IOUT absolute maximum rating must be observed.

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Note 3: V<sub>OUT</sub> < GND, V<sub>OUT</sub> > V<sub>CC</sub>

TC7MA541FK

## **Recommended Operating Range**

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vcc	1.8~3.6	V	
Supply voltage	v CC	1.2~3.6 (Note 4)	V	
Input voltage	V <sub>IN</sub>	-0.3~3.6	V	
Output voltage	\/	0~3.6 (Note 5)	V	
Output voltage	Vout	0~V <sub>CC</sub> (Note 6)	V	
		±24 (Note 7)		
Output current	I <sub>OH</sub> /I <sub>OL</sub>	±18 (Note 8)	mA	
		±6 (Note 9)		
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Input rise and fall time	dt/dv	0~10 (Note 10)	ns/V	

Note 4: Data retention only

Note 5: Off-state

Note 6: High or low state

Note 7:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 9:  $V_{CC} = 1.8 \text{ V}$ 

Note 10:  $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$ 

### **Electrical Characteristics**

## DC Characteristics (Ta = -40~85°C, 2.7 V < V<sub>CC</sub> $\leq$ 3.6 V)

Characte	riotico	Cumbal	Too	at Condition		Min	Max	Unit
Characte	ensucs	Symbol	Test Condition		V <sub>CC</sub> (V)	IVIII	IVIAX	Onit
Input voltage	High level	$V_{IH}$		_	2.7~3.6	2.0	_	V
input voitage	Low level	V <sub>IL</sub>			2.7~3.6	_	0.8	V
				I <sub>OH</sub> = -100 μA	2.7~3.6	V <sub>CC</sub> - 0.2	_	
	High level	Voh	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -12 mA	2.7	2.2	_	
				I <sub>OH</sub> = -18 mA	3.0	2.4	_	
Output voltage				I <sub>OH</sub> = -24 mA	3.0	2.2	_	V
			L V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OL} = 100 \mu A$	2.7~3.6	_	0.2	
	Low level	V		I <sub>OL</sub> = 12 mA	2.7	_	0.4	
Low	Low level	V <sub>OL</sub>		I <sub>OL</sub> = 18 mA	3.0	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage curr	ent	I <sub>IN</sub>	V <sub>IN</sub> = 0~3.6 V		2.7~3.6	_	±5.0	μА
2 state output off	ototo ourront	la-	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$			±10.0	^
3-state output off-state current		loz	V <sub>OUT</sub> = 0~3.6 V		2.7~3.6	_	±10.0	μΑ
Power off leakage	current	l <sub>OFF</sub>	FF V <sub>IN</sub> , V <sub>OUT</sub> = 0~3.6 V		0	_	10.0	μА
			$V_{IN} = V_{CC}$ or GND	V <sub>IN</sub> = V <sub>CC</sub> or GND		_	20.0	
Quiescent supply current		icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		2.7~3.6	_	±20.0	μΑ
		Δl <sub>CC</sub>	$V_{IH} = V_{CC} - 0.6 V (p)$	er input)	2.7~3.6		750	

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## DC Characteristics (Ta = $-40~85^{\circ}$ C, 2.3 V $\leq$ V<sub>CC</sub> $\leq$ 2.7 V)

Characteri	stics	Symbol	Test	Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Input voltage	High level	VIH		_	2.3~2.7	1.6	_	V
input voltage	Low level	V <sub>IL</sub>		_	2.3~2.7	_	0.7	٧
				I <sub>OH</sub> = -100 μA	2.3~2.7	V <sub>CC</sub> - 0.2	_	
	High level	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -6 \text{ mA}$	2.3	2.0	_	
				$I_{OH} = -12 \text{ mA}$	2.3	1.8	_	V
Output voltage				$I_{OH} = -18 \text{ mA}$	2.3	1.7	_	
		V <sub>OL</sub> V <sub>IN</sub> =	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 100 \ \mu A$	2.3~2.7	_	0.2	
	Low level			$I_{OL} = 12 \text{ mA}$	2.3	_	0.4	
				$I_{OL} = 18 \text{ mA}$	2.3	_	0.6	
Input leakage curre	nt	I <sub>IN</sub>	V <sub>IN</sub> = 0~3.6 V		2.3~2.7	_	±5.0	μΑ
3-state output off-state current		l <sub>OZ</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	$V_{IN} = V_{IH}$ or $V_{IL}$		_	±10.0	μА
		OZ.	V <sub>OUT</sub> = 0~3.6 V	' <sub>OUT</sub> = 0~3.6 V				
Power off leakage of	current	I <sub>OFF</sub>	$V_{IN}$ , $V_{OUT} = 0$ ~3.6 $V$		0	_	10.0	μΑ
Quiescent supply c	urrent	Icc	$V_{IN} = V_{CC}$ or GND		2.3~2.7	_	20.0	μΑ
Quiocociii supply o	anon.	.00	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3$	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		_	±20.0	par (

## DC Characteristics (Ta = $-40 \sim 85$ °C, 1.8 V $\leq$ V<sub>CC</sub> < 2.3 V)

Characteristics		Symbol	Test Condition			Min	Max	Unit
Characteric	, ii o o	Cymbol	100.0	onalion	V <sub>CC</sub> (V)		Max	Orme
Input voltage	High level	V <sub>IH</sub>	-	_	1.8~2.3	$^{0.7\times}_{\text{VCC}}$		V
input voltage	Low level	V <sub>IL</sub>	-	_	1.8~2.3		0.2 × V <sub>CC</sub>	V
	High level	Voн	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -100 μA	1.8	V <sub>CC</sub> - 0.2		
Output voltage				$I_{OH} = -6 \text{ mA}$	1.8	1.4		V
	Low level	Voi	$V_{IN} = V_{IH}$ or $V_{II}$	$I_{OL} = 100 \mu A$	1.8		0.2	
	LOW level	V <sub>OL</sub>	AIV = AIH OL AIF	I <sub>OL</sub> = 6 mA	1.8	_	0.3	
Input leakage currer	nt	I <sub>IN</sub>	V <sub>IN</sub> = 0~3.6 V	•	1.8	_	±5.0	μΑ
3-state output off-sta	ate current	loz	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \sim 3.6 \text{ V}$		1.8	_	±10.0	μА
Power off leakage c	urrent	I <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0~3.6 V		0	_	10.0	μΑ
Quiescent supply current		laa	$V_{IN} = V_{CC}$ or GND		1.8	_	20.0	
		Icc	$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		1.8	_	±20.0	μΑ

### AC Characteristics (Ta = -40~85°C, Input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ $\Omega$ )

Characteristics	Symbol	Test Condition		Min	Max	Unit
2.10.100.101.101	-,		V <sub>CC</sub> (V)			Omit
	<b></b>		1.8	1.5	8.4	
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	$2.5 \pm 0.2$	0.8	4.2	ns
	t <sub>pHL</sub>		$3.3 \pm 0.3$	0.6	3.5	
	4		1.8	1.5	9.8	
3-state output enable time	t <sub>pZL</sub>	Figure 1, Figure 3	$2.5 \pm 0.2$	0.8	5.5	ns
			$3.3 \pm 0.3$	0.6	4.5	
	4		1.8	1.5	6.5	
3-state output disable time	t <sub>pLZ</sub>	Figure 1, Figure 3	$2.5\pm0.2$	0.8	3.6	ns
			$3.3\pm0.3$	0.6	3.3	
		(Note 11)	1.8	_	0.5	
Output to output skew	tosLH		$2.5\pm0.2$	_	0.5	ns
	tosHL		$3.3 \pm 0.3$	_	0.5	

For  $C_L = 50$  pF, add approximately 300 ps to the AC maximum specification.

Note 11: This parameter is guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$ 

## Dynamic Switching Characteristics (Ta = 25°C, Input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
				vCC (v)		
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 12)	1.8	0.25	
Quiet output maximum dynamic V <sub>OL</sub>	$V_{OLP}$	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 12)	2.5	0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 12)	3.3	8.0	
	V <sub>OLV</sub>	V <sub>IH</sub> = 1.8 V, V <sub>IL</sub> = 0 V	(Note 12)	1.8	-0.25	V
Quiet output minimum dynamic V <sub>OL</sub>		V <sub>IH</sub> = 2.5 V, V <sub>IL</sub> = 0 V	(Note 12)	2.5	-0.6	
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 12)	3.3	-0.8	
		V <sub>IH</sub> = 1.8 V, V <sub>IL</sub> = 0 V	(Note 12)	1.8	1.5	
Quiet output minimum dynamic V <sub>OH</sub>	V <sub>OHV</sub>	V <sub>IH</sub> = 2.5 V, V <sub>IL</sub> = 0 V	(Note 12)	2.5	1.9	V
		V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	(Note 12)	3.3	2.2	

Note 12: This parameter is guaranteed by design.

### **Capacitive Characteristics (Ta = 25°C)**

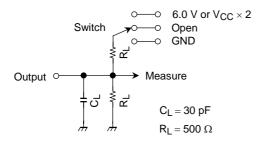
Characteristics	Symbol	Test Condition			Тур.	Unit
Characteristics	Cymbol			V <sub>CC</sub> (V)	ıyρ.	Offic
Input capacitance	C <sub>IN</sub>	_		1.8, 2.5, 3.3	6	pF
Output capacitance	CO	_		1.8, 2.5, 3.3	7	pF
Power dissipation capacitance	C <sub>PD</sub>	$f_{IN} = 10 \text{ MHz}$ (I	(Note 13)	1.8, 2.5, 3.3	20	pF

Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$ 

### **AC Test Circuit**



Parameter	Switch		
t <sub>pLH</sub> , t <sub>pHL</sub>	Open		
t <sub>pLZ</sub> , t <sub>pZL</sub>	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		
t <sub>pHZ</sub> , t <sub>pZH</sub>	GND		

Figure 1

## **AC Waveform**

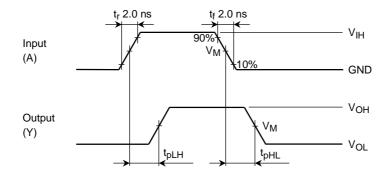


Figure 2 tpLH, tpHL

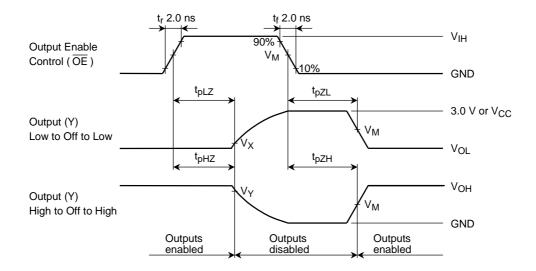
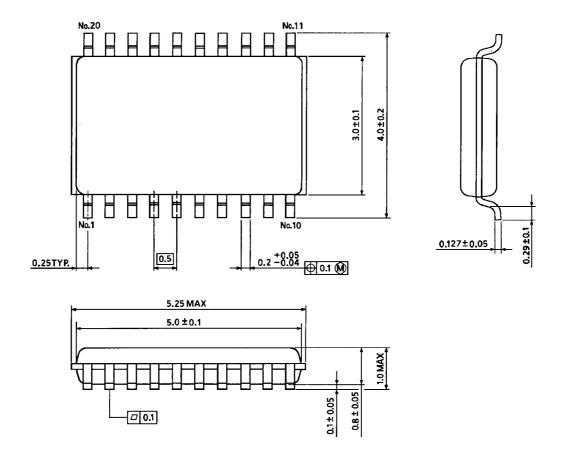


Figure 3  $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$ 

Cumbal	V <sub>CC</sub>					
Symbol	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2\textrm{V}$	1.8 V			
$V_{IH}$	2.7 V	V <sub>CC</sub>	V <sub>CC</sub>			
V <sub>M</sub>	1.5 V	V <sub>CC</sub> /2	V <sub>CC</sub> /2			
V <sub>X</sub>	V <sub>OL</sub> + 0.3 V	V <sub>OL</sub> + 0.15 V	V <sub>OL</sub> + 0.15 V			
VY	V <sub>OH</sub> – 0.3 V	V <sub>OH</sub> – 0.15 V	V <sub>OH</sub> – 0.15 V			

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## **Package Dimensions**



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Weight: 0.03 g (typ.)

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