

Silicon NPN Power Transistors**2N6542 2N6543****DESCRIPTION**

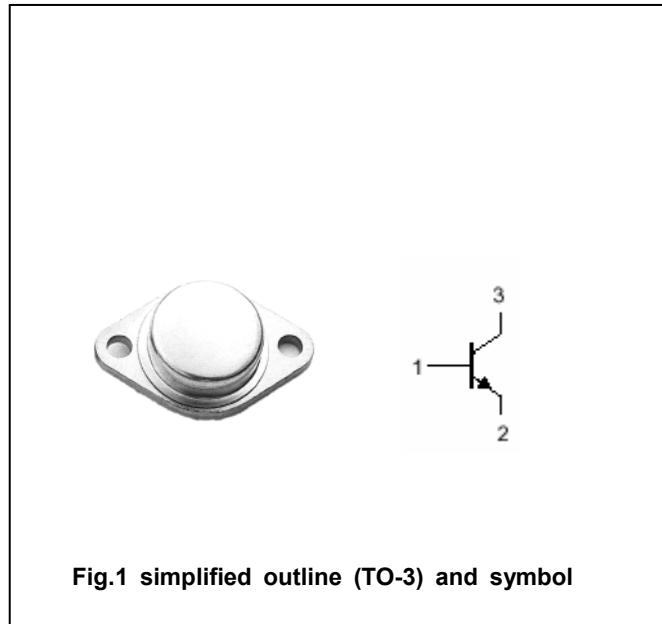
- With TO-3 package
- High voltage,high speed

APPLICATIONS

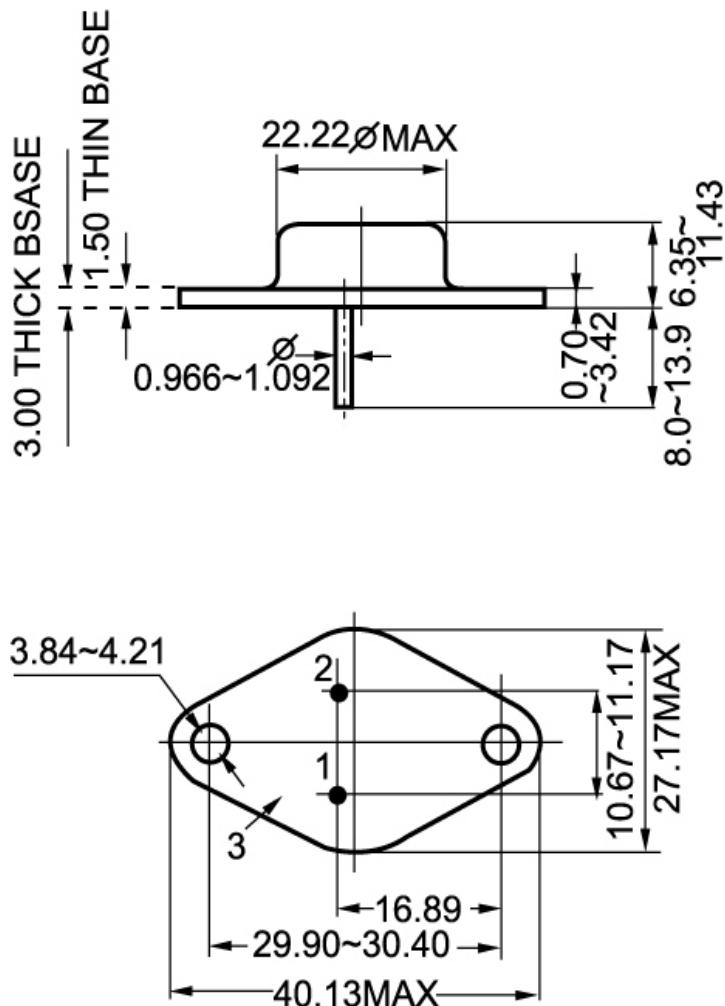
- Switching regulators
- PWM inverters and motor controls
- Solenoid and relay drivers
- Deflection circuits

PINNING

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

**Absolute maximum ratings(Ta=□)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter 2N6542	650	V
			850	
V _{CEO}	Collector-emitter voltage	Open base 2N6542	300	V
			400	
V _{EBO}	Emitter-base voltage	Open collector	8	V
I _C	Collector current		5	A
I _{CM}	Collector current-peak		10	A
I _B	Base current		5	A
I _E	Emitter current		10	A
I _{EM}	Emitter current-peak		20	A
P _D	Total Power Dissipation	T _C =25□	100	W
T _j	Junction temperature		200	□
T _{stg}	Storage temperature		-65~200	□

Silicon NPN Power Transistors**2N6542 2N6543****PACKAGE OUTLINE**Fig.2 outline dimensions (unindicated tolerance: $\pm 0.10\text{mm}$)