TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

# 2SK1827

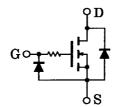
# High Speed Switching Applications Analog Switch Applications

- 4 V gate drive
- Low threshold voltage:  $V_{th} = 0.8 \sim 2.5 \text{ V}$
- High speed
- Enhancement-mode
- Small package

Marking

**Equivalent Circuit** 





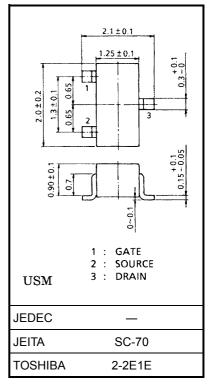
#### Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	$V_{DS}$	50	V
Gate-source voltage	$V_{GSS}$	10	<b>V</b>
DC drain current	I <sub>D</sub>	50	mA
Drain power dissipation	$P_{D}$	100	mW
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	<b>−55~150</b>	°C

Note: This transistor is electrostatic sensitive device.

Please handle with caution.

Unit: mm

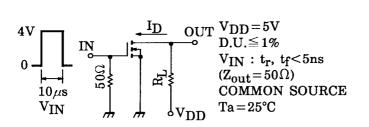


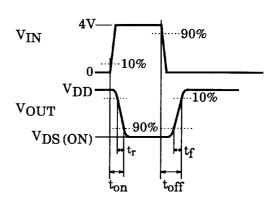
Weight: 0.006 g (typ.)

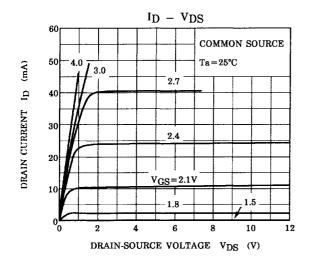
### **Electrical Characteristics (Ta = 25°C)**

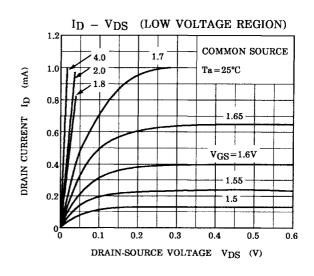
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0	_	_	1	μА
Drain-source breakdown voltage		V <sub>(BR) DSS</sub>	$I_D = 100 \ \mu A, \ V_{GS} = 0$	50	_	_	V
Drain cut-off curre	ent	I <sub>DSS</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 0$	_	_	1	μА
Gate threshold vo	Itage	V <sub>th</sub>	$V_{DS} = 5 \text{ V}, I_D = 0.1 \text{ mA}$	0.8	_	2.5	V
Forward transfer a	admittance	Y <sub>fs</sub>	$V_{DS} = 5 \text{ V}, I_{D} = 10 \text{ mA}$	20	_	_	mS
Drain-source ON	resistance	R <sub>DS</sub> (ON)	$I_D = 10 \text{ mA}, V_{GS} = 4.0 \text{ V}$	_	20	50	Ω
Input capacitance		C <sub>iss</sub>	$V_{DS} = 5 V$ , $V_{GS} = 0$ , $f = 1 MHz$	_	6.3	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 5 V$ , $V_{GS} = 0$ , $f = 1 MHz$	_	1.3	_	pF
Output capacitance		Coss	$V_{DS} = 5 V$ , $V_{GS} = 0$ , $f = 1 MHz$	_	5.7	_	pF
Switching time	Turn-on time	t <sub>on</sub>	$V_{DD} = 5 \text{ V}, I_D = 10 \text{ mA}, V_{GS} = 0~4.0 \text{ V}$	_	0.11	_	μS
	Turn-off time	t <sub>off</sub>	$V_{DD} = 5 \text{ V}, I_D = 10 \text{ mA}, V_{GS} = 0~4.0 \text{ V}$	_	0.15	_	

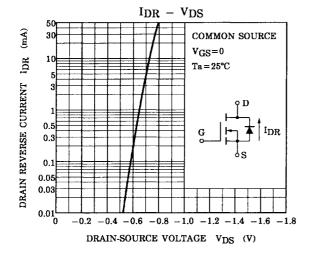
## **Switching Time Test Circuit**

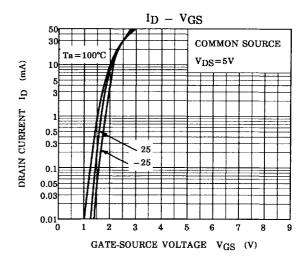


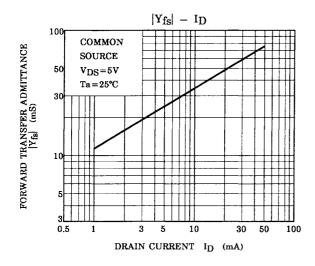


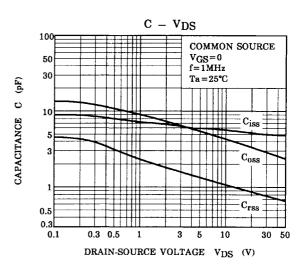




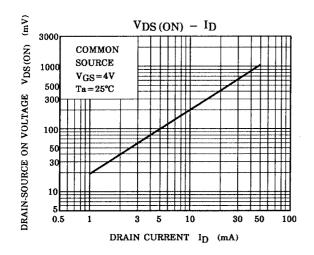


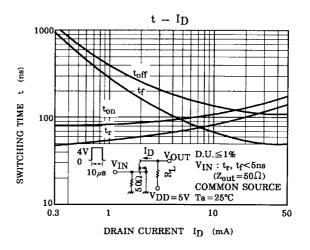


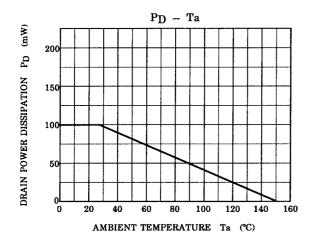




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