



# STU/D432S

SamHop Microelectronics Corp.

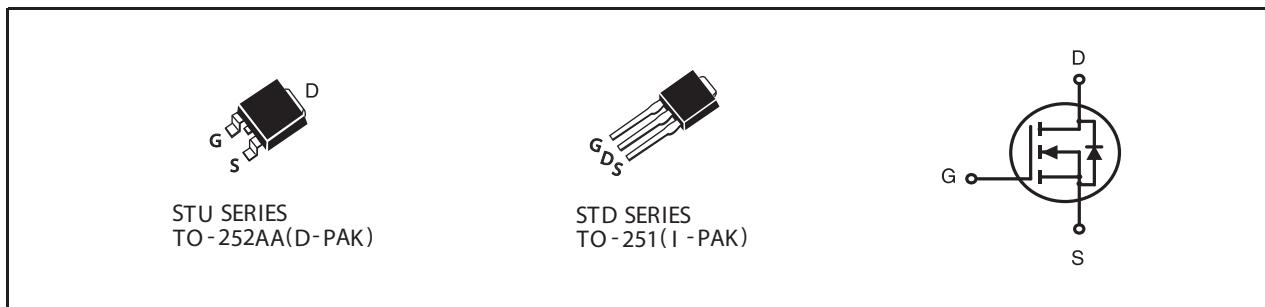
Ver 1.1

## N-Channel Logic Level Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY		
VDSS	ID	RDS(ON) (mΩ) Max
40V	50A	9 @ VGS=10V
		11 @ VGS=4.5V

### FEATURES

- Super high dense cell design for low RDS(ON).
- Rugged and reliable.
- TO-252 and TO-251 Package.



### ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Limit	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous <sup>a</sup>	$T_C=25^\circ\text{C}$	A
		$T_C=70^\circ\text{C}$	A
$I_{DM}$	-Pulsed <sup>b</sup>	147	A
$I_{AS}$	Single Pulse Avalanche Current <sup>d</sup>	23	A
$E_{AS}$	Single Pulse Avalanche Energy <sup>d</sup>	130	mJ
$P_D$	Maximum Power Dissipation <sup>a</sup>	$T_C=25^\circ\text{C}$	W
		$T_C=70^\circ\text{C}$	W
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	-55 to 150	$^\circ\text{C}$

### THERMAL CHARACTERISTICS

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case <sup>a</sup>	3	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient <sup>a</sup>	50	$^\circ\text{C/W}$

Details are subject to change without notice.

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## ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>OFF CHARACTERISTICS</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	40			V
I <sub>DS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =32V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> = ±20V , V <sub>DS</sub> =0V			±100	nA
<b>ON CHARACTERISTICS</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.25	1.6	3	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =10A		7	9	m ohm
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		9	11	m ohm
g <sub>Fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =10A		28		S
<b>DYNAMIC CHARACTERISTICS <sup>c</sup></b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V f=1.0MHz		1130		pF
C <sub>oss</sub>	Output Capacitance			240		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			145		pF
<b>SWITCHING CHARACTERISTICS <sup>c</sup></b>						
t <sub>D(ON)</sub>	Turn-On Delay Time	V <sub>DD</sub> =15V I <sub>D</sub> =1A V <sub>GS</sub> =10V R <sub>GEN</sub> =3.3 ohm		18		ns
t <sub>r</sub>	Rise Time			22		ns
t <sub>D(OFF)</sub>	Turn-Off Delay Time			61		ns
t <sub>f</sub>	Fall Time			9.6		ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V,I <sub>D</sub> =10A,V <sub>GS</sub> =10V		23.5		nC
		V <sub>DS</sub> =15V,I <sub>D</sub> =10A,V <sub>GS</sub> =4.5V		11.5		nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =15V,I <sub>D</sub> =10A, V <sub>GS</sub> =10V		2.7		nC
Q <sub>gd</sub>	Gate-Drain Charge			3.2		nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
I <sub>s</sub>	Maximum Continuous Drain-Source Diode Forward Current			20		A
V <sub>SD</sub>	Diode Forward Voltage <sup>b</sup>	V <sub>GS</sub> =0V,I <sub>s</sub> =20A		0.91	1.3	V
<b>Notes</b>						
a.Surface Mounted on FR4 Board,t ≤ 10sec.						
b.Pulse Test:Pulse Width ≤ 300us, Duty Cycle ≤ 2%.						
c.Guaranteed by design, not subject to production testing.						
d.Starting T <sub>J</sub> =25°C,L=0.5mH,V <sub>DD</sub> = 20V.(See Figure13)						

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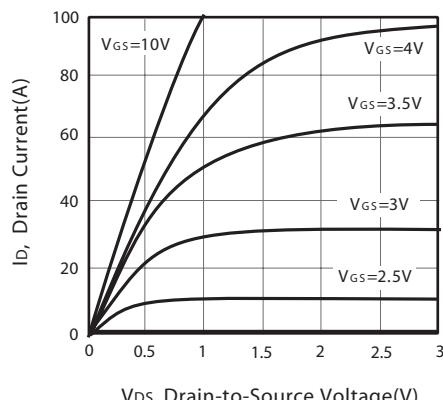


Figure 1. Output Characteristics

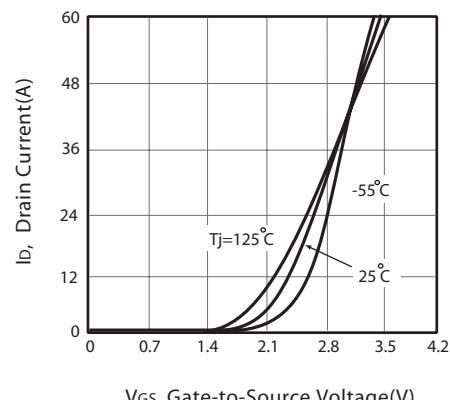


Figure 2. Transfer Characteristics

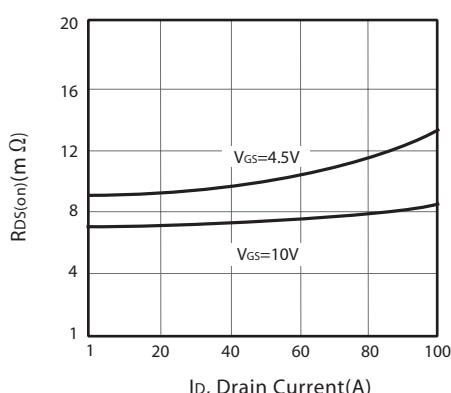


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

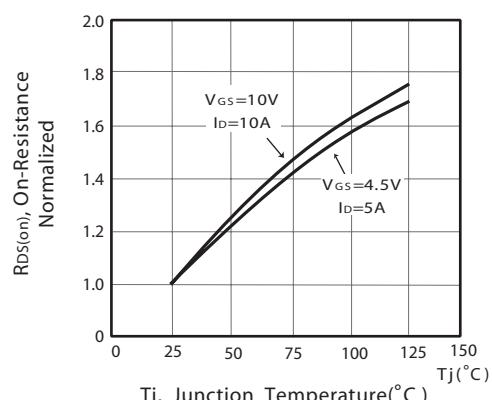


Figure 4. On-Resistance Variation with Drain Current and Temperature

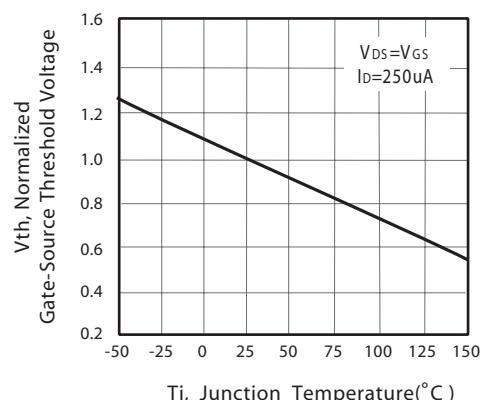


Figure 5. Gate Threshold Variation with Temperature

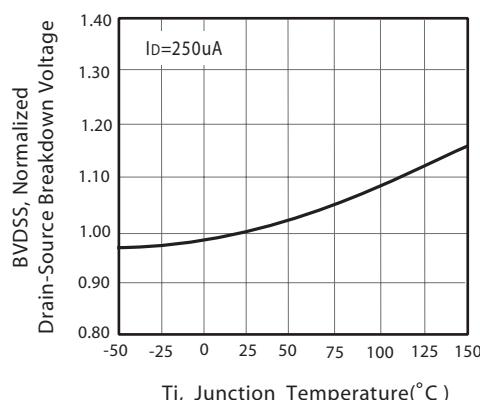
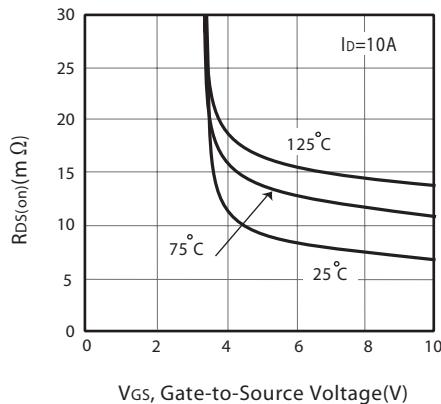


Figure 6. Breakdown Voltage Variation with Temperature

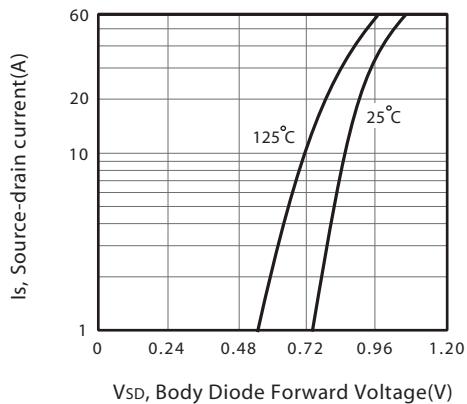
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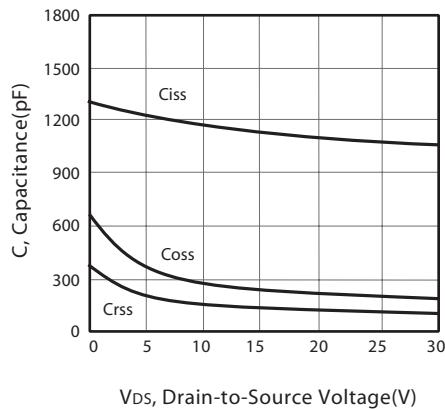
V<sub>GS</sub>, Gate-to-Source Voltage(V)

Figure 7. On-Resistance vs. Gate-Source Voltage



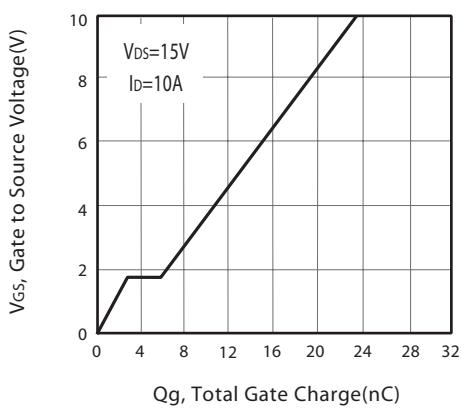
V<sub>SD</sub>, Body Diode Forward Voltage(V)

Figure 8. Body Diode Forward Voltage Variation with Source Current



V<sub>DS</sub>, Drain-to-Source Voltage(V)

Figure 9. Capacitance



Q<sub>g</sub>, Total Gate Charge(nC)

Figure 10. Gate Charge

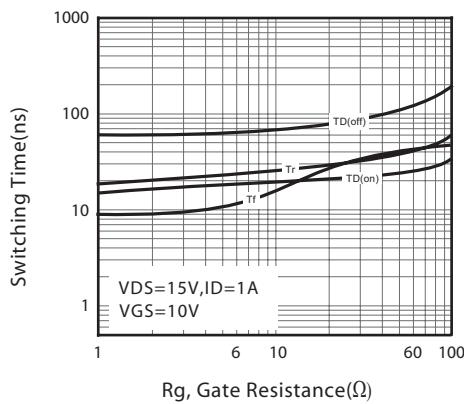


Figure 11. switching characteristics

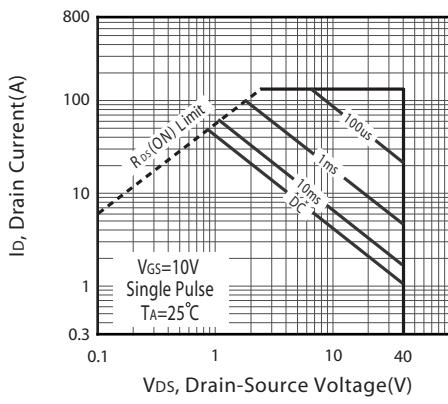
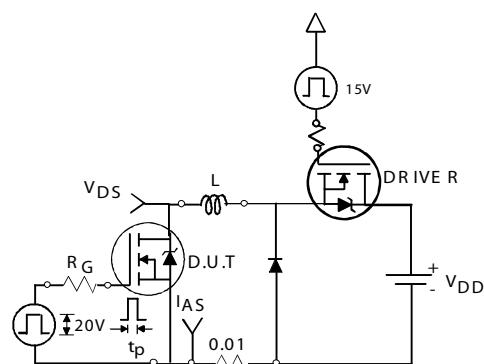


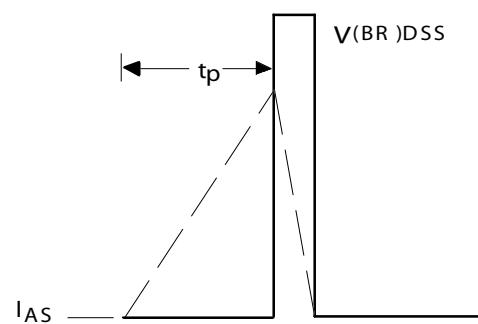
Figure 12. Maximum Safe Operating Area

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Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

Figure 13b.

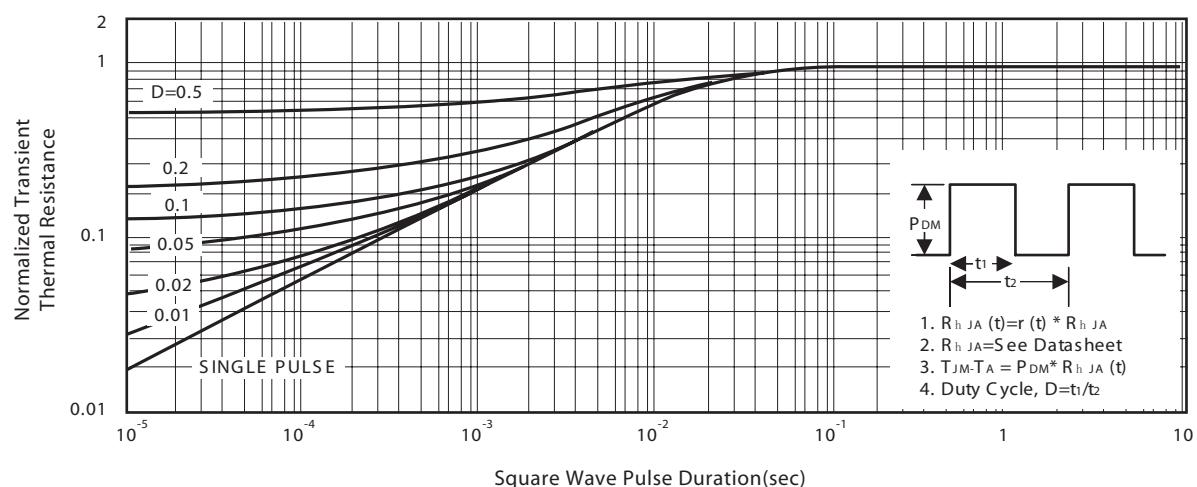
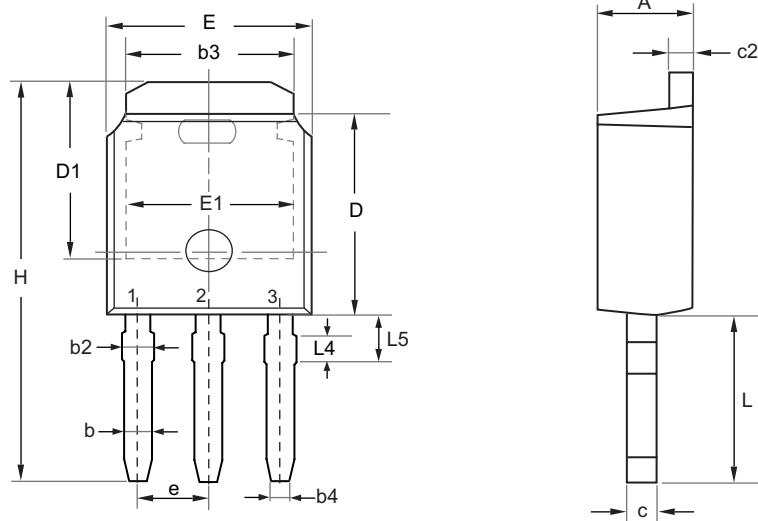


Figure 14. Normalized Thermal Transient Impedance Curve

## PACKAGE OUTLINE DIMENSIONS

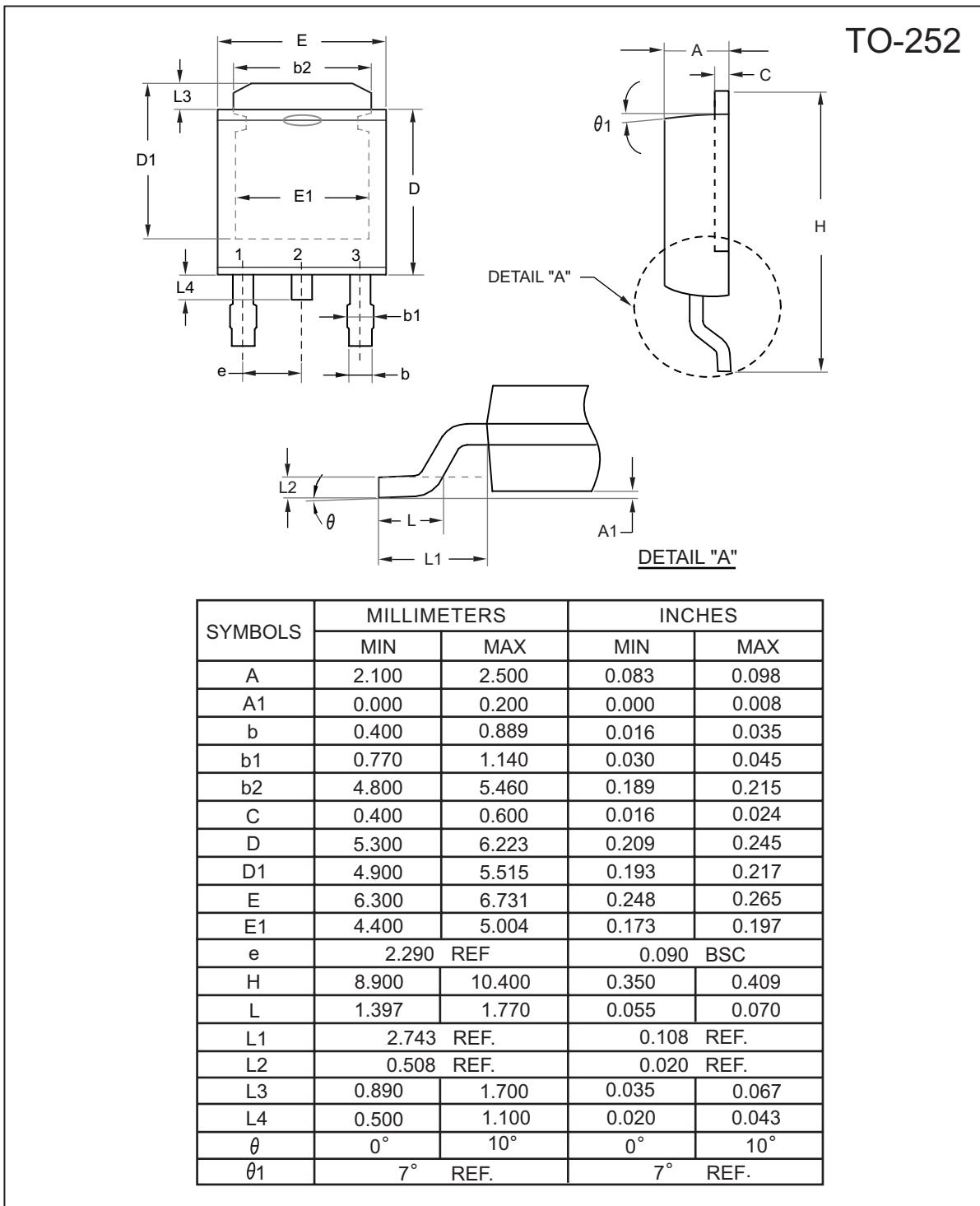
TO-251



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
E	6.400	6.731	0.252	0.265
L	3.980	4.280	0.157	0.169
L4	0.698	REF	0.027	REF
L5	0.972	1.226	0.038	0.048
D	6.000	6.223	0.236	0.245
H	11.050	11.450	0.435	0.450
b	0.640	0.880	0.025	0.035
b2	0.770	1.140	0.030	0.045
b3	5.210	5.460	0.205	0.215
b4	0.450	0.550	0.018	0.022
e	2.286	BSC	0.090	BSC
A	2.200	2.380	0.087	0.094
c	0.400	0.600	0.016	0.024
c2	0.400	0.600	0.016	0.024
D1	5.100	---	0.201	---
E1	4.400	---	0.173	---

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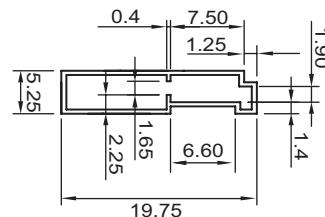
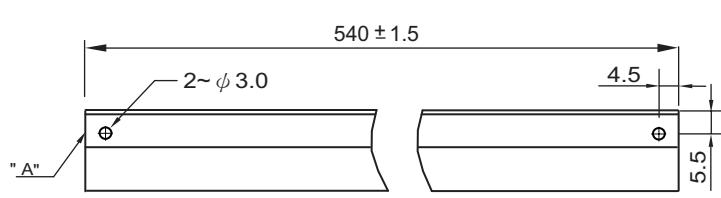
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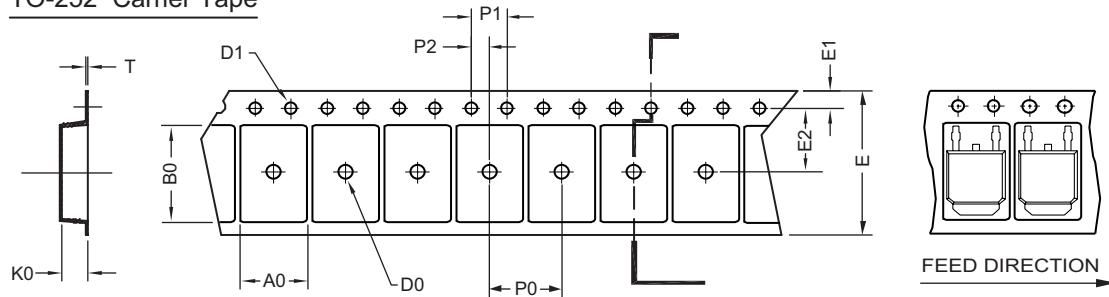
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## TO-251 Tube/TO-252 Tape and Reel Data

### TO-251 Tube



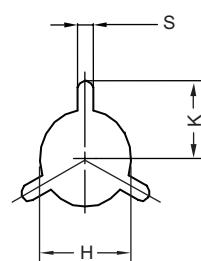
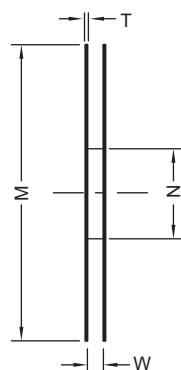
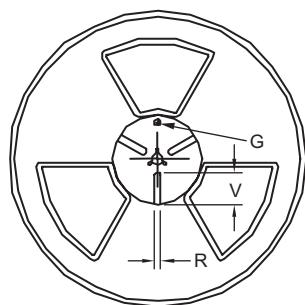
### TO-252 Carrier Tape



UNIT:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
TO-252 (16 mm)	6.96 ±0.1	10.49 ±0.1	2.79 ±0.1	ψ 2	ψ 1.5 + 0.1 - 0	16.0 ±0.3	1.75 ±0.1	7.5 ±0.15	8.0 ±0.1	4.0 ±0.1	2.0 ±0.15	0.3 ±0.05

### TO-252 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	T	H	K	S	G	R	V
16 mm	ψ 330	ψ 330 ± 0.5	ψ 97 ± 1.0	17.0 + 1.5 - 0	2.2	ψ 13.0 + 0.5 - 0.2	10.6	2.0 ± 0.5	---	---	---

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