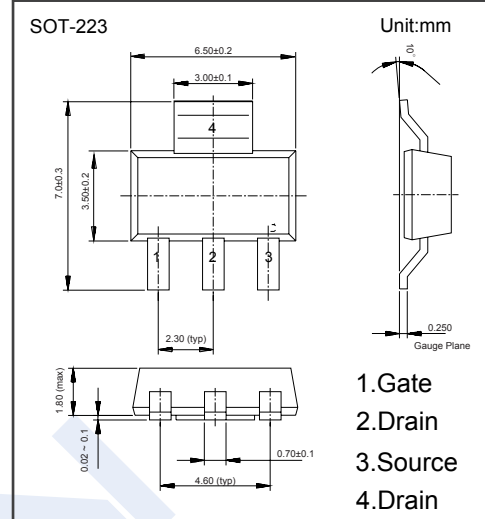
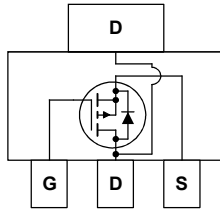


## P-Channel MOSFET

### NDT2955 (KDT2955)

#### ■ Features

- $V_{DS} (V) = -60V$
- $I_D = -2.5 A (V_{GS} = -10V)$
- $R_{DS(ON)} < 300m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 500m\Omega (V_{GS} = -4.5V)$



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current (Note.1)	$I_D$	-2.5	A
Pulsed Drain Current (Note.1)	$I_{DM}$	-15	
Power Dissipation (Note.2)	$P_D$	3	W
(Note.3)		1.3	
		1.1	
Thermal Resistance.Junction- to-Ambient (Note.1)	$R_{thJA}$	42	$^\circ C/W$
Thermal Resistance.Junction- to-Case	$R_{thJC}$	12	
Junction Temperature	$T_J$	150	$^\circ C$
Junction Storage Temperature Range	$T_{stg}$	-55 to 150	

Note.1:  $42^\circ C/W$  when mounted on a  $1in^2$  pad of 2 oz copper

Note.2:  $95^\circ C/W$  when mounted on a  $.0066in^2$  pad of 2 oz copper

Note.3:  $110^\circ C/W$  when mounted on a minimum pad.

## P-Channel MOSFET

### NDT2955 (KDT2955)

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =-250 μA, V <sub>GS</sub> =0V		-60		V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V			-10	μA
Gate-Body leakage current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> I <sub>D</sub> =-250 μA	-2		-4	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.5A			300	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A			500	
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.5A T <sub>J</sub> =125°C			513	
On state drain current	I <sub>D(ON)</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-5V	-12			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-2.5A		5.5		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-30V, f=1MHz		601		pF
Output Capacitance	C <sub>oss</sub>			85		
Reverse Transfer Capacitance	C <sub>rss</sub>			35		
Total Gate Charge	Q <sub>g</sub>			11	15	
Gate Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-30V, I <sub>D</sub> =-2.5A (Note.1)		2.4		
Gate Drain Charge	Q <sub>gd</sub>			2.7		
Turn-On DelayTime	t <sub>d(on)</sub>		V <sub>GS</sub> =-10V, V <sub>DS</sub> =-30V, I <sub>D</sub> =-1A, R <sub>G</sub> =6 Ω (Note.1)		12	21
Turn-On Rise Time	t <sub>r</sub>			10	20	
Turn-Off DelayTime	t <sub>d(off)</sub>			19	34	
Turn-Off Fall Time	t <sub>f</sub>			6	12	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =-2.5A, di/dt=100A/μs			25	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			40		nC
Drain-Source Avalanche Energy	W <sub>DSS</sub>	Single Pulse, V <sub>DD</sub> = 30 V, I <sub>D</sub> = 2.5 A			174	mJ
Maximum Body-Diode Continuous Current	I <sub>S</sub>				-2.5	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-2.5A, V <sub>GS</sub> =0V (Note.1)			-1.2	V

Note.1: Pulse Test: Pulse Width < 300μs, Duty Cycle < 2.0%

#### ■ Marking

Marking	*DT2955
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## P-Channel MOSFET NDT2955 (KDT2955)

■ Typical Characteristics

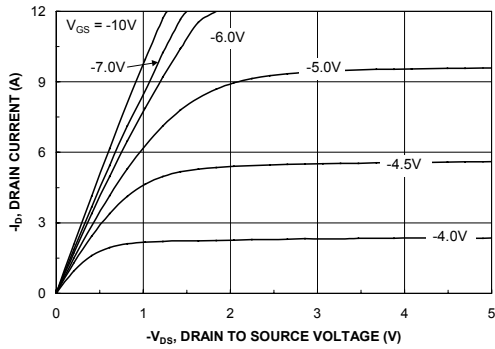


Figure 1. On-Region Characteristics.

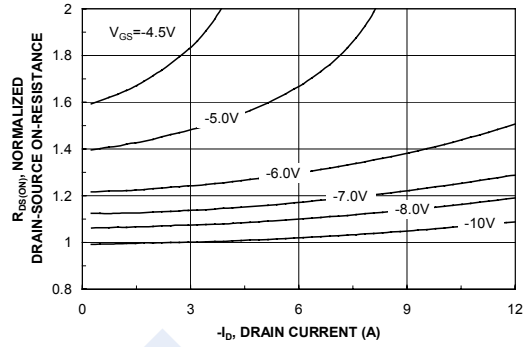


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

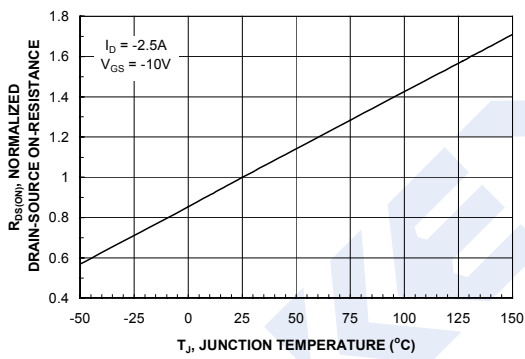


Figure 3. On-Resistance Variation with Temperature.

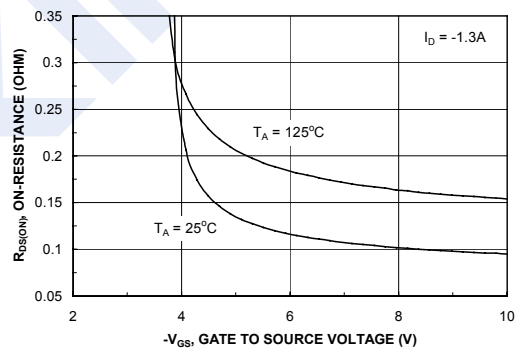


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

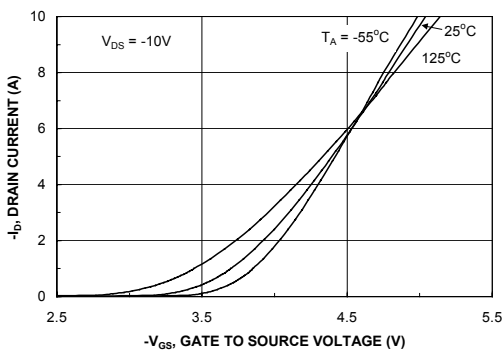


Figure 5. Transfer Characteristics.

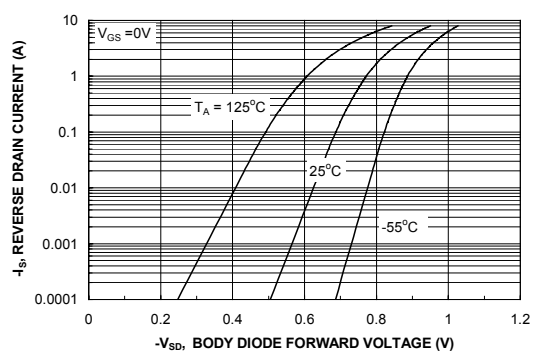


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## P-Channel MOSFET NDT2955 (KDT2955)

■ Typical Characteristics

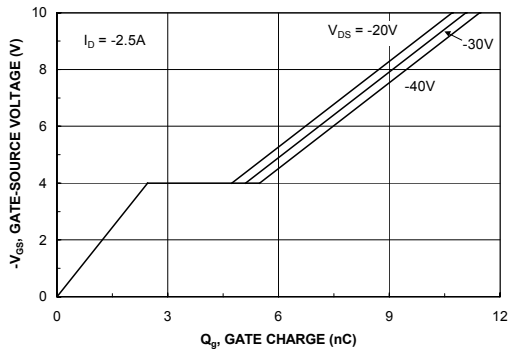


Figure 7. Gate Charge Characteristics.

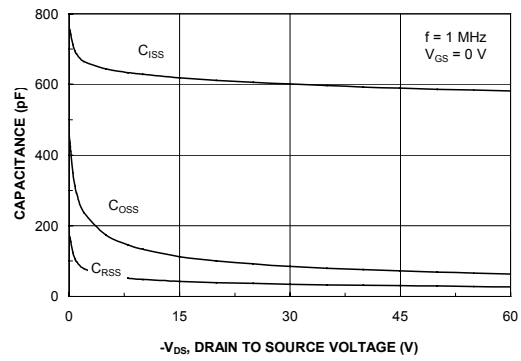


Figure 8. Capacitance Characteristics.

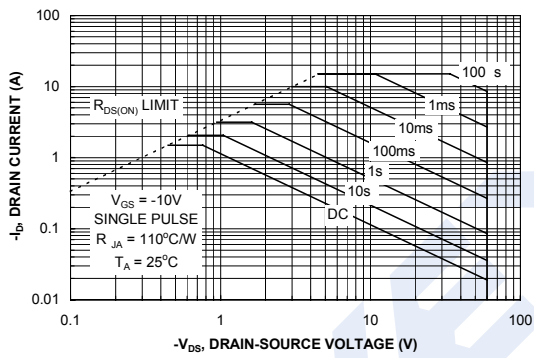


Figure 9. Maximum Safe Operating Area.

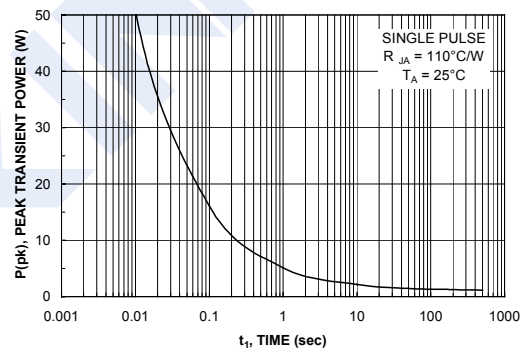


Figure 10. Single Pulse Maximum Power Dissipation.

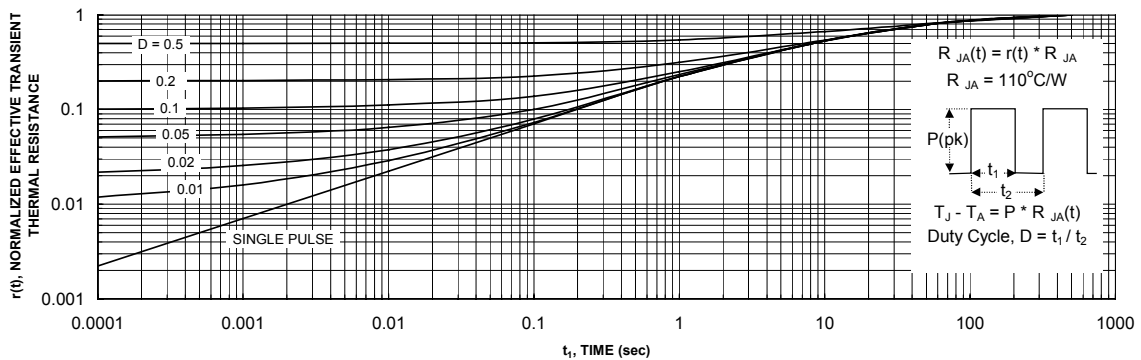


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.