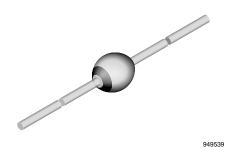
### Vishay Semiconductors



### **Ultra-Fast Avalanche Sinterglass Diode**



MECHANICAL DATA

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

Mounting position: any Weight: approx. 369 mg

#### **FEATURES**

- Controlled avalanche characteristic
- Low forward voltage
- Ultra fast recovery time
- · Glass passivated junction
- · Hermetically sealed package
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition







#### COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

 Very fast rectification diode e.g. for switch mode power supply

PARTS TABLE				
PART	TYPE DIFFERENTIATION	PACKAGE		
BYV27-50	V <sub>R</sub> = 50 V; I <sub>FAV</sub> = 2 A	SOD-57		
BYV27-100	V <sub>R</sub> = 100 V; I <sub>FAV</sub> = 2 A	SOD-57		
BYV27-150	V <sub>R</sub> = 150 V; I <sub>FAV</sub> = 2 A	SOD-57		
BYV27-200	V <sub>R</sub> = 200 V; I <sub>FAV</sub> = 2 A	SOD-57		

PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
Peak reverse voltage, non repetitive	See electrical characteristics	BYV27-50	V <sub>RSM</sub>	55	V
		BYV27-100	V <sub>RSM</sub>	110	V
		BYV27-150	V <sub>RSM</sub>	165	V
		BYV27-200	V <sub>RSM</sub>	220	V
Reverse voltage = repetitive peak reverse voltage	See electrical characteristics	BYV27-50	$V_R = V_{RRM}$	50	V
		BYV27-100	$V_R = V_{RRM}$	100	V
		BYV27-150	$V_R = V_{RRM}$	150	V
		BYV27-200	$V_R = V_{RRM}$	200	V
Peak forward surge current	t <sub>p</sub> = 10 ms, half sine wave		I <sub>FSM</sub>	50	Α
Repetitive peak forward current			I <sub>FRM</sub>	15	Α
Average forward current			I <sub>FAV</sub>	2	Α
Pulse energy in avalanche mode, non repetitive (inductive load switch off)	I <sub>(BR)R</sub> = 1 A, T <sub>j</sub> = 175 °C		E <sub>R</sub>	20	mJ
Junction and storage temperature range			$T_j = T_{stg}$	- 55 to + 175	°C



# Ultra-Fast Avalanche Sinterglass Vishay Semiconductors Diode

MAXIMUM THERMAL RESISTANCE (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Junction ambient	I = 10 mm, T <sub>L</sub> = constant	$R_{thJA}$	45	K/W
	On PC board with spacing 25 mm	$R_{thJA}$	100	K/W

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 3A		$V_{F}$	-	-	1.07	V
	I <sub>F</sub> = 3 A, T <sub>j</sub> = 175 °C		$V_{F}$	-	-	0.88	V
Reverse current	$V_R = V_{RRM}$		I <sub>R</sub>	-	-	1	μΑ
	$V_{RSM}$		I <sub>R</sub>	-	-	100	μΑ
	$V_R = V_{RRM}$ , $T_j = 165$ °C		I <sub>R</sub>	-	-	150	μΑ
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_R = 0.25 \text{ A}$		t <sub>rr</sub>	-	-	25	ns

#### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

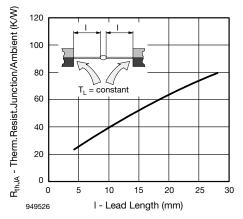


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

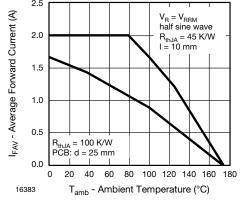


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

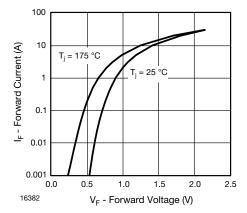


Fig. 2 - Forward Current vs. Forward Voltage

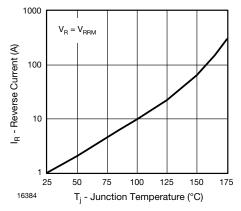


Fig. 4 - Reverse Current vs. Junction Temperature

# BYV27-50, BYV27-100, BYV27-150, BYV27-200

# Vishay Semiconductors

#### Ultra-Fast Avalanche Sinterglass Diode



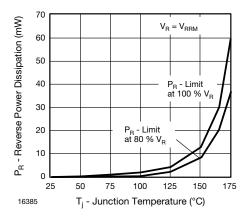


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

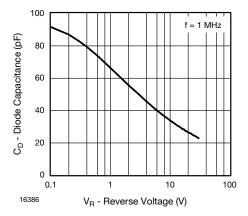
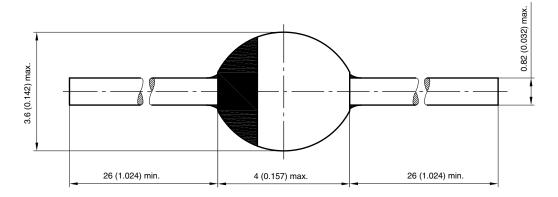


Fig. 6 - Diode Capacitance vs. Reverse Voltage

#### PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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