# **MORNSUN®**

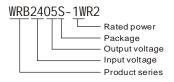
# WRA\_S - 1WR2 & WRB\_S-1WR2 Series 1W, WIDE INPUT, ISOLATED & REGULATED DUAL/SINGLE OUTPUT DC-DC CONVERTER





Patent Protected RoHS

## PART NUMBER SYSTEM



#### **FEATURES**

- Miniature SIP Package
- 2:1 wide input voltage range
- Temperature range: -40°C ~ +85°C
- 1.5KVDC isolation
- Short Circuit Protection(automatic recovery)
- Remote On/Off

## **APPLICATION**

The WRA\_S-1WR2 & WRB\_S-1WR2 Series are specially designed for applications where a wide range input voltage power supplies are isolated from the input power supply in a distributed power supply system on a circuit board. For these DC-DC converters, you can reduce the failure points of design, and save the manpower, material and time cost in developing micro power supply, and also ensure better quality, stability, safety protection, and reliability for the end products.

These products apply to where:

- Input voltage range ≤2:1;
- 2) Input and output isolation ≤1.5KVDC;
- 3) Regulated and low ripple noise is required.

Such as: industrial control, tele-communications etc.

	Input Volta	ge(VDC)	Output	Output Cu	rrent (mA)	Input Current (mA)(Typ.)		Reflected	Max.	Efficiency		
Model	Nominal	Max. <sup>①</sup>	Voltage	Max.	Min.	@Max.	@No	Ripple Current	Capacitive Load <sup>©</sup> (µF)	(%, Typ. @Max.		
	(Range)	IVIAX.	(VDC)	1110211	IVIII I.	Load	Load	(mA, Typ.)		Load		
WRA0505S-1WR2	5		±5	±100	±5	274			1000	73		
WRA0512S-1WR2			±12	±42	±2	263	25		470	76		
WRA0515S-1WR2		11	±15	±33	±2	267		30	330	75		
WRB0505S-1WR2	(4.5-9)	''	5	200	10	278	25	30	2200	72		
WRB0512S-1WR2			12	83	4	263			1000	76		
WRB0515S-1WR2			15	67	3	267			680	75		
WRA1205S-1WR2			±5	±100	±5	107			1000	78		
WRA1212S-1WR2			±12	±42	±2	103			470	81		
WRA1215S-1WR2	12 (9-18)	20	±15	±33	±2	104	12	40	330	80		
WRB1203S-1WR2			3.3	303	15	112			2700	75		
WRB1205S-1WR2		20	5	200	10	108		40	2200	77		
WRB1209S-1WR2			9	111	6	106			1800	79		
WRB1212S-1WR2			12	83	4	104			1000	80		
WRB1215S-1WR2			15	67	3	104			680	80		
WRA2405S-1WR2					±5	±100	±5	52			1000	80
WRA2412S-1WR2			±12	±42	±2	52			470	80		
WRA2415S-1WR2			±15	±33	±2	52			330	80		
WRB2403S-1WR2	24	40	3.3	303	15	56			2700	75		
WRB2405S-1WR2	(18-36)	40	5	200	10	54	6	55	2200	77		
WRB2412S-1WR2			12	83	4	51			1000	81		
WRB2415S-1WR2			15	67	3	53			680	79		
WRB2424S-1WR2			24	42	2	54			470	77		
WRA4805S-1WR2			±5	±100	±5	27			1000	76		
WRA4812S-1WR2			±12	±42	±2	26			470	80		
WRA4815S-1WR2	48 (36-75)	80	±15	±33	±2	26	4	70	330	80		
WRB4803S-1WR2	(55-75)		3.3	303	15	28			2700	75		
WRB4805S-1WR2			5	200	10	27			2200	76		

WRB4812S-1WR2	12	83	4	26		1000	81
WRB4815S-1WR2	15	67	3	26		680	80

Note: ①. Absolute maximum rating without damage on the converter, but it isn't recommended. ②. For dual output converter, the given value is the same for each output.

Item	Test Conditions	Min.	Тур.	Max.	Unit		
	5V input	-0.7		12			
	12V input	-0.7		25			
Input Surge Voltage (1sec. max.)	24V input	-0.7		50			
	48V input	-0.7		100			
	5V input	3.5	4	4.5			
Ctart un Valtaga	12V input	4.5	8	9			
Start-up Voltage	24V input	11	16	18	]		
	48V input	24	33	36	]		
nput Filter		C Filter					
Ctrl*	Models ON	Ctrl open or be insulated					
	Models OFF	Connect higher level of voltage to make sure that the input current flow into					

Note: \*Please refer to "DESIGN CONSIDERATIONS" as the direction for use of Ctrl .

Item	Test Conditions	Min.	Тур.	Max.	Unit
Output Voltage Accuracy	5% to 100% load		±1	±3	
No-load Output Voltage Accuracy	Input voltage range	-	±1.5	±5	
Output Voltage Balance	Dual output, balanced loads	-	±0.3	±0.5	%
Line Regulation	Full load, Input voltage from low to high		±0.2	±0.5	
Load Regulation	5% to 100% load		±0.4	±0.75	
Transient Recovery Time	25% lead step shapes	/	0.5	2	ms
Transient Response Deviation	25% load step change		±2.5	±5	%
Temperature coefficient	100% load		±0.02	±0.03	%/°C
Ripple *	20MHz Bandwidth		30	50	m\/n n
Noise*	ZUMITZ Dariuwium		55	75	mVp-p
Output Short Circuit Protection Continuous, automatic recovery					

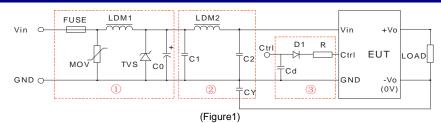
COMMON SPECIFICATIONS							
Item	Test Conditions	Min.	Тур.	Max.	Unit		
Isolation Voltage	Input-Output, Tested for 1 minute, leakage current less than 1 mA	1500			VDC		
Isolation Resistance	Input-Output, Test at 500VDC	1000			MΩ		
Isolation Capacitance	Input-Output,100KHz/0.1V		120		pF		
Switching Frequency(PFM Mode)	100% load, Nominal Input voltage		200		KHz		
MTBF	MIL-HDBK-217F @25℃	1000			K hours		
Case Material		Plastic (UL94-V0)					
Weight			4.9		g		

ENVIRONMENTAL SPECIFICATIONS								
Item	Test Conditions	Min.	Тур.	Max.	Unit			
Storage Humidity	Non condensing			95	%			
Operating Temperature	Power derating (above85°C, see Figure 5)	-40		85				
Storage Temperature		-55		125	°C			
Case Temperature Rise	Ta=25°C		25					
Lead Temperature	1.5mm from case for 10 seconds			300				
Cooling		Free air convection						

EMC SPECIFICATIONS							
CE CISPR22/EN55022 CLASS B (Recommended Circuit Refer to Figure 1-② or Figure 3)							
EIVII	RE CISPR22/EN55022 CLASS B (Recommended Refer to Figure 1-②or Figure 3)						
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EMS	ESD	IEC/EN61000-4-2	Contact ±4KV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2KV (Recommended Circuit Refer to Figure1-①)	perf. Criteria B
		IEC/EN61000-4-4	±4KV (Recommended Circuit Refer to Figure 3)	perf. Criteria B
	Surge	IEC/EN61000-4-5	±2KV (Recommended Circuit Refer to Figure1-① or Figure 3)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A
	Voltage dips, short and interruptions immunity	IEC/EN61000-4-29	0%-70%	perf. Criteria B

# **EMC RECOMMENDED CIRCUIT**

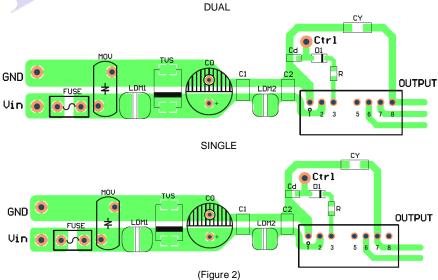


Recommended external circuit parameters:

ded external circuit parameters.									
Model	Vin:5V	Vin:12V	Vin:24V	Vin:48V					
FUSE		Slow blow ,choose according	ng to practical input current						
MOV			S14K35	S14K60					
LDM1			56µH	56µH					
TVS	SMCJ13A	SMCJ28A	SMCJ48A	SMCJ90A					
C0	680µF/16V	680μF/25V	330µF/50V	330µF/100V					
C1	4.7µF	4.7μF/	.7μF/100V						
LDM2	12µH								
C2		4.7µF/50V		4.7μF/100V					
CY		1nF/	2KV						
D1	RB160M-60/1A								
R	Follows: $R = \frac{V_C - V_D - 1.0}{I_C} - 300$								
Cd		47nF/	100V						

Note: 1. In Figure 1, part ① is the recommended external circuit for EMS , and part ② is for EMI .Choose according to requirements.

## **EMC RECOMMENDED CIRCUIT PCB LAYOUT**

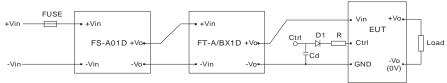


Note: The pad space between input and output GND (CY) must be≥2mm.

<sup>2.</sup> VC is between Ctrl and GND, VD is the forward conduction voltage drop of D1, IC is the current through Ctrl pin which is normally 5-10mA, the external circuit of Ctrl is as shown in figure 1-3.

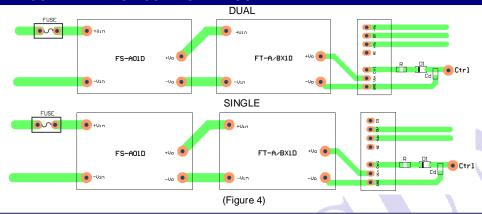
<sup>3.</sup> If there is no recommended parameters, no external component is required.

# **EMC MODULE APPLICATION CIRCUIT**

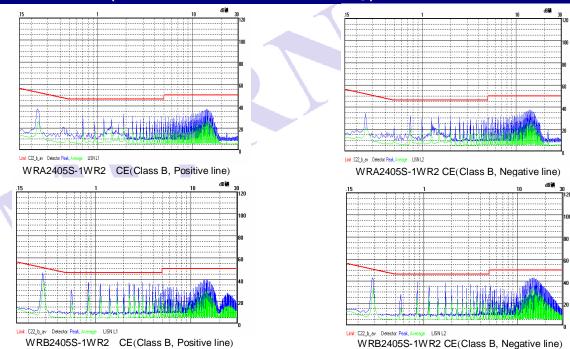


FS-A01D,FT-A/BX1D are MORNSUN's EFT suppresser (Figure 3)

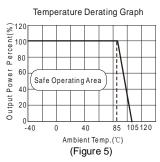
## **EMC MODULE RECOMMENDED CIRCUIT PCB LAYOUT**



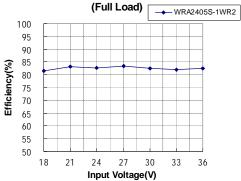
## EMI TEST WAVEFORM (RECOMMENDED CIRCUIT FINGURE 1-2)



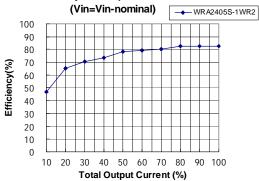
# PRODUCT TYPICAL PERFORMANCE CURVE



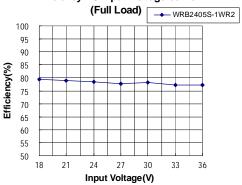
# Efficiency VS Input Voltage curve



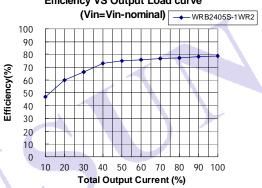
#### Efficiency VS Output Load curve



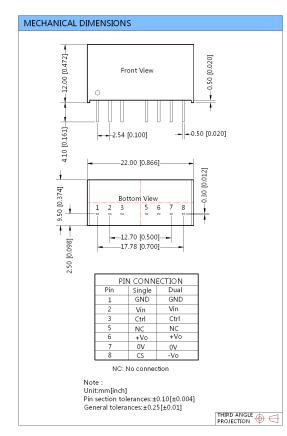
# Efficiency VS Input Voltage curve

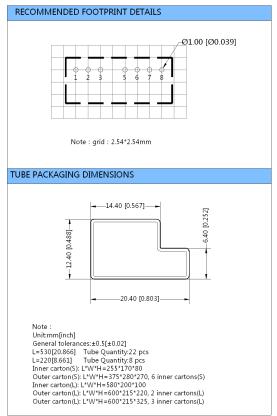


#### Efficiency VS Output Load curve



# **DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING**

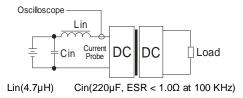




# **TEST CONFIGURATIONS**

#### Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate the source impedance.



## **DESIGN CONSIDERATIONS**

#### 1) Requirement on output load

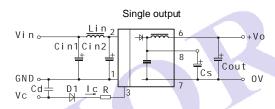
To ensure this module operate efficiently and reliably, during operation, the minimum output load could not be less than 5% of the full load, otherwise output ripple may increase dramatically. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, suppose to use the resistance of 5% rated power, or use our company's products with a lower rated output power.

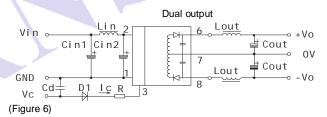
#### 2) Recommended Circuit

All the WRA\_S-1WR2 & WRB\_S-1WR2 Series have been tested according to the following recommended test circuit before leaving the factory (see Figure 6).

If you want to further decrease the input/output ripple, increase the value of the capacitance properly or choose capacitors with low ESR. However, the capacitance of the output filter must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, under the condition of safe and reliable operation, the max capacitance must be less than the Max capacitive load.

5V&12V 100uF General: Cin1: 24V&48V 10µF Cin2: 5V&12V 47µF 24V&48V 1uF 4.7μH~12μH Lin: Cs: 10μF~22μF Cout: 100μF(Typ.) Lout: 2.2µH~10µH 47nF/100V Cd:





# 3) Ctrl Terminal

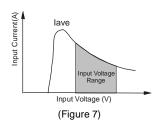
When being open or with high impedance, the converter works well; When its pin is connected to high level voltage, the converter shut down. It should be noticed that the input current should be between 5-10mA, exceeding the maximum current of 20mA will cause permanent damage to the converter. The value of R can be derived as follows:

$$R = \frac{V_C - V_D - 1.0}{I_C} - 300$$

For Detailed parameter, please refer to "EMC RECOMMENDED CIRCUIT".

#### 4) Input Current

When it is used in unregulated power supply, be sure that the fluctuating range of the power supply and the rippled voltage do not exceed the standard of module. Input current of power supply should afford the flash startup average current of this kind of DC/DC module (Figure 7).



5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

#### Note:

- 1. Min. load shouldn't be less than 5%, otherwise ripple maybe increased dramatically. If the product operates under min. load, it may not be guaranteed to meet all specifications listed. Operation with minimum load will not damage the converter.
- Recommended Dual output models unbalanced load is ≤±5%, if the product operates >±5%, it may not be guaranteed to meet all specifications listed. Please contact our technical support for more details.
- 3. Max. Capacitive Load is tested at nominal input voltage and full load.
- 4. All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 5. In this datasheet, all test methods are based on our corporate standards.
- 6. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more details.
- 7. Please contact our technical support for any specific requirement.
- 8. Specifications of this product are subject to changes without prior notice.

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