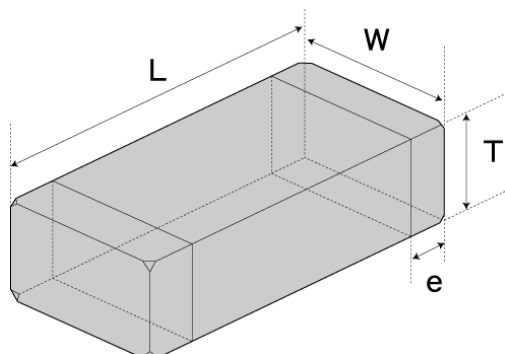


High Value Multilayer Ceramic Capacitors (High dielectric type)

UMK212AB7104MD-T



■ Features

- Item Summary
50V, X7R, 0.1 μ F, $\pm 20\%$, 0805
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping paper 4000pcs

■ Products characteristics table

CaseSize (EIA/JIS)	0805/2012
Rated Voltage (max)	50V
Capacitance	0.1 μ F
Capacitance Tolerance	$\pm 20\%$
Resistance	500 M Ω · μ F min.
Dielectric Loss Tangent	10%MAX
Temperature Characteristics (EIA/JIS)	X7R/-
Temperature Range (EIA/JIS)	-55 to +125°C/-
Capacitance Change (EIA/JIS)	$\pm 15\%$ -%
RoHS Compliance	Yes
Halogen Free	Yes
Soldering Method	Reflow

■ External Dimensions

L	2.0mm +0.15:-0.05
W	1.25mm +0.15:-0.05
T	0.85mm ± 0.10
e	0.5mm ± 0.25

2014.12.04

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
 TAIYO YUDEN reserves the right to make change to the Date at any time without notice.
 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor

TAIYO YUDEN

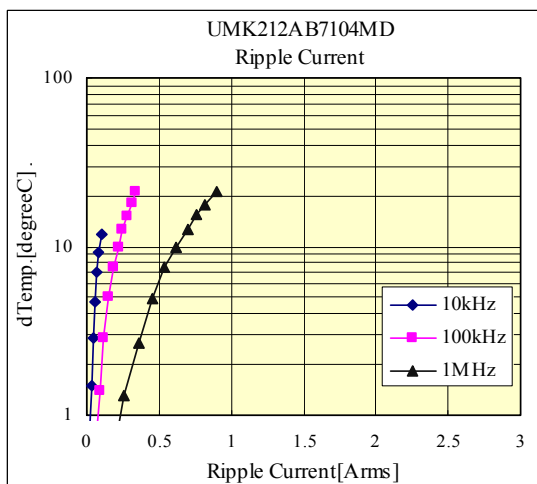
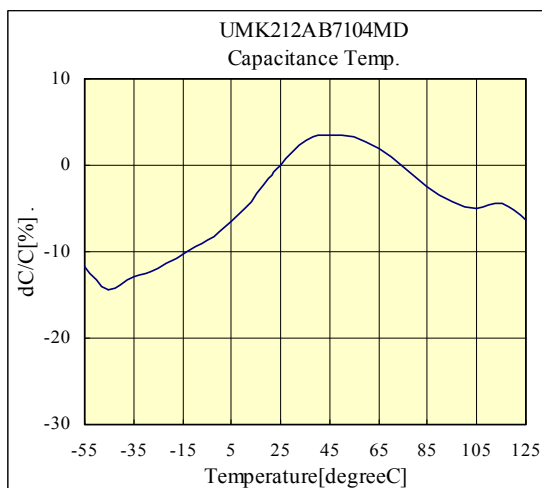
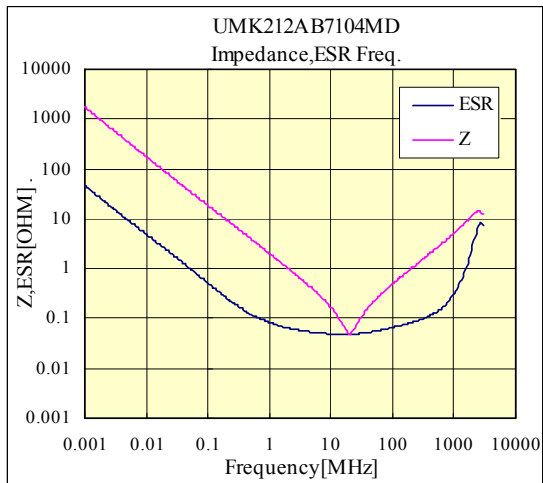
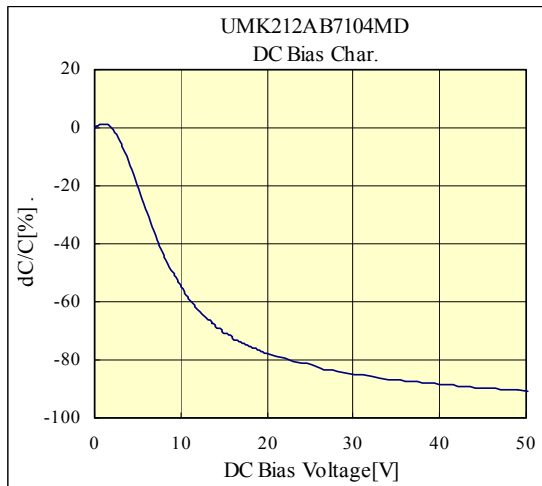
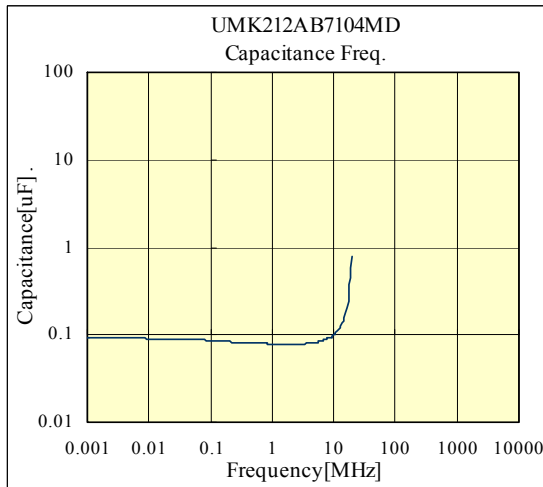
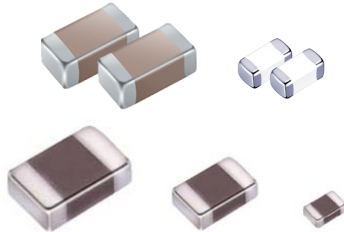
-Electrical Characteristics Data-

11R2A03

RoHS compliance product

○UMK212AB7104MD

50V 2012 X7R 0.1 μ F $\pm$ 20% $t=0.95\text{mmMAX}$
50V 0805 X7R 0.1 μ F $\pm$ 20% $t=0.037\text{inchMAX}$



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