



CHENMKO ENTERPRISE CO., LTD

SURFACE MOUNT

Dual Enhancement Mode Field Effect Transistor

N-channel: VOLTAGE 30 Volts CURRENT 7 Ampere
 P-channel: VOLTAGE 30 Volts CURRENT 5.2 Ampere

CHM8958JPT

Lead free devices

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

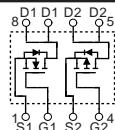
FEATURE

- * Small flat package. (SO-8)
- * Super high dense cell design for extremely low R_{DS(ON)}.
- * Lead free product is acquired.
- * High power and current handing capability.

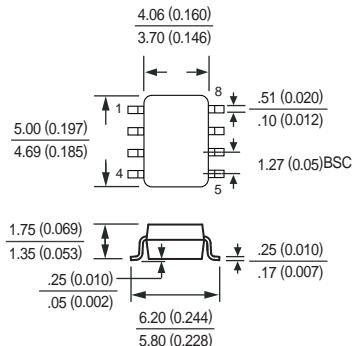
CONSTRUCTION

- * N-Channel & P-Channel Enhancement in the package

CIRCUIT



SO-8



Dimensions in millimeters

SO-8

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	N-Channel	P-Channel	Units	
V _{DSS}	Drain-Source Voltage	30	-30	V	
V _{GSS}	Gate-Source Voltage	±20	±20	V	
I _D	Maximum Drain Current - Continuous	7.0	-5.2	A	
	- Pulsed (Note 3)	20	-20		
P _D	Maximum Power Dissipation	2000			
T _J	Operating Temperature Range	-55 to 150			
T _{STG}	Storage Temperature Range	-55 to 150			

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	62.5	°C/W
2006-02			

RATING CHARACTERISTIC CURVES (CHM8958JPT)

N-Channel Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
$I_{DS(0)}$	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
I_{GSSF}	Gate-Body Leakage	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1		3	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10 \text{ V}, I_D=7 \text{ A}$		22	28	$\text{m}\Omega$
		$V_{GS}=4.5 \text{ V}, I_D=6 \text{ A}$		30	40	
g_{FS}	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_D = 7 \text{ A}$		25		S

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{DS}=15 \text{ V}, I_D=5.8 \text{ A}$ $V_{GS}=10 \text{ V}$		12.3	16	nC
Q_{gs}	Gate-Source Charge			1.5		
Q_{gd}	Gate-Drain Charge			2.5		
t_{on}	Turn-On Time	$V_{DD}=15 \text{ V}$ $I_D = 1.0 \text{ A}, V_{GS} = 10 \text{ V}$ $R_{GEN}=2.7 \Omega$		9	20	nS
t_r	Rise Time			4	10	
t_{off}	Turn-Off Time			24	50	
t_f	Fall Time			4	10	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_S	Drain-Source Diode Forward Current	(Note 1)			1.3	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_S = 1.3 \text{ A}, V_{GS} = 0 \text{ V}$ (Note 2)			1.2	V

RATING CHARACTERISTIC CURVES (CHM8958JPT)

P-Channel Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}} = -24 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			-1	μA
I_{GSSF}	Gate-Body Leakage	$V_{\text{GS}} = 20\text{V}, V_{\text{DS}} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{\text{GS}} = -20\text{V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250 \mu\text{A}$	-1		-3	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}, I_D=-5\text{A}$		42	52	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_D=-4\text{A}$		60	80	
g_{FS}	Forward Transconductance	$V_{\text{DS}} = -5\text{V} , I_D = -5\text{A}$		10		S

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{\text{DS}}=-15\text{V}, I_D=-5.3\text{A}$ $V_{\text{GS}}=-10\text{V}$		11	14	nC
Q_{gs}	Gate-Source Charge			1.7		
Q_{gd}	Gate-Drain Charge			2.3		
t_{on}	Turn-On Time	$V_{\text{DD}}= -15\text{V}$ $I_D = -1.0\text{A}, V_{\text{GS}}= -10 \text{ V}$ $R_{\text{GEN}}= 6\Omega$		12	25	nS
t_r	Rise Time			5	10	
t_{off}	Turn-Off Time			57	110	
t_f	Fall Time			24	50	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_s	Drain-Source Diode Forward Current	(Note 1)			-1.3	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_s = -1.3\text{A}, V_{\text{GS}} = 0 \text{ V}$ (Note 2)			-1.2	V

RATING CHARACTERISTIC CURVES (CHM8958JPT)

N-Channel Typical Electrical Characteristics

Figure 1. Output Characteristics

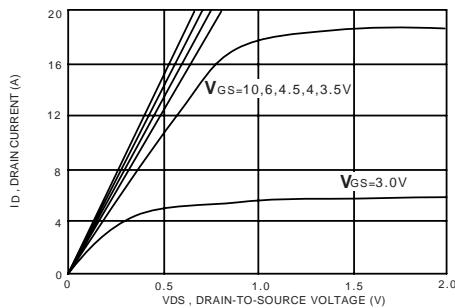


Figure 2. Transfer Characteristics

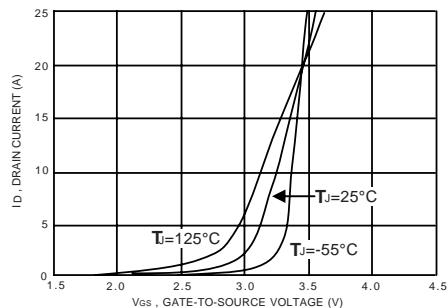


Figure 3. Gate Charge

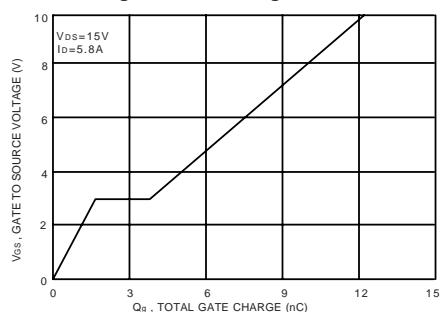


Figure 4. On-Resistance Variation with Temperature

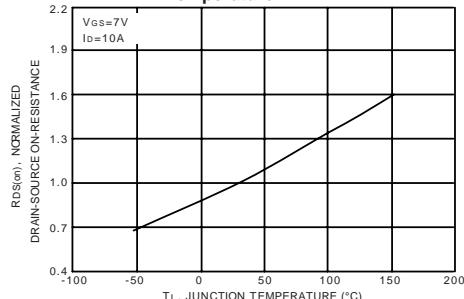
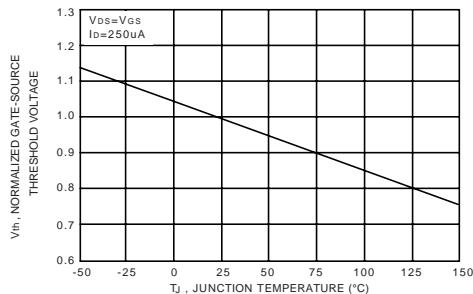


Figure 5. Gate Threshold Variation with Temperature



RATING CHARACTERISTIC CURVES (CHM8958JPT)

P-Channel Typical Electrical Characteristics

Figure 1. Output Characteristics

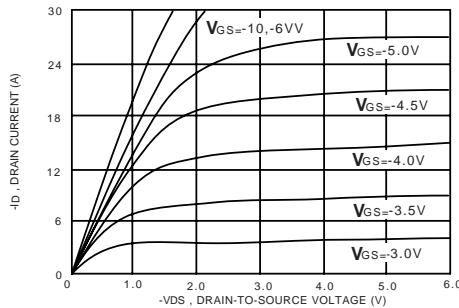


Figure 2. Transfer Characteristics

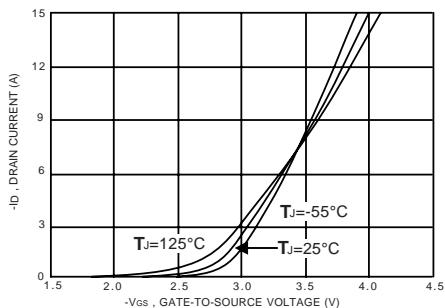


Figure 3. Gate Charge

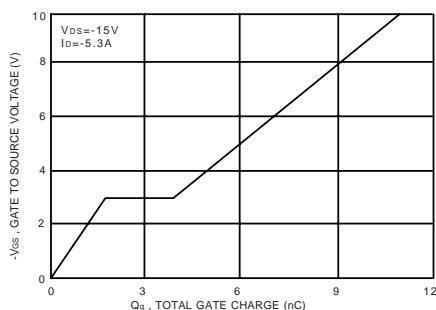


Figure 4. On-Resistance Variation with Temperature

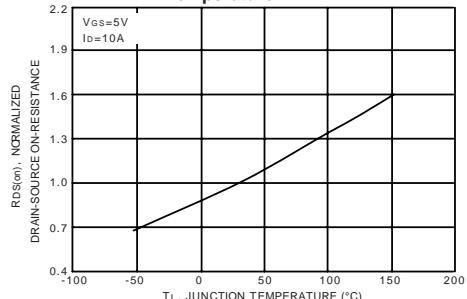


Figure 5. Gate Threshold Variation with Temperature

