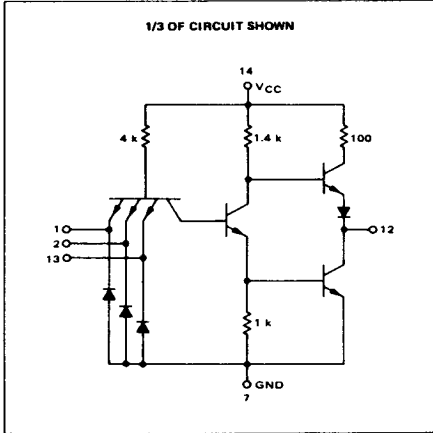


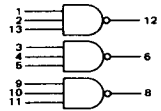
TRIPLE 3-INPUT "NAND" GATE

MTTL MC7400P series
MTTL MC5400L/7400L series

MC5410L*
MC7410P,L*



This package consists of three 3-input NAND gates. Each gate may be used as an inverter, or two gates may be cross-coupled to form bistable circuits.



Positive Logic: $12 = \overline{1 \cdot 2 \cdot 13}$
Negative Logic: $12 = \overline{1 \cdot 2 \cdot 13}$

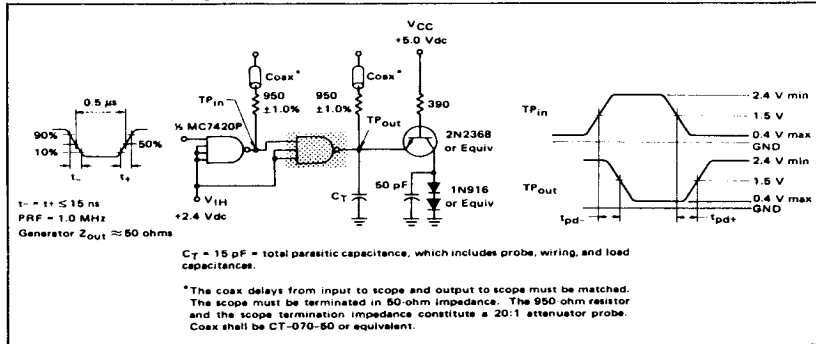
Input Loading Factor = 1
Output Loading Factor = 10

Total Power Dissipation = 30 mW typ/pkg
Propagation Delay Time = 13 ns typ

*L suffix = TO-116 ceramic package (Case 632)
P suffix = TO-116 plastic package (Case 605)
See General Information section for package outline dimensions.

SWITCHING TIME TEST CIRCUIT

VOLTAGE WAVEFORMS AND DEFINITIONS



375

375

