

Preliminary Information

256 x 8 Bit Serial EEPROM

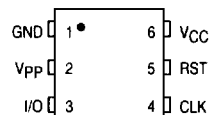
The MCM2814ATR is a 2048-bit serial electrically erasable PROM fabricated using floating gate HCMOS technology. It is designed for handling data in applications requiring both non-volatile memory and in-system information updates. The EEPROM is organized as 256 x 8 bit. The memory is protected via a scheme which uses one byte of the EEPROM to define the number of bytes in the array which are read only. The card has an M-Bus interface for serial communication. In addition, the card responds to an Answer-to-Reset condition as defined in the ISO7816 standard, during which the first four bytes of the array are transmitted.

- 2048 Bits Organized as 256 Bytes
- Byte Programmable
- Single 5 V Supply During Read and Programming Operations
- On-Chip Programming Voltage Generator
- Internally Controlled Program Time
- Automatic Byte Address Increment in Read Mode
- Two-Wire (IIC Compatible) M-Bus
- Internal Pull-Up Resistor on CLK and I/O Pins
- Answer to Reset Feature as Defined in ISO7816 Standard
- Pin Configuration as Defined in ISO7816 Standard
- Serial Interface as Defined in ISO7816 Standard
- Data Protection of Any Number of Bytes of Array Using One EEPROM Byte
- Simultaneous Programming of One to Four Bytes
- Digital Filtering on Clock and Data Inputs
- Byte Program Operation: No Byte Erase Necessary
- Data Protection After Reset
- Write/Erase Endurance: 10,000 Cycles over 0 to 70°C
- Typically 100,000 W/E Cycles at Ambient Temperatures
- Data Retention: 10 Years

MCM2814ATR

**X SUFFIX
XX PACKAGE
CASE TBD**

PIN ASSIGNMENT



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Motorola is announcing the end-of-life status of the 256Kx8 bit EEPROM (MCM2814) product family. Motorola will accept orders until March 30, 1996, and will support deliveries until September 30, 1996. There are no Motorola offerings that will directly replace these devices.

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