

## SOT-23 Plastic-Encapsulate MOSFETS

### CJ2305 P-Channel 8-V(D-S) MOSFET

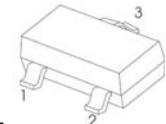
#### FEATURE

TrenchFET Power MOSFET

#### APPLICATIONS

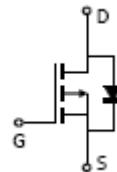
- Load Switch for Portable Devices
- DC/DC Converter

**SOT-23**



1. GATE  
2. SOURCE  
3. DRAIN

#### MARKING: S5



#### Maximum ratings ( $T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	-8	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	
Continuous Drain Current	$I_D$	-4.1	A
Continuous Source-Drain Diode Current	$I_S$	-0.8	
Maximum Power Dissipation	$P_D$	0.35	W
Thermal Resistance from Junction to Ambient( $t \leq 10\text{s}$ )	$R_{\theta JA}$	357	$^\circ\text{C/W}$
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-50 ~+150	

**Electrical characteristics ( $T_a=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
<b>Static</b>						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = -250\mu\text{A}$	-8			V
Gate-source threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = -250\mu\text{A}$	-0.5		-0.9	
Gate-source leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 8\text{V}$			$\pm 100$	nA
Zero gate voltage drain current	$I_{\text{DSS}}$	$V_{\text{DS}} = -8\text{V}, V_{\text{GS}} = 0\text{V}$			-1	$\mu\text{A}$
Drain-source on-state resistance <sup>a</sup>	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -4.5\text{V}, I_{\text{D}} = -3.5\text{A}$			0.045	$\Omega$
		$V_{\text{GS}} = -2.5\text{V}, I_{\text{D}} = -3\text{A}$			0.060	
		$V_{\text{GS}} = -1.8\text{V}, I_{\text{D}} = -2.0\text{A}$			0.090	
Forward transconductance <sup>a</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = -5\text{V}, I_{\text{D}} = -4.1\text{A}$	6			S
<b>Dynamic</b>						
Input capacitance <sup>b,c</sup>	$C_{\text{iss}}$	$V_{\text{DS}} = -4\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		740		pF
Output capacitance <sup>b,c</sup>	$C_{\text{oss}}$			290		
Reverse transfer capacitance <sup>b,c</sup>	$C_{\text{rss}}$			190		
Total gate charge <sup>b</sup>	$Q_g$	$V_{\text{DS}} = -4\text{V}, V_{\text{GS}} = -4.5\text{V}, I_{\text{D}} = -4.1\text{A}$		7.8	15	nC
Gate-source charge <sup>b</sup>	$Q_{\text{gs}}$	$V_{\text{DS}} = -4\text{V}, V_{\text{GS}} = -2.5\text{V}, I_{\text{D}} = -4.1\text{A}$		4.5	9	
Gate-drain charge <sup>b</sup>	$Q_{\text{gd}}$			1.2		
Gate resistance <sup>b,c</sup>	$R_g$	$f = 1\text{MHz}$	1.4	7	14	$\Omega$
Turn-on delay time <sup>b,c</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -4\text{V}, R_L = 1.2\Omega, I_{\text{D}} \approx -3.3\text{A}, V_{\text{GEN}} = -4.5\text{V}, R_{\text{G}} = 1\Omega$		13	20	ns
Rise time <sup>b,c</sup>	$t_r$			35	53	
Turn-off Delay time <sup>b,c</sup>	$t_{\text{d}(\text{off})}$			32	48	
Fall time <sup>b,c</sup>	$t_f$			10	20	
Turn-on delay time <sup>b,c</sup>	$t_{\text{d}(\text{on})}$			5	10	
Rise time <sup>b,c</sup>	$t_r$			11	17	
Turn-off delay time <sup>b,c</sup>	$t_{\text{d}(\text{off})}$	$V_{\text{DD}} = -4\text{V}, R_L = 1.2\Omega, I_{\text{D}} \approx -3.3\text{A}, V_{\text{GEN}} = -8\text{V}, R_{\text{G}} = 1\Omega$		22	33	
Fall time <sup>b,c</sup>	$t_f$			16	24	
<b>Drain-source body diode characteristics</b>						
Continuous source-drain diode current	$I_s$	$T_c = 25^\circ\text{C}$			-1.4	A
Pulse diode forward current <sup>a</sup>	$I_{\text{SM}}$				-10	
Body ciode voltage	$V_{\text{SD}}$	$I_F = -3.3\text{A}$		-0.8	-1.2	

**Note :**

- a. Pulse Test ; Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.
- c. These parameters have no way to verify.

