



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
813-SL160.0M-10	Low-Loss 160MHz IF SAW Filter 9.5MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response

Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



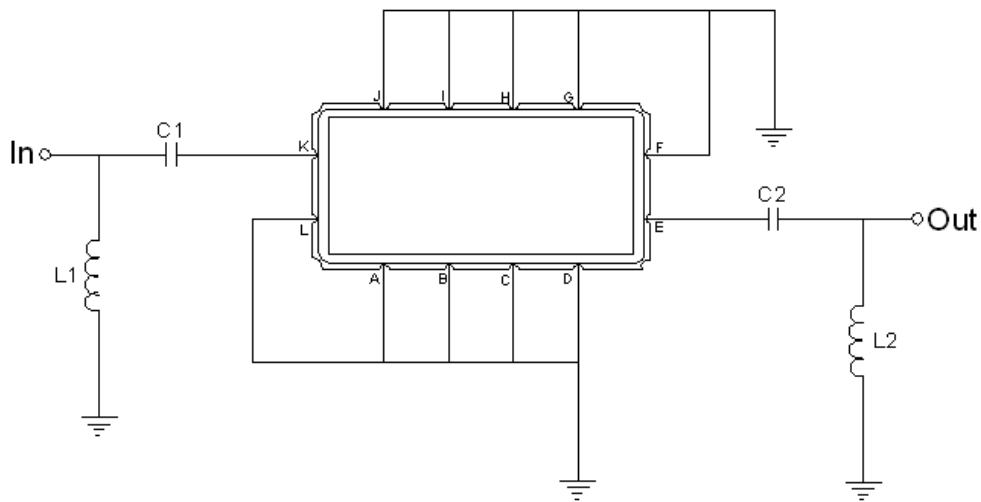


Mechanical Dimensions (mm)



Pin Description	
A, B, C, D, F, G, H, I, J, L	Ground
K	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1=27nH, Q > 40, C1=68pF
Output	L2=27nH, Q > 40, C2=43pF
Source/Load Impedance	50 Ω



Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	+80
Storage Temperature Range	°C	-40	-	+85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	-	160	-
Insertion Loss at Fo	dB	-	9.7	12.0
Amplitude Ripple Variation	dB _{p-p}	-	0.8	1.0
Group Delay Variation	nsec	-	55	150
Absolute Delay at Fo	μsec	-	0.91	-
Temperature Coefficient	ppm/°C	-	-86	-
Bandwidth at -1.0 dB	MHz	9.5	9.8	-
Bandwidth at -3.0 dB	MHz	10.3	10.6	-
Bandwidth at -40.0 dB	MHz	-	13.6	14.5
Relative Attenuation:				
Lower sidelobe	dB	40	50	-
Upper sidelobe	dB	40	43	-



Frequency Response

