MGBR30S50C Preliminary DIODE

DUAL MOS GATED BARRIER RECTIFIERS

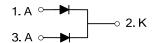
■ DESCRIPTION

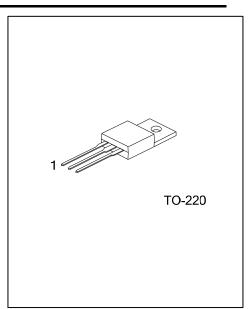
The UTC **MGBR30S50C** is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Super low forward voltage drop
- * High switching speed

■ SYMBOL

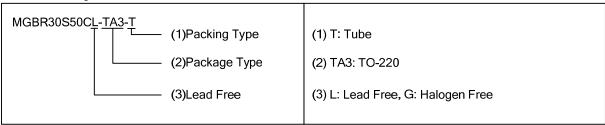




■ ORDERING INFORMATION

Ordering Number		Doolsogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR30S50CL-TA3-T	MGBR30S50CG-TA3-T	TO-220	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode, K: Cathode



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■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V_{RM}	50	V
Working Peak Reverse Voltage	V_{RWM}	50	V
Peak Repetitive Reverse Voltage	V_{RRM}	50	V
Average Rectified Output Current Per Device		15	Α
Total	Io	30	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Loa	d I _{FSM}	250	Α
Operating Junction Temperature	T_J	-65~+150	°C
Storage Temperature	T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	2	°C/W

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.50mA	50			V
Faculty Markets Date	V _{FM}	I _F =15A, T _J =25°C			0.50	V
Forward Voltage Drop		I _F =15A, T _J =125°C			0.45	V
Landana Commant (Nata 4)	I _{RM}	V _R =50V, T _J =25°C			500	μA
Leakage Current (Note 1)		V _R =50V, T _J =125°C			100	mΑ

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.

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