MGBR30L50C

DUAL MOS GATED BARRIER RECTIFIER

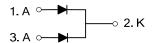
■ DESCRIPTION

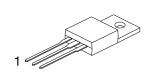
The UTC **MGBR30L50C** is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed

■ SYMBOL



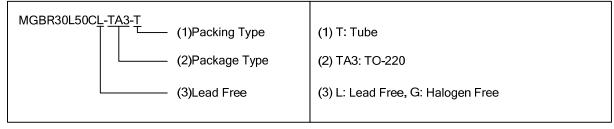


TO-220

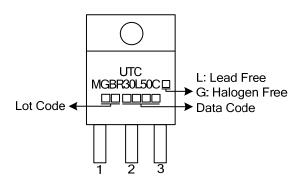
■ ORDERING INFORMATION

Ordering Number		Doolsogo	Pin Assignment			Packing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR30L50CL-TA3-T	MGBR30L50CG-TA3-T	TO-220	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode, K: Cathode



■ MARKING INFORMATION



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MGBR30L50C

■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	50	V
Working Peak Reverse Voltage		V_{RWM}	50	V
Peak Repetitive Reverse Voltage		V_{RRM}	50	V
Average Destified Output Current Der Device	Per Leg		15	Α
Average Rectified Output Current Per Device	Total	I _O	30	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM}	200	Α
Operating Junction Temperature		T_J	-65~+150	Ŝ
Storage Temperature		T_{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	2	°C/W

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A =25°C unless otherwise specified.)

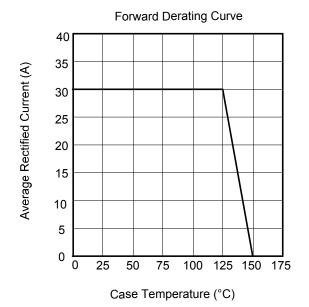
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.50mA	50			V
Forward Voltage Drop	V_{FM}	I _F =15A, T _J =25°C			0.60	V
		I _F =15A, T _J =125°C			0.55	V
Leakage Current (Note 1)	I PM	V _R =50V, T _J =25°C			300	μΑ
		V _R =50V, T _J =125°C			100	mΑ

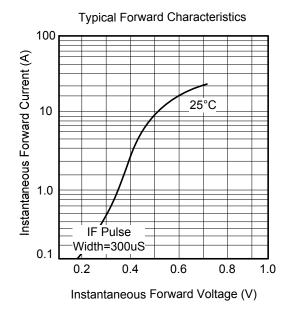
Notes: 1. Short duration pulse test used to minimize self-heating effect.

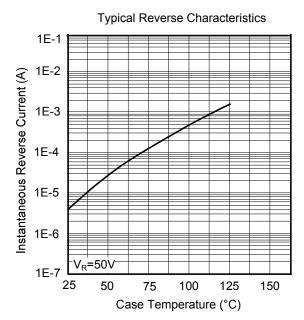
^{2.} Thermal resistance junction to case mounted on heatsink.

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■ TYPICAL CHARACTERISTICS







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