

## 4 Watt DC-DC Converters

## IMX 4 Series

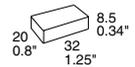
**Input voltage ranges up to 121 V DC**  
**1 or 2 outputs up to 48 V DC**  
**Up to 2000 V DC I/O electric strength test**

- Extremely wide input voltage ranges
- Electrical isolation, single and dual outputs
- Immunity to IEC/EN 61000-4-2, -3, -4, -5 and -6
- High efficiency (typ. 82%)
- Flex power: flexible load distribution
- No load and short-circuit proof
- High reliability and no derating
- Operating ambient temperature  $-40\dots+85^{\circ}\text{C}$
- Industrial, alternative and SMD pinout
- DIL 24 case with 8.5 mm profile

Safety according to IEC/EN 60950, UL 1950



<sup>1</sup> For 70 IMX 4 types



### Summary

The IMX 4 series of board mountable 4 Watt DC-DC converters has been designed according to the latest industry requirements and standards. The converters are particularly suitable for use in mobile or stationary applications in transport, industry or telecommunications where variable input voltages or high transient voltages are prevalent.

Covering a total input voltage range from 4.7 V DC up to 121 V DC with four different types the units are available with single and dual outputs from 3.3 up to  $\pm 24$  V DC with flexible load distribution on dual outputs. Features include efficient input and output filtering with unsurpassed transient and surge protection, low output ripple and noise, consistently high efficiency over the entire input voltage range, high reliability as well as excellent dynamic response to load and line changes.

The converters are designed and built according to the international safety standards IEC/EN 60950, UL 1950, CAN/CSA C22.2 No.950-95 and are LGA and UL marked. 70 IMX 4 types have supplementary insulation and are CE marked.

A special feature is their small case size, DIL 24 with only 8.5 mm profile. The circuit comprises integrated planar magnetics and all components are automatically assembled and solidly soldered onto a single PCB without any wire connections. Thanks to the rigid mechanical design the units withstand an extremely high level of shock and vibrations. Careful considerations of possible thermal stresses ensure the absence of hot spots providing long life in environments where temperature cycles are a reality. The thermal design allows operation at full load up to an ambient temperature of  $85^{\circ}\text{C}$  in free air without using any potting material.

Various options as e.g. extended temperature range  $-40\dots+85^{\circ}\text{C}$ , SMD pinout or K-pinout, an alternative to the standard industrial pinout, provide a high level of application specific engineering and design-in flexibility.

### Table of Contents

	Page	Page	
Summary .....	1	Electromagnetic Compatibility (EMC) .....	8
Type Survey and Key Data .....	2	Mechanical Data .....	10
Type Key .....	2	Immunity to Environmental Conditions .....	11
Functional Description .....	3	Safety and Installation Instructions .....	12
Electrical Input Data .....	4	Description of Options .....	14
Electrical Output Data .....	6		

## Type Survey and Key Data

Table 1: Type survey

Output 1		Output 2		Output Power	Input voltage range	Efficiency	Type designation	Options
$U_{o1\ nom}$ [V DC]	$I_{o1\ nom}$ [A] <sup>1</sup>	$U_{o2\ nom}$ [V DC]	$I_{o2\ nom}$ [A] <sup>1</sup>	$P_{o\ nom}$ [W]	[V DC]	$\eta_{typ}$ [%]		
3.3	800	-	-	2.6	4.7...16.8	78	5 IMX 4-03-9	-8, L, Z
3.3	900	-	-	3.0	8.4...36	79	20 IMX 4-03-9	-8, M, L, Z
3.3	900	-	-	3.0	16.8...75	80	40 IMX 4-03-9	-8, M, L, Z
3.3	900	-	-	3.0	40...121	80	70 IMX 4-03-9	-8, L, Z
5	700	-	-	3.5	4.7...16.8	82	5 IMX 4-05-9	-8, L, Z
5	700	-	-	3.5	8.4...36	81	20 IMX 4-05-9	-8, M, K, L, Z
5	700	-	-	3.5	16.8...75	81	40 IMX 4-05-9	-8, M, K, L, Z
5	700	-	-	3.5	40...121	81	70 IMX 4-05-9	-8, L, Z
12	340	-	-	4.1	4.7...16.8	82	5 IMX 4-12-9	-8, L, Z
12	340	-	-	4.1	8.4...36	82	20 IMX 4-12-9	-8, M, K, L, Z
12	340	-	-	4.1	16.8...75	82	40 IMX 4-12-9	-8, M, K, L, Z
12	340	-	-	4.1	40...121	82	70 IMX 4-12-9	-8, L, Z
15	280	-	-	4.2	4.7...16.8	82	5 IMX 4-15-9	-8, L, Z
15	280	-	-	4.2	8.4...36	82	20 IMX 4-15-9	-8, M, K, L, Z
15	280	-	-	4.2	16.8...75	82	40 IMX 4-15-9	-8, M, K, L, Z
15	280	-	-	4.2	40...121	82	70 IMX 4-15-9	-8, L, Z
+5	350	-5	350	3.5	8.4...36	81	20 IMX 4-0505-9	-8, M, K, L, Z
+5	350	-5	350	3.5	16.8...75	81	40 IMX 4-0505-9	-8, M, K, L, Z
+5	350	-5	350	3.5	40...121	81	70 IMX 4-0505-9	-8, L, Z
+12	170	-12	170	4.1	8.4...36	82	20 IMX 4-1212-9	-8, M, K, L, Z
+12	170	-12	170	4.1	16.8...75	82	40 IMX 4-1212-9	-8, M, K, L, Z
+15	140	-15	140	4.2	8.4...36	82	20 IMX 4-1515-9	-8, M, K, L, Z
+15	140	-15	140	4.2	16.8...75	82	40 IMX 4-1515-9	-8, M, K, L, Z
+24	80	-24	80	3.8	8.4...36	83	20 IMX 4-2424-9	-8, M, L, Z
+24	80	-24	80	3.8	16.8...75	83	40 IMX 4-2424-9	-8, M, L, Z

<sup>1</sup> Flexible load distribution on double outputs possible.

## Type Key

Input voltage range $U_i$		20 IMX 4 - 05 05 -9 K M L Z
4.7...16.8 V DC .....	5	
8.4...36 V DC .....	20	
16.8...75 V DC .....	40	
40...121 V DC .....	70	
Series .....	IMX 4	
Output voltage type for output 1 .....	03, 05, 12, 15, 24	
Output voltage type for output 2 .....	05, 12, 15, 24	
Operating ambient temperature range $T_A$		
-40...71 °C .....	-9	
-40...85 °C (option) .....	-8	
Options:		
Alternative pinout .....	K <sup>1</sup>	
Surface mount version with pins .....	M <sup>1</sup>	
Surface mount version with PCB lid .....	L	
Open frame .....	Z	

<sup>1</sup> Option M excludes option K and vice versa

Examples: 40 IMX 4-0505-8K: DC-DC converter, input voltage range 16.8...75 V, 2 outputs providing  $\pm 5$  V, 350 mA, temperature range -40...85 °C, alternative pinout.

20 IMX 4-12-9M: DC-DC converter, input voltage range 8.4...36 V, SMD-pinout, one output providing 12 V, 340 mA. Temperature range -40...71 °C.

**Functional Description**

The IMX 4 DC-DC modules are feedback controlled flyback converters using current mode PWM (Pulse Width Modulation).

The converter input is protected against transients by means of a suppressor diode.

The output voltage is monitored by a separate transformer winding close to the secondary windings and fed back to the control circuit.

Current limitation is provided by the primary circuit, thus limiting the total output current ( $I_{o\ nom}$  for the single and  $I_{o1\ nom} + I_{o2\ nom}$  for the dual output types).

The close magnetic coupling provided by the planar construction ensures very good regulation and allows for flexible load distribution on dual output types.

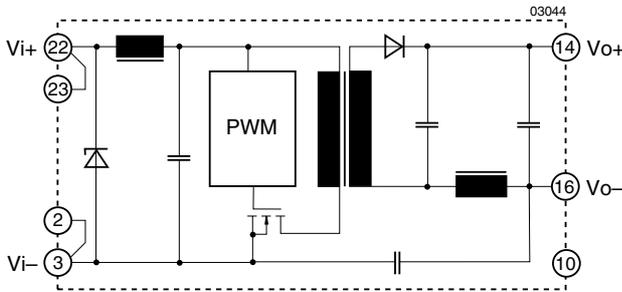


Fig. 1  
Block diagram for single output types.  
Standard industrial pinout.

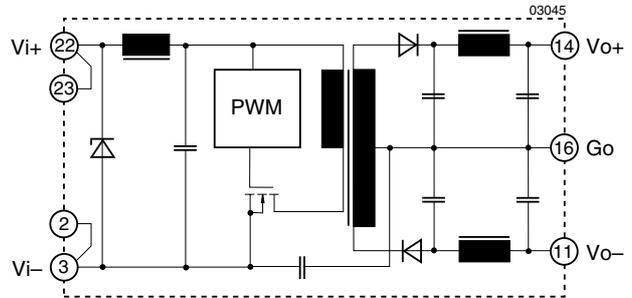


Fig. 2  
Block diagram for dual output types.  
Standard industrial pinout.

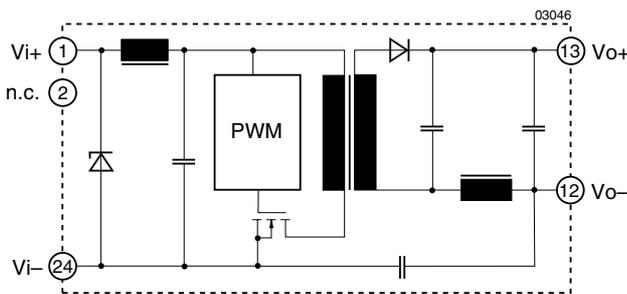


Fig. 3  
Block diagram for single output types.  
Special pinout (option K).

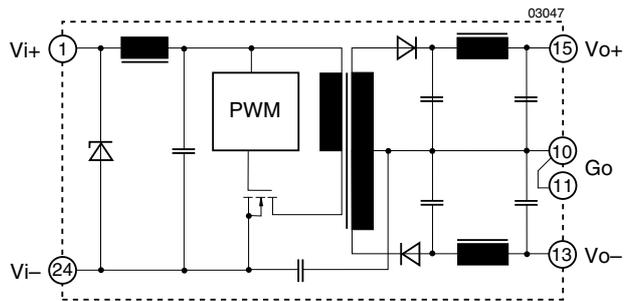


Fig. 4  
Block diagram for dual output types.  
Special pinout (option K).

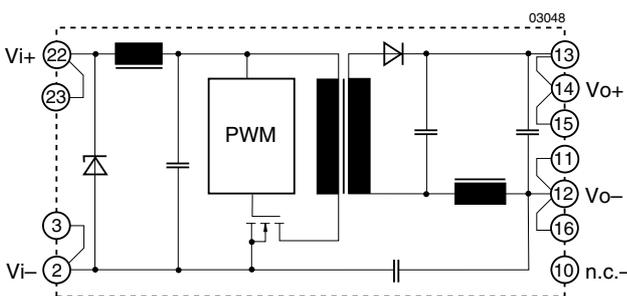


Fig. 5  
Block diagram for single output types.  
SMD pinout (option M and L).

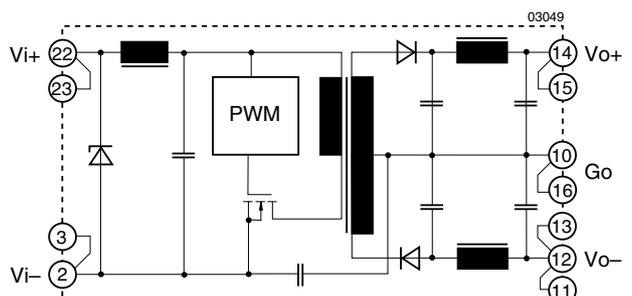


Fig. 6  
Block diagram for dual output types.  
SMD pinout (option M and L).

## Electrical Input Data

General conditions:  $T_A = 25^\circ\text{C}$ , unless  $T_C$  is specified.

Table 2: Input Data

Input			5 IMX 4			20 IMX 4			40 IMX 4			70 IMX 4			Unit
Characteristics		Conditions	min	typ	max	min	typ	max	min	typ	max	min	typ	max	
$U_i$	Input voltage range	$T_C \text{ min} \dots T_C \text{ max}$	4.7	16.8		8.4	36		16.8 <sup>4</sup>	75		40	121	V DC	
$U_{i \text{ nom}}$	Nominal input voltage	$I_o = 0 \dots I_o \text{ nom}$	5			20			40			70			
$U_{i \text{ sur}}$	Repetitive surge voltage	abs. max input (3 s)	20			40			100			150			
$t_{\text{start up}}$	Converter start-up time <sup>1</sup>	Worst case condition at $U_{i \text{ min}}$ and full load	0.25	0.5		0.25	0.5		0.25	0.5		0.25	0.5	s	
$t_{\text{rise}}$	Rise time <sup>1</sup>	$U_{i \text{ nom}}$ resistive load	5			5			5			5			ms
		$I_o \text{ nom}$ capacitive load	12			12			12			12			
$I_{i \text{ o}}$	No load input current	$I_o = 0, U_{i \text{ min}} \dots U_{i \text{ max}}$	50	100		15	20		5	10		5	10	mA	
$C_i$	Input capacitance	for surge calculation	10			0.54			0.3			0.15			$\mu\text{F}$
$I_{\text{inr p}}$	Inrush peak current	$U_i = U_{i \text{ nom}}^3$	1.4			3.7			4.2			5.6			A
$f_s$	Switching frequency	$U_{i \text{ min}} \dots U_{i \text{ max}}, I_o = 0 \dots I_o \text{ nom}$	approx. 400			approx. 400			approx. 400			approx. 400			kHz
$I_{i \text{ rr}}$	Reflected ripple current	$I_o = 0 \dots I_o \text{ nom}$	60			100			60			30			$\text{mA}_{\text{pp}}$
$U_{i \text{ RFI}}$	Input RFI level conducted and radiated	EN 55022 <sup>2</sup>	B			B <sup>1</sup>			B <sup>1</sup>			A			

<sup>1</sup> Measured with a resistive or max. admissible capacitive load. (See fig.: Converter start-up and rise time)

<sup>2</sup> External filter required. (See: Filter recommendations for compliance with EN 55022)

<sup>3</sup> Source impedance according to prETS 300132-2, version 4.3.

<sup>4</sup> Operation at lower input voltage possible:  $P_o$  approx. 80% of  $P_{o \text{ nom}}$  at  $U_{i \text{ min}} = 14.4 \text{ V}$

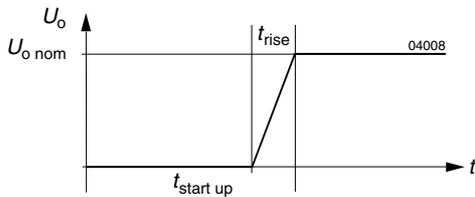


Fig. 7  
Converter start-up and rise time

### Inrush Current

The inrush current has been kept as low as possible by choosing a very small input capacitance. A series resistor may be inserted in the input line to limit this current further.

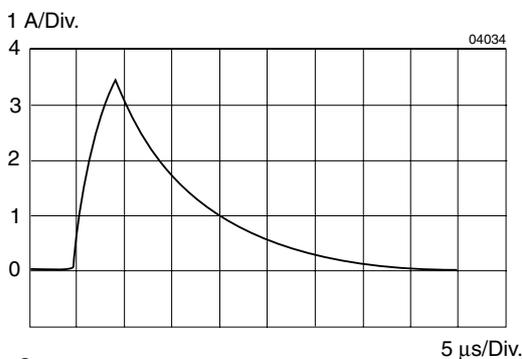


Fig. 8  
Typical inrush current at  $U_{i \text{ nom}}, P_{o \text{ nom}}$  versus time (40 IMX 4) measured according to prETS 300132-2, version 4.3.

### Reverse Polarity Protection at Input

The suppressor diode on the input also provides for reverse polarity protection by conducting current in the reverse direction, thus protecting the unit. An external fuse is required to limit this current:

- For 5 IMX 4 a fast 2 A (F2A) fuse is recommended
- For 20 IMX 4 a fast 1 A (F1A) fuse is recommended
- For 40 IMX 4 a fast 0.63 A (F0.63A) fuse is recommended
- For 70 IMX 4 a fast 0.35 A (F035A) fuse is recommended

### Filter recommendations for compliance with EN 55022

Electromagnetic emission requirements according to table Input data can be achieved by adding an external capacitor as close as possible to the input terminals.

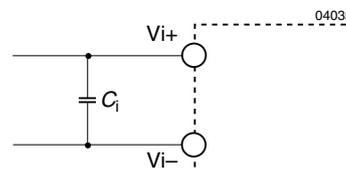


Fig. 9  
Input filter arrangement

Table 3: Input filter components (EN 55022)

Ref.	20 IMX 4	40 IMX 4	70 IMX 4
$C_1$	4.7 $\mu\text{F}$ , 63 V, 85 $^\circ\text{C}$	2.2 $\mu\text{F}$ , 100 V, 85 $^\circ\text{C}$	2.2 $\mu\text{F}$ , 150 V, 85 $^\circ\text{C}$
Type	ceramic or film		

### Input Transient Voltage Protection

In many applications transient voltages on the input of the converter are always a possibility. These may be caused for example by short circuits between  $V_{i+}$  and  $V_{i-}$  where the network inductance may cause high energy pulses.

In order to protect the converter a large transient voltage suppressor has been fitted to the input of the IMX 4 types. Specifications as on table below.

Table 4: Built-in transient voltage suppressor

Type	Breakdown Voltage $V_{BR\ nom}$	Peak Power at 1 ms $P_P$	Peak Pulse Current $I_{PP}$
20 IMX 4	40 V	600 W	10.3 A
40 IMX 4	100 V	600 W	4.1 A
70 IMX 4	150 V	600 W	2.9 A

If transients generating currents above the peak pulse current  $I_{PP}$  are possible then an external limiting network such as the circuit recommended for IEC/EN 61000-4-5 Level 2 compliance is necessary.

To achieve IEC/EN 61000-4-5, level 2 compliance an additional transzorb, inductor and capacitor should be provided externally as shown in the figure below. The components should have similar characteristics as listed in table below.

Table 5: Components for external circuitry (IEC/EN 61000-4-5)

Circuit Ref.	20 IMX 4	40 IMX 4	70 IMX 4
L	330 $\mu$ H, 0.42 $\Omega$ , 0.6 A TDK TSL1110-331KR55	330 $\mu$ H, 0.42 $\Omega$ , 0.6 A TDK TSL1110-331KR55	330 $\mu$ H, 0.65 $\Omega$ , 0.62 A Toko, 494LXF-0098K
C	68 $\mu$ F, 63 V, 85°C	68 $\mu$ F, 100 V, 85°C	100 $\mu$ F, 150 V, 85°C
D	Motorola 1.5KE 39 A	Motorola 1.5KE 82 A	–

To withstand the 150 V transient according to 19 Pfl 1, applicable for 40 IMX 4 types, the same external circuitry with similar components as for IEC/EN 61000-4-5, level 2 compliance can be used.

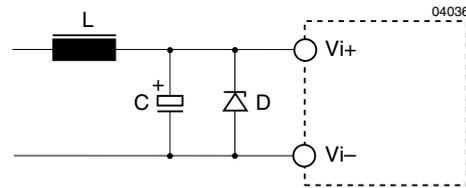


Fig. 10  
Example for external circuitry to comply with IEC/EN 61000-4-5, level 2.

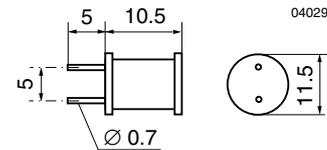


Fig. 11  
Dimensions for inductor L

## Electrical Output Data

General conditions:  $T_A = 25^\circ\text{C}$ , unless  $T_C$  is specified.

Table 6a: Output data for single output units

Output		$U_{o\text{ nom}}$	3.3 V		5.0 V		12.0 V		15.0 V		Unit
Characteristics		Conditions	min	typ	max	min	typ	max	min	typ	
$U_o$	Output voltage	$U_{i\text{ nom}}, I_o = 0.5 I_{o\text{ nom}}$	3.27	3.33	4.96	5.04	11.90	12.10	14.88	15.12	VDC
$I_{o\text{ nom}}$	Output current	$U_{i\text{ min}} \dots U_{i\text{ max}}$	900 <sup>9</sup>		700		340		280		mA
$I_{o\text{ L}}$	Current limit <sup>2</sup>	$U_{i\text{ nom}}, T_C = 25^\circ\text{C}$	1800		1400		680		560		
$\Delta U_{o\text{ U}}$	Line regulation	$U_{i\text{ min}} \dots U_{i\text{ max}}, I_{o\text{ nom}}$	$\pm 1$		$\pm 1$		$\pm 1$		$\pm 1$		%
$\Delta U$	Load regulation	$U_{i\text{ nom}}, I_o = (0.1 \dots 1) I_{o\text{ nom}}$	$\pm 3.5$		$\pm 3$		$\pm 3$		$\pm 3$		
$u_{o1,2}$	Output voltage noise	$U_{i\text{ min}} \dots U_{i\text{ max}}$ <sup>5</sup>	80		80		120		150		mV <sub>pp</sub>
		$I_o = I_{o\text{ nom}}$ <sup>6</sup>	20	40	20	40	40	60	50	75	
$U_{o\text{ clp}}$	Output overvoltage limitation	Min. load 1%	130		130		130		130		%
$C_{o\text{ ext}}$	Admissible capacitive load <sup>3</sup>		$\leq 680$		$\leq 680$		$\leq 150$		$\leq 100$		$\mu\text{F}$
$u_{o\text{ d}}$	Dynamic load regulat.	Voltage deviat.	$\pm 250$		$\pm 250$		$\pm 250$		$\pm 250$		mV
$t_d$		Recovery time	$I_{o\text{ nom}} \leftrightarrow 1/2 I_{o\text{ nom}}$	1		1		1		1	
$\alpha_{U_o}$	Temperature coefficient $\Delta U_o / \Delta T_C$	$U_{i\text{ min}} \dots U_{i\text{ max}}, I_o = 0 \dots I_{o\text{ nom}}$	$\pm 0.02$		$\pm 0.02$		$\pm 0.02$		$\pm 0.02$		%/K

Table 6b: Output data for dual output units

Output		$U_{o\text{ nom}}$	$\pm 5\text{ V}$ <sup>8</sup>		$\pm 12\text{ V}$ <sup>7,8</sup>		$\pm 15\text{ V}$ <sup>7,8</sup>		$\pm 24\text{ V}$ <sup>7,8</sup>		Unit
Characteristics		Conditions	min	typ	max	min	typ	max	min	typ	
$U_{o1}$ $U_{o2}$	Output voltage	$U_{i\text{ nom}}$	4.96	5.04	11.90	12.10	14.88	15.12	23.81	24.19	VDC
		$I_{o1} = I_{o2} = 0.5 I_{o\text{ nom}}$	4.95	5.05	11.88	12.12	14.85	15.15	23.75	24.25	
$I_{o\text{ nom}}$	Output current <sup>1</sup>	$U_{i\text{ min}} \dots U_{i\text{ max}}$	$2 \times 350$		$2 \times 170$		$2 \times 140$		$2 \times 80$		mA
$P_{o\text{ L}}$	Power limit <sup>2,3</sup>	$U_{i\text{ nom}}, T_C = 25^\circ\text{C}$	$2 \times 3.5$		$2 \times 4.0$		$2 \times 4.2$		$2 \times 4.0$		
$\Delta U_{o\text{ U}}$	Line regulation	$U_{i\text{ min}} \dots U_{i\text{ max}}, I_{o\text{ nom}}$	$\pm 1$		$\pm 1$		$\pm 1$		$\pm 1$		%
$\Delta U$	Load regulation <sup>4</sup>	$U_{i\text{ nom}}, I_o = (0.1 \dots 1) I_{o\text{ nom}}$	$\pm 3$		$\pm 3.5$		$\pm 3$		$\pm 3$		
$u_{o1,2}$	Output voltage noise	$U_{i\text{ min}} \dots U_{i\text{ max}}$ <sup>5</sup>	100		140		150		240		mV <sub>pp</sub>
		$I_o = I_{o\text{ nom}}$ <sup>6</sup>	40	60	45	70	50	75	40	120	
$U_{o\text{ clp}}$	Output overvoltage limitation	Min. load 1%	130		130		130		130		%
$C_{o\text{ ext}}$	Admissible capacitive load <sup>3</sup>		$\leq 680$		$\leq 150$		$\leq 100$		$\leq 45$		$\mu\text{F}$
$u_{o\text{ d}}$	Dynamic load regulat.	Voltage deviat.	$\pm 250$		$\pm 600$		$\pm 750$		$\pm 750$		mV
$t_d$		Recovery time	$I_{o\text{ nom}} \leftrightarrow 1/2 I_{o\text{ nom}}$	1		1		1		1	
$\alpha_{U_o}$	Temperature coefficient $\Delta U_o / \Delta T_C$	$U_{i\text{ min}} \dots U_{i\text{ max}}, I_o = 0 \dots I_{o\text{ nom}}$	$\pm 0.02$		$\pm 0.02$		$\pm 0.02$		$\pm 0.02$		%/K

<sup>1</sup> Each output capable of delivering full output power.

<sup>2</sup> The current limit is primary side controlled.

<sup>3</sup> Sum of both outputs.

<sup>4</sup> Conditions for specified output. Other output loaded with constant current  $I_o = 0.5 I_{o\text{ nom}}$ .

<sup>5</sup> BW = 20 MHz

<sup>6</sup> Measured with a probe according to EN 61204.

<sup>7</sup> Not available for 70 IMX 4 types.

<sup>8</sup> Not available for 5 IMX 4 types.

<sup>9</sup> 5 IMX 4 have reduced output current, see table *Type survey*

### Thermal Considerations

If a converter, mounted on a PCB, is located in free, quasi-stationary air (convection cooling) at the indicated maximum ambient temperature  $T_{A \max}$  (see table: *Temperature specifications*) and is operated at its nominal input voltage and output power, the case temperature  $T_C$  measured at the: *Measuring point of case temperature  $T_C$*  (see: *Mechanical Data*) will approach the indicated value  $T_{C \max}$  after the warm-up phase. However, the relationship between  $T_A$  and  $T_C$  depends heavily on the conditions of operation and integration into a system. The thermal conditions are influenced by input voltage, output current, airflow, temperature of surrounding components and surfaces and the properties of the printed circuit board.  $T_{A \max}$  is therefore only an indicative value and under practical operating conditions, the ambient temperature  $T_A$  may be higher or lower than this value.

**Caution:** The case temperature  $T_C$  measured at the *Measuring point of case temperature  $T_C$*  (see: *Mechanical Data*) may under no circumstances exceed the specified maximum value. The installer must ensure that under all operating conditions  $T_C$  remains within the limits stated in the table: *Temperature specifications*.

### Short Circuit Behaviour

The current limit characteristic shuts down the converter whenever a short circuit is applied to its output. It acts self-protecting and automatically recovers after removal of the overload condition.

### Typical Performance Curves

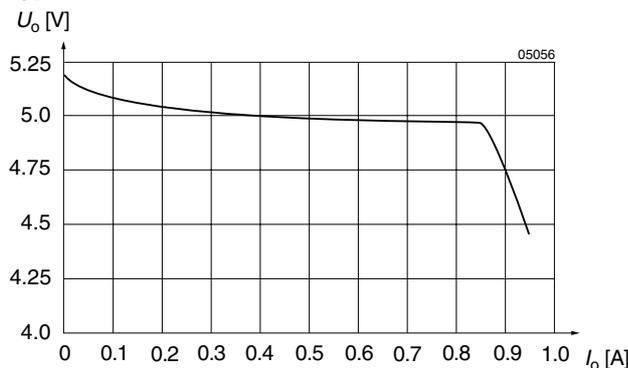


Fig. 12  
 $U_o$  versus  $I_o$  (typ) of single output units (example for 20/40 IMX 4-05)

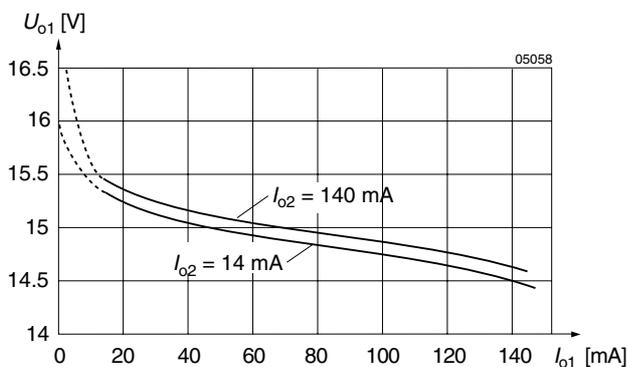


Fig. 14  
Cross load regulation of dual output units.  $U_{o1}$  versus  $I_{o1}$  (typ) for various  $I_{o2}$  (40 IMX 4-1515).

### Output overvoltage protection

The outputs of the IMX 4 converters are protected against overvoltages by Zener diodes. In the event of an overvoltage on the output, the unit will shut-down and attempt to restart automatically. The main purpose of this feature is to protect against possible overvoltages which could occur due to a failure in the feedback control circuit. The units are not designed to withstand external overvoltages applied to the outputs.

### Connection in series

The outputs of single or dual output units can be connected in series without any precautions, taking into consideration that the highest output voltage should remain below 60 V for SELV operation.

### Connection in parallel

The outputs of several units with equal nominal output voltage can be connected in parallel. Approximate current sharing between 2 or several units is ensured by their load dependent output characteristic.

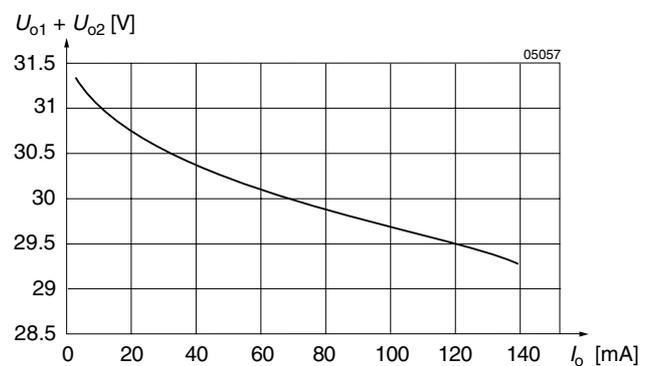


Fig. 13  
 $U_o$  versus  $I_o$  (typ) of dual output units ( $\pm 15$  V), with 30 V load connected to  $Vo+$  and  $Vo-$ .

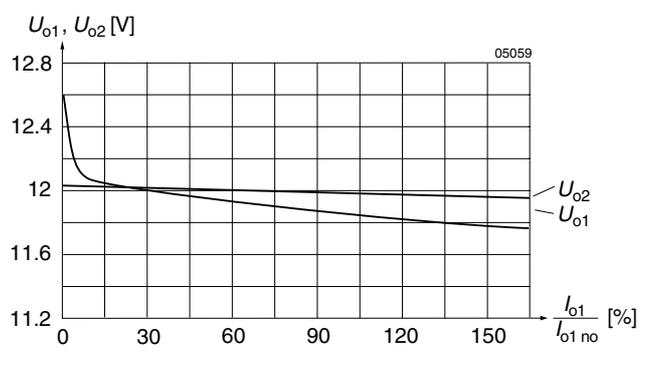


Fig. 15  
Flexible load distribution on dual outputs ( $2 \times 12$  V) with load variation from 0...150% of  $P_{o1 \text{ nom}}$  on output 1. Output 2 loaded with 25% of  $P_{o2 \text{ nom}}$ .

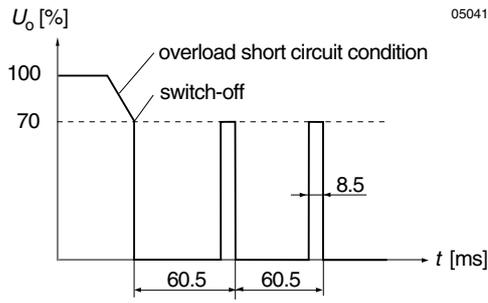


Fig. 16  
 Overload switch-off (hiccup mode).  
 Frequency of pulses: 16.5 Hz, puls duration: 8.5 ms.

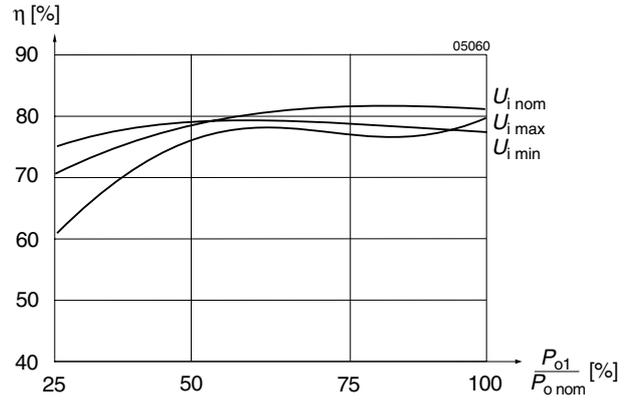


Fig. 17  
 Efficiency versus input voltage and load.  
 Typical values (40 IMX 4-12-12).

## Electromagnetic Compatibility (EMC)

### Electromagnetic Immunity

Table 7: Immunity type tests

Phenomenon	Standard <sup>1</sup>	Class Level	Coupling mode <sup>2</sup>	Value applied	Waveform	Source Imped.	Test procedure	In oper.	Per-form. <sup>3</sup>
Electrostatic discharge to case	IEC/EN 61000-4-2	2	contact discharge	4000 V <sub>p</sub>	1/50 ns	330 Ω	10 positive and 10 negative discharges	yes	B
		3	air discharge	8000 V <sub>p</sub>					
Electromagnetic field	IEC/EN 61000-4-3	3	antenna	10 V/m	AM 80% 1 kHz		26...1000 MHz	yes	A
	ENV 50204				PM, 50% duty cycle, 200 Hz resp. frequ.		300 MHz		
Electrical fast transient/burst	IEC/EN 61000-4-4 <sup>6</sup>	4	+i/-i	4000 V <sub>p</sub>	bursts of 5/50 ns 5 kHz rep. rate transients with 15 ms burst duration and a 300 ms period	50 Ω	1 min positive 1 min negative bursts per coupling mode	yes	B
Surge	IEC/EN 61000-4-5 <sup>5 6</sup>	2	+i/-i	1000 V <sub>p</sub>	1.2/50 μs	2 Ω	5 pos. and 5 neg. surges per coupling mode	yes	A
Conducted disturbances	IEC/EN 61000-4-6	3	+i/-i	3 V <sub>rms</sub> (140 dBμV)	AM 80% 1 kHz	50 Ω	0.15...80 MHz 150 Ω	yes	A
Transient	19 Pfl 1 <sup>4 6</sup>		+i/-i	150 V <sub>p</sub>	0.1/0.3 ms	limited to <100 A	3 positive 5 repetitions	yes	A

<sup>1</sup> Related and previous standards are referenced in: *Technical Information: Standards.*

<sup>2</sup> i = input, o = output

<sup>3</sup> A = normal operation, no deviation from specification, B = temporary deviation from specs possible.

<sup>4</sup> For 40 IMX 4 types (additional external components required). Not applicable for 20 IMX 4 types

<sup>5</sup> External components required

<sup>6</sup> Not applicable for 5 IMX 4 types.

### Electromagnetic Emission

Conducted RFI noise at input according to EN 55022

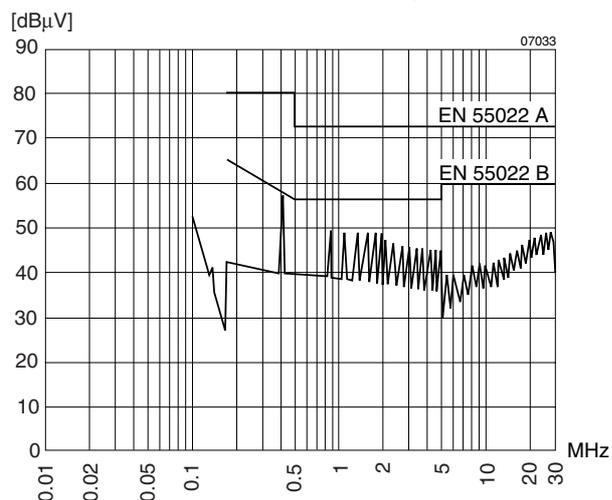


Fig. 18

Typical disturbance voltage (quasi-peak) at the input according to CISPR 11/EN 55011 and CISPR 22/EN 55022, measured at  $U_{i\text{nom}}$  and  $I_{o\text{nom}}$ . Output leads 0.1 m, twisted. External capacitor at the input required (see: Recommendations for compliance with EN 55022) (40 IMX 4-1515).

Radiated RFI noise according to EN 55022.

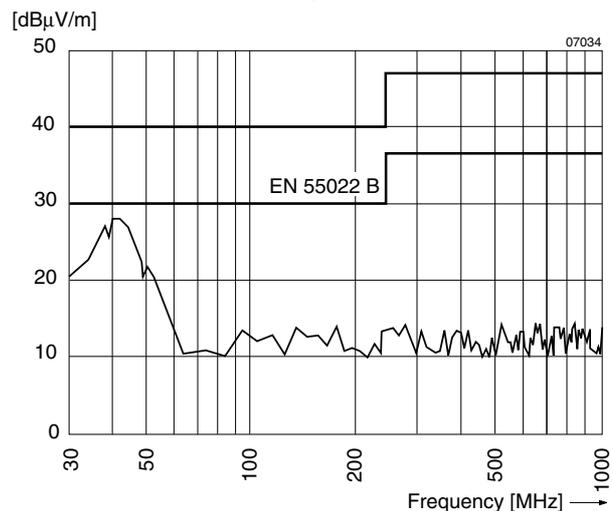


Fig. 19

Typical radio frequency interference voltage at  $U_{i\text{nom}}$ ,  $I_{o\text{nom}}$ , measured with an antenna (distance 10 m). Output leads 0.1 m, twisted (40 IMX 4-1515).

**Mechanical Data**

Dimensions in mm. Tolerances  $\pm 0.3$  mm unless otherwise indicated.

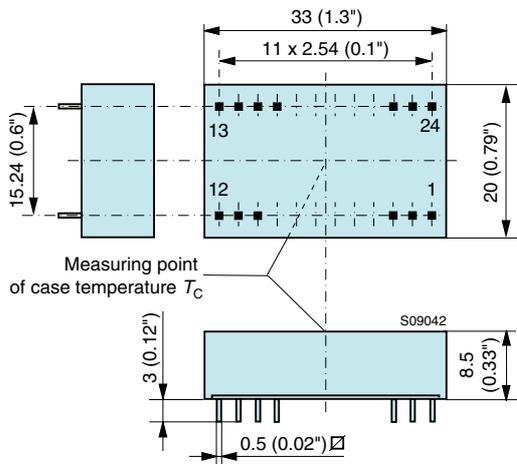


Fig. 20  
Standard or alternative pinout  
Weight: <10 g

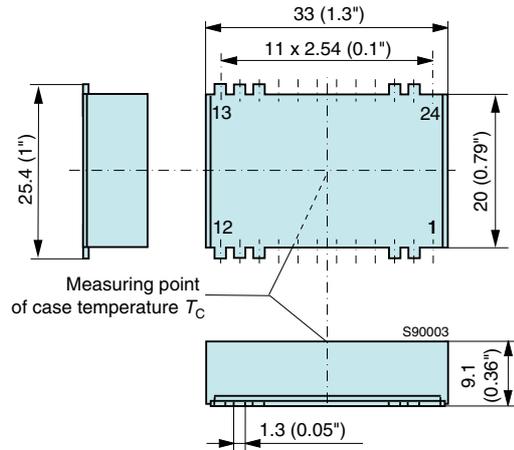


Fig. 21  
Surface mount version with PCB lid (Option L)  
Weight: <10 g

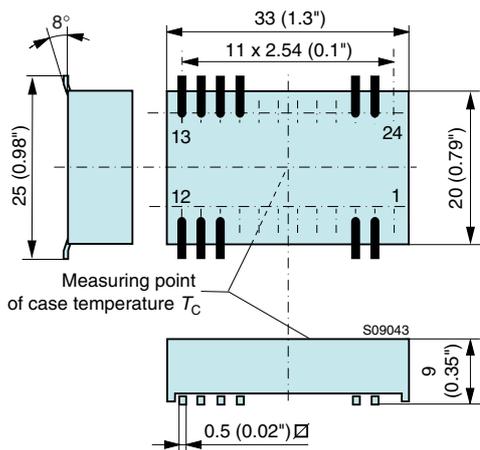


Fig. 22  
Surface mount version with pins (Option M)  
Weight: <10 g

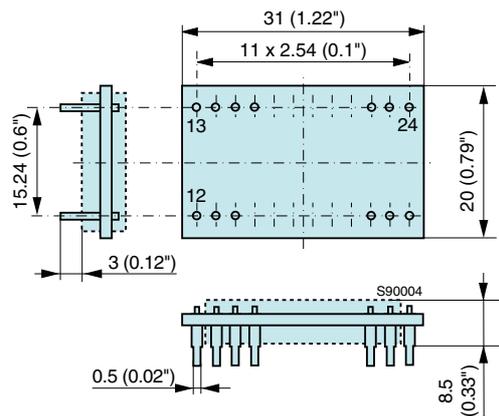


Fig. 23  
Open frame (Option Z)  
Weight: <10 g

## Immunity to Environmental Conditions

Table 8: Environmental testing

Test Method		Standard	Test Conditions		Status
Ca	Damp heat steady state	IEC/DIN IEC 60068-2-3 MIL-STD-810D section 507.2	Temperature: Relative humidity: Duration:	40 ±2 °C 93 +2/-3 % 56 days	Unit not operating
Ea	Shock (half-sinusoidal)	IEC/EN/DIN EN 60068-2-27 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	100 g <sub>n</sub> = 981 m/s <sup>2</sup> 6 ms 18 (3 each direction)	Unit operating
Eb	Bump (half-sinusoidal)	IEC/EN/DIN EN 60068-2-29 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	40 g <sub>n</sub> = 392 m/s <sup>2</sup> 6 ms 6000 (1000 each direction)	Unit operating
Fc	Vibration (sinusoidal)	IEC/EN/DIN EN 60068-2-6	Acceleration amplitude: Frequency (1 Oct/min): Test duration:	0.35 mm (10...60 Hz) 5 g <sub>n</sub> = 49 m/s <sup>2</sup> (60...2000 Hz) 10...2000 Hz 7.5 h (2.5 h each axis)	Unit operating
Fh	Vibration, broad-band random (digital control)	IEC/EN 60068-2-64 DIN 40046 part 23 MIL-STD-810D section 514.3	Acceleration spectral density: Frequency band: Acceleration magnitude: Test duration:	0.05 g <sub>n</sub> <sup>2</sup> /Hz 10...500 Hz 4.9 g <sub>n rms</sub> 3 h (1 h each axis)	Unit operating
Kb	Salt mist, cyclic (sodium chloride NaCl solution)	IEC/EN/DIN IEC 60068-2-52	Concentration: Duration: Storage: Storage duration: Number of cycles:	5% (30 °C) 2 h per cycle 40 °C, 93% rel. humidity 22 h per cycle 3	Unit not operating

Table 9: Temperature specifications, valid for air pressure of 800...1200 hPa (800...1200 mbar)

Temperature			Standard -9		Option -8		Unit
Characteristics	Conditions	min	max	min	max		
T <sub>A</sub>	Ambient temperature <sup>1</sup>	Operational <sup>2</sup>	-40	71	-40	85	°C
T <sub>C</sub>	Case temperature		-40	95	-40	105	
T <sub>S</sub>	Storage temperature <sup>1</sup>	Non operational	-40	100	-55	105	

<sup>1</sup> MIL-STD-810D section 501.2 and 502.2

<sup>2</sup> See *Thermal Considerations*

Table 10: MTBF and device hours

MTBF	Ground Benign	Ground Fixed		Ground Mobile
MTBF acc. to MIL-HDBK-217F	T <sub>C</sub> = 40 °C	T <sub>C</sub> = 40 °C	T <sub>C</sub> = 70 °C	T <sub>C</sub> = 50 °C
40 IMX 4-05-7	2'651'000 h	349'000 h	124'000 h	119'000 h

## Safety and Installation Instructions

### Installation Instruction

Installation of the DC-DC converters must strictly follow the national safety regulations in compliance with the enclosure, mounting, creepage, clearance, casualty, markings and segregation requirements of the end-use application.

Connection to the system shall be made via a printed circuit board according to: *Mechanical Data*.

The units should be connected to a secondary circuit.

Check for hazardous voltages before altering any connections.

Do not open the module.

Ensure that a unit failure (e.g. by an internal short-circuit) does not result in a hazardous condition. See also: *Safety of operator accessible output circuit*.

Table 11: Pin allocation for standard industrial pinout and option Z

Pin	Single output units	Dual output units
2	Vi-	Vi-
3	Vi-	Vi-
10	n.c.	-
11	-	Vo-
14	Vo+	Vo+
16	Vo-	Go
22	Vi+	Vi+
23	Vi+	Vi+

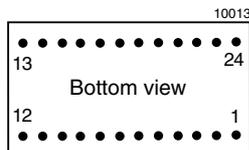


Fig. 24  
Pin numbering

### Input Fuse

To prevent excessive current flowing through the input supply line in case of a short-circuit across the converter input an external fuse should be installed in a non earthed input supply line. We recommend a fast acting fuse F2A for 5 IMX 4, F1A for 20 IMX 4, F0.5A for 40 IMX 4 and F0.315A for 70 IMX 4 types.

### Safety of operator accessible output circuits

If the output circuit of a DC-DC converter is operator accessible, it shall be an SELV circuit according to IEC/EN 60950 related safety standards

The insulation concept table below shows some possible installation configurations, compliance with which causes the output circuit of the DC-DC converter to be an SELV circuit according to IEC/EN 60950 up to a configured output voltage (sum of nominal voltages if in series or +/- configuration) of 46 V.

However, it is the sole responsibility of the installer to ensure the compliance with the relevant and applicable safety regulations. More information is given in: *Technical Information: Safety*.

Table 12: Pin allocation for K pinout (option K) and option Z

Pin	Single output units	Dual output units
1	Vi+	Vi+
2	n.c.	-
10	-	Go
11	-	Go
12	Vo-	-
13	Vo+	Vo-
15	-	Vo+
24	Vi-	Vi-

Table 13: Pin allocation for SMD pinout (option M and L)

Pin	Single output units	Dual output units
2	Vi-	Vi-
3	Vi-	Vi-
10	n.c.	Go
11	Vo-	Vo-
12	Vo-	Vo-
13	Vo+	Vo-
14	Vo+	Vo+
15	Vo+	Vo+
16	Vo-	Go
22	Vi+	Vi+
23	Vi+	Vi+

### Standards and approvals

All DC-DC converters are UL recognized according to UL 1950, UL recognized for Canada to CAN/CSA C22.2 No. 950-95 and LGA approved to IEC/EN 60950 standards.

The units have been evaluated for:

- Building in
- Supplementary insulation input to output, based on their maximum input voltage
- The use in a pollution degree 2 environment
- Connecting the input to a secondary circuit which is subject to a maximum transient rating of 1000 V for 5 IMX 4, 1500 V for 20 IMX 4 and 40 IMX 4, 2000 V for 70 IMX 4 types.

The DC-DC converters are subject to manufacturing surveillance in accordance with the above mentioned UL, CSA, EN and ISO 9001 standards.

### Cleaning Agents

In order to avoid possible damage, any penetration of cleaning fluids has to be prevented, since the power supplies are not hermetically sealed.

### Protection Degree

The protection degree of the DC-DC converters is IP 30.

### Isolation

The electric strength test is performed as factory test in accordance with IEC/EN 60950 and UL 1950 and should not be repeated in the field. Power-One will not honour any guarantee claims resulting from electric strength field tests.

Table 14: Electric strength test voltages

Characteristic	Input - Output			Unit
	5 IMX 4	20/40 IMX 4	70 IMX 4	
Electric strength test voltage 1 s	0.7	1.2	1.5	kV <sub>rms</sub>
	1	1.5	2.0	kV DC
Coupling capacitance		≈1.2	≈1.2	nF
Insulation resist. at 500 V DC	>100	>100	>100	MΩ
Partial discharge extinction voltage	Consult factory			kV

Table 15: Insulation concept leading to an SELV output circuit

Conditions	Front end			DC-DC converter	Result
Supply voltage	Minimum required grade of isolation, to be provided by the AC-DC front end, including mains supplied battery charger	Maximum DC output voltage from the front end <sup>1</sup>	Minimum required safety status of the front end output circuit	Measures to achieve the specified safety status of the output circuit	Safety status of the DC-DC converter output circuit
Mains ≤250 V AC	Basic	≤60 V	Earthed SELV circuit <sup>2</sup>	Operational insulation, provided by the DC-DC converter	SELV circuit
		>60 V	Hazardous voltage secondary circuit	Input fuse <sup>3</sup> output suppressor diodes <sup>4</sup> , and earthed output circuit <sup>2</sup>	Earthed SELV circuit
	Double or reinforced	≤60 V	SELV circuit	Operational insulation, provided by the DC-DC converter	SELV circuit
		>60 V	TNV-2 circuit Double or reinforced insulated unearthed hazardous voltage secondary circuit <sup>5</sup>	Basic insulation, based on the maximum input voltage, provided by the DC-DC converter <sup>6</sup>	

<sup>1</sup> The front end output voltage should match the specified input voltage range of the DC-DC converter.

<sup>2</sup> The earth connection has to be provided by the installer according to the relevant safety standard, e.g. IEC/EN 60950.

<sup>3</sup> The installer shall provide an approved fuse (type with the lowest rating suitable for the application) in a non-earthed input line directly at the input of the DC-DC converter (see fig.: *Schematic safety concept*). For UL's purpose, the fuse needs to be UL-listed. See also: *Input Fuse*.

<sup>4</sup> Each suppressor diode should be dimensioned in such a way, that in the case of an insulation fault the diode is able to limit the output voltage to SELV (<60 V) until the input fuse blows (see fig.: *Schematic safety concept*).

<sup>5</sup> Has to be insulated from earth by double or reinforced insulation according to the relevant safety standard, based on the maximum output voltage from the front end.

<sup>6</sup> Only 70 IMX 4. The other types provide only operational insulation.

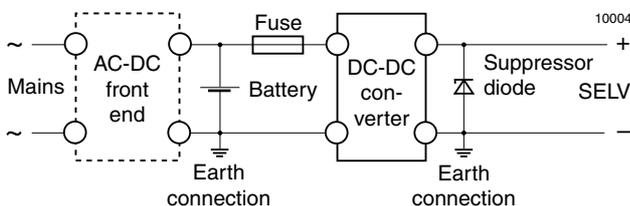


Fig. 25  
*Schematic safety concept. Use fuse, suppressor diode and earth connection as per table: Safety concept leading to an SELV output circuit.*

### Description of Options

Table 16: Survey of options

Option	Function of option	Characteristic
-8	Extended operational ambient temperature range	$T_A = -40...85^{\circ}\text{C}$
M	Surface mount version with pins	Not available for 5 IMX 4 and 70 IMX 4 types
L	Surface mount version with PCB lid	-
K	Alternative pinout	Not available for 5 IMX 4 and 70 IMX 4 types as well as all types with 3.3 V or $\pm 24$ V outputs
Z	Open frame	All models are available without case

#### Option -8

Extension of the temperature range from standard  $-40...71^{\circ}\text{C}$  to  $-40...85^{\circ}\text{C}$ . The modules will provide full output power with free air convection cooling.

This option defines an alternative pinout, i.e. compatible with IPS 3 series.

Option K excludes option M and vice versa.

#### Option M and L Surface mount version.

**Note:** Precautions should be taken when reflow soldering the SMD version. The reflow soldering instructions below should be strictly adhered to. An inadequate soldering process may permanently damage the converter or degrade its performance and Power-One will not honour any guarantee/warranty claims resulting from damage caused by ignoring the soldering instructions.

Infrared soldering is not permitted.

The surface mountable version of this product is assembled with high melting point solder ( $227^{\circ}\text{C}$ ) to ensure that the solder joints of the internal components do not degrade in the end users SMD soldering process.

This product is only specified for "Forced Convection Reflow Soldering" (Hot Air). Any conventional soldering profile is acceptable provided that the restriction curve below is not exceeded at any time during the reflow process.

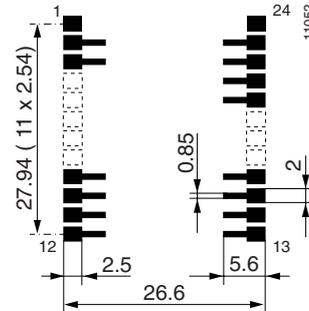


Fig. 27  
Proposed solder lands for option M and L.

#### Option K Alternative pinout.

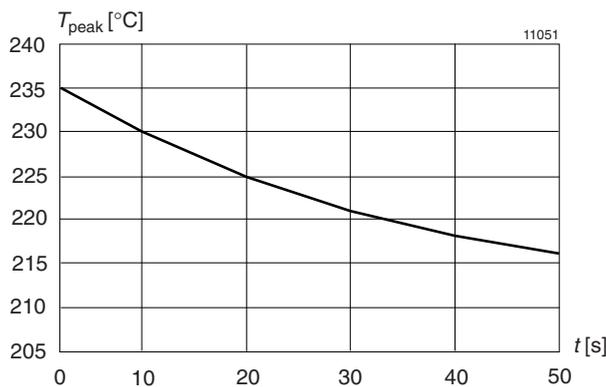


Fig. 26  
Forced convection reflow soldering restriction curve measured on pin 2