N-Channel 650-V (D-S) MOSFET

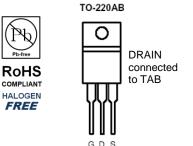
Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

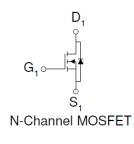
Typical Applications:

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I _D (A)	
650	800 @ V _{GS} = 10V	12 ^a	
030	850 @ V _{GS} = 6V	12	



Top View



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage		V_{DS}	650	V		
Gate-Source Voltage		V_{GS}	±20	i v		
Continuous Drain Current a	T _C =25°C	I _D	12	Α		
Pulsed Drain Current ^b		I _{DM}	50	^		
Continuous Source Current (Diode Conduction) a			40	Α		
Power Dissipation	T _C =25°C	P_{D}	300	W		
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	°C		

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	1	C/VV

1

Notes

- a. Calculated continuous current based on maximum allowable junction temperature.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

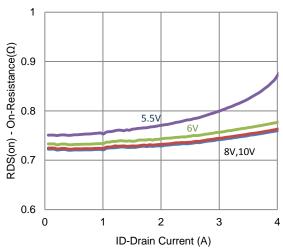
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	1	$V_{DS} = 520 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
	I _{DSS}	$V_{DS} = 520 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
Drain-Source On-Resistance ^a	r	$V_{GS} = 10 \text{ V}, I_{D} = 2 \text{ A}$			800	mΩ	
	r _{DS(on)}	$V_{GS} = 6 \text{ V}, I_D = 1.6 \text{ A}$			850		
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 2 \text{ A}$		58		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		0.9		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = 100 \text{ V}, V_{GS} = 6 \text{ V},$ $I_{D} = 2 \text{ A}$		23		nC	
Gate-Source Charge	Q_{gs}			11			
Gate-Drain Charge	Q_gd			11			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 100 \text{ V}, R_{I} = 50 \Omega,$		24		ns	
Rise Time	t _r	$V_{DS} = 100 \text{ V}, \text{ NL} = 30 \Omega,$ $I_{D} = 2 \text{ A},$		10			
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		60			
Fall Time	t _f			11			
Input Capacitance	C_{iss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		2691		pF	
Output Capacitance	C _{oss}			165			
Reverse Transfer Capacitance	C_{rss}			33			

Notes

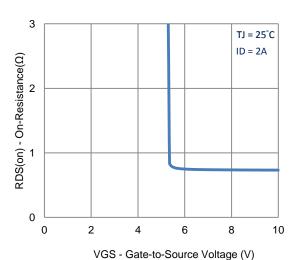
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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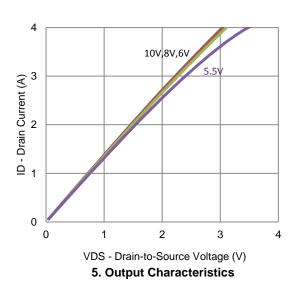
Typical Electrical Characteristics

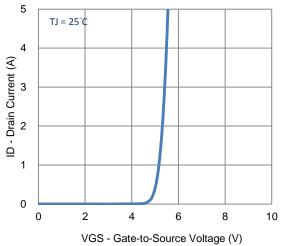


1. On-Resistance vs. Drain Current

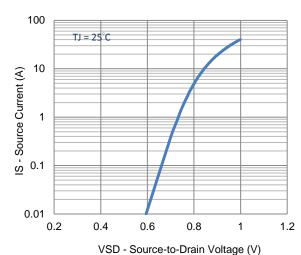


3. On-Resistance vs. Gate-to-Source Voltage

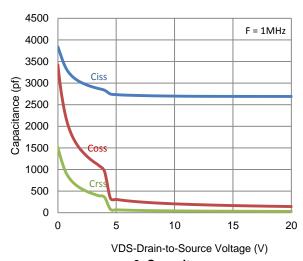




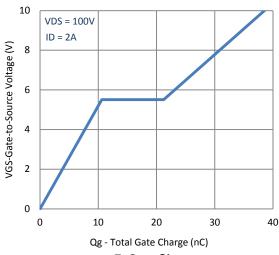
2. Transfer Characteristics



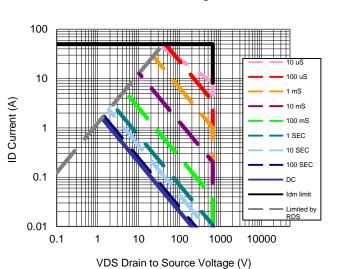
4. Drain-to-Source Forward Voltage



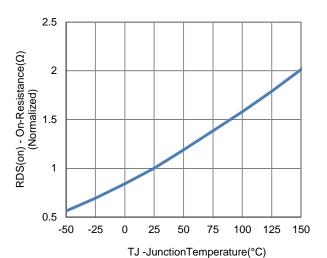
Typical Electrical Characteristics



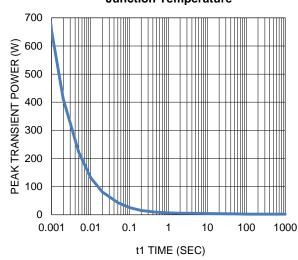
7. Gate Charge



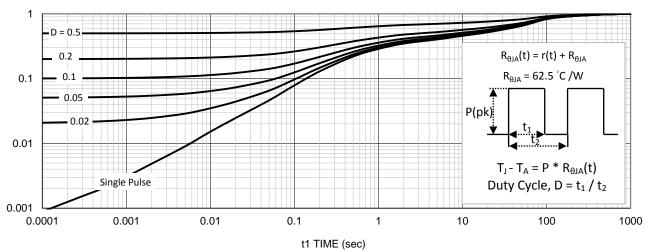
9. Safe Operating Area



8. Normalized On-Resistance Vs Junction Temperature



10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information

