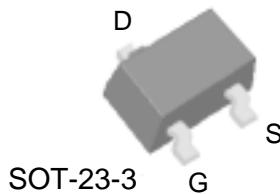


N-channel Enhancement-mode Power MOSFET

PRODUCT SUMMARY

BV _{DSS}	30V
R _{DS(ON)}	42mΩ
I _D	4.7A

 Pb-free; RoHS-compliant SOT-23-3



DESCRIPTION

The SSM2316GN achieves fast switching performance with low gate charge without a complex drive circuit. It is suitable for low voltage applications such as DC/DC converters and general load-switching circuits.

The SSM2316GN is supplied in an RoHS-compliant SOT-23-3 package, which is widely used for lower power commercial and industrial surface mount applications.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Units
V _{DS}	Drain-source voltage	30	V
V _{GS}	Gate-source voltage	± 20	V
I _D	Continuous drain current ³ , T _A = 25°C	4.7	A
	T _A = 70°C	3.7	A
I _{DM}	Pulsed drain current ^{1,2}	10	A
P _D	Total power dissipation ³ , T _A = 25°C	1.38	W
	Linear derating factor	0.01	W/°C
T _{STG}	Storage temperature range	-55 to 150	°C
T _J	Operating junction temperature range	-55 to 150	°C

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Units
R _{θJA}	Maximum thermal resistance, junction-ambient ³	90	°C/W

Notes:

1. Pulse width must be limited to avoid exceeding the maximum junction temperature of 150°C.
2. Pulse width <300us, duty cycle <2%.
3. Mounted on a square inch of copper pad on FR4 board ; 270°C/W when mounted on the minimum pad area required for soldering.

ELECTRICAL CHARACTERISTICS (at $T_j = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-source breakdown voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	30	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown voltage temperature coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	-	0.02	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static drain-source on-resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=4\text{A}$	-	-	42	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=2\text{A}$	-	-	72	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate threshold voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	1	-	3	V
g_{fs}	Forward transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=4\text{A}$	-	5	-	S
I_{DSS}	Drain-source leakage current	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	1	uA
		$V_{\text{DS}}=24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_j = 70^\circ\text{C}$	-	-	10	uA
I_{GSS}	Gate-source leakage current	$V_{\text{GS}}=\pm 20\text{V}$	-	-	± 100	nA
Q_g	Total gate charge ²	$I_{\text{D}}=4\text{A}$	-	5	8	nC
Q_{gs}	Gate-source charge	$V_{\text{DS}}=24\text{V}$	-	1	-	nC
Q_{gd}	Gate-drain ("Miller") charge	$V_{\text{GS}}=4.5\text{V}$	-	3	-	nC
$t_{\text{d}(\text{on})}$	Turn-on delay time ²	$V_{\text{DS}}=15\text{V}$	-	7	-	ns
t_r	Rise time	$I_{\text{D}}=1\text{A}$	-	8	-	ns
$t_{\text{d}(\text{off})}$	Turn-off delay time	$R_{\text{G}}=3.3\Omega$, $V_{\text{GS}}=10\text{V}$	-	12	-	ns
t_f	Fall time	$R_{\text{D}}=15\Omega$	-	3	-	ns
C_{iss}	Input capacitance	$V_{\text{GS}}=0\text{V}$	-	270	430	pF
C_{oss}	Output capacitance	$V_{\text{DS}}=25\text{V}$	-	70	-	pF
C_{rss}	Reverse transfer capacitance	f=1.0MHz	-	60	-	pF
R_g	Gate Resistance	f=1.0MHz	-	1.4	2.1	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward voltage ²	$I_{\text{s}}=1.2\text{A}$, $V_{\text{GS}}=0\text{V}$	-	-	1.2	V
t_{rr}	Reverse recovery time	$I_{\text{s}}=4\text{A}$, $V_{\text{GS}}=0\text{V}$,	-	14	-	ns
Q_{rr}	Reverse recovery charge	$dI/dt=100\text{A}/\mu\text{s}$	-	9	-	nC

Notes:

- 1.Pulse width must be limited to avoid exceeding the maximum junction temperature of 150°C .
- 2.Pulse width <300us, duty cycle <2%.

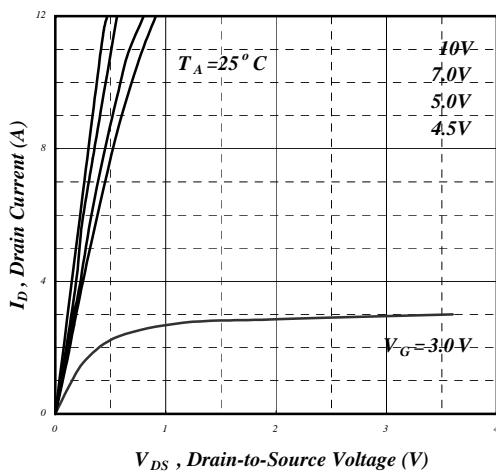


Fig 1. Typical Output Characteristics

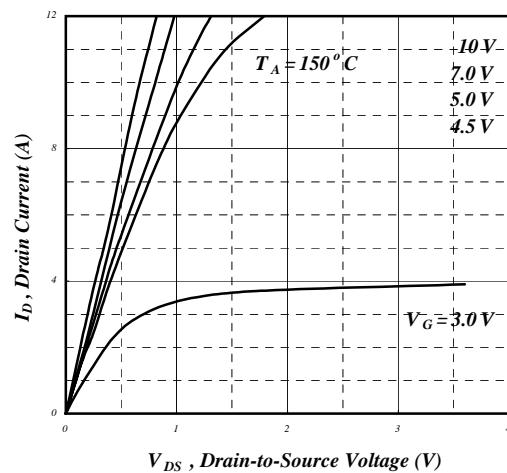


Fig 2. Typical Output Characteristics

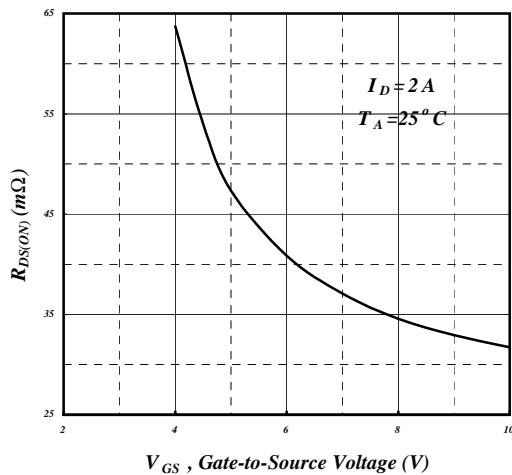


Fig 3. On-Resistance vs. Gate Voltage

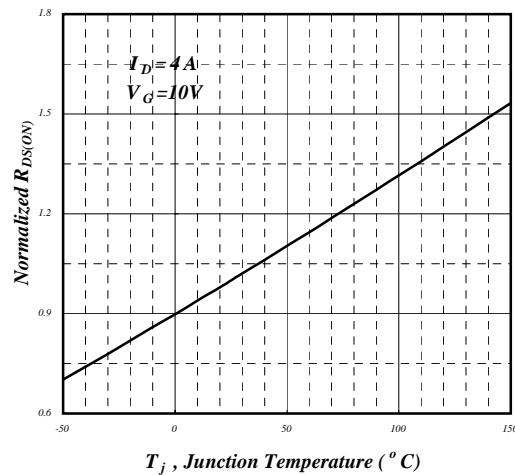


Fig 4. Normalized On-Resistance vs. Junction Temperature

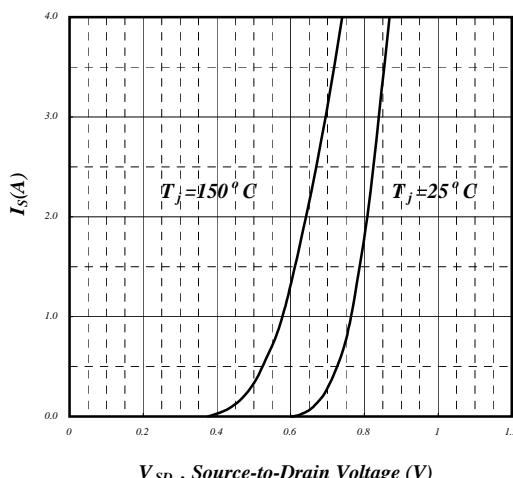


Fig 5. Forward Characteristic of Reverse Diode

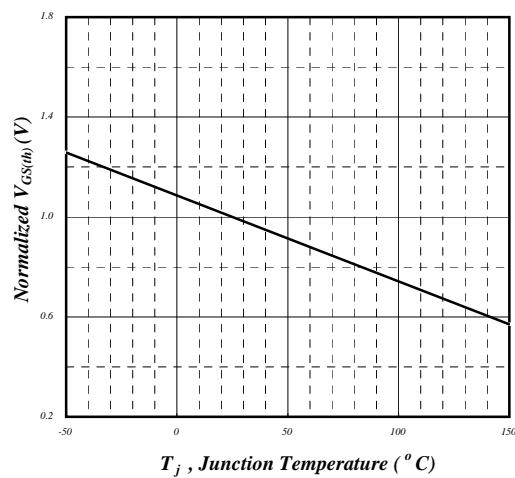
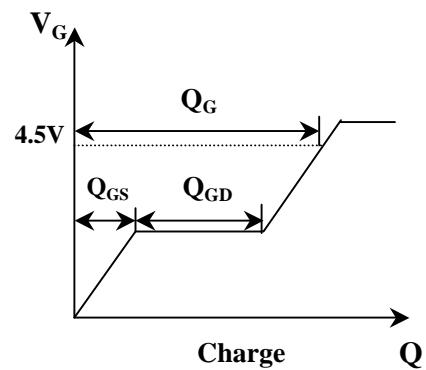
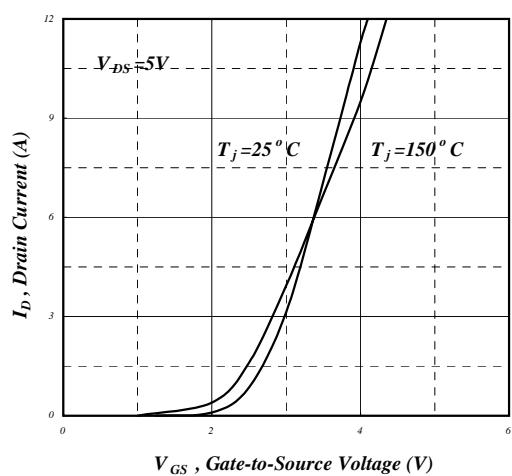
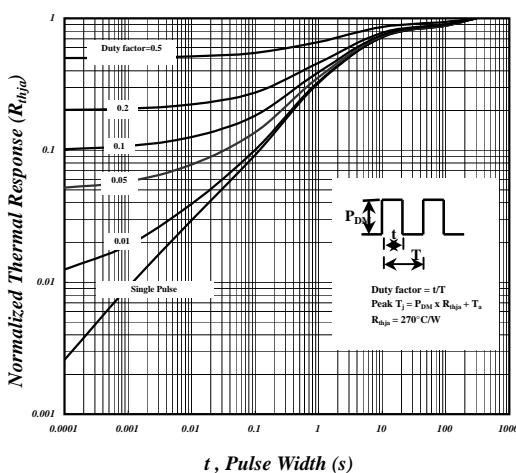
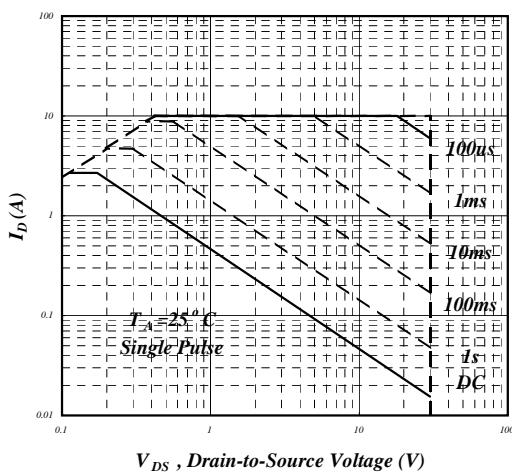
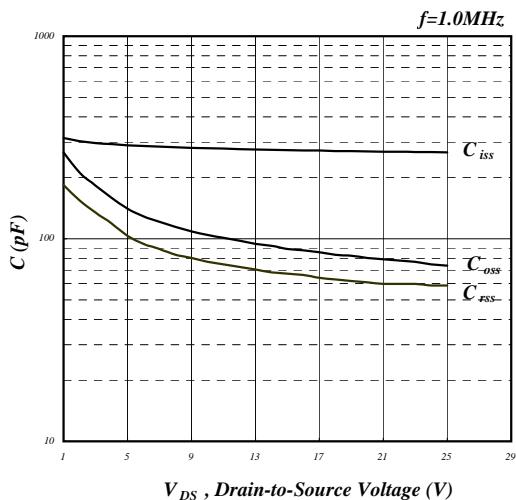
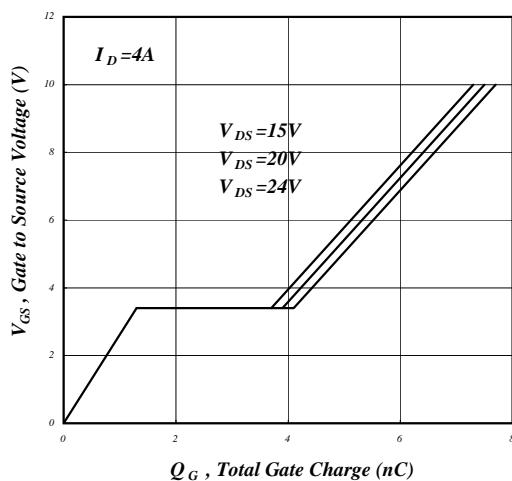
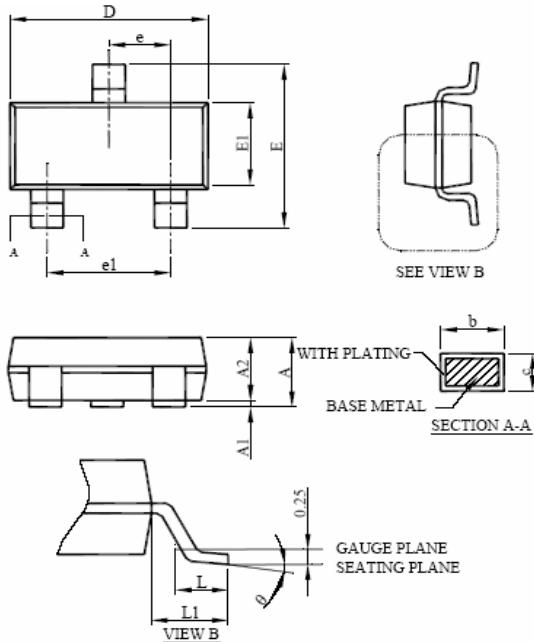


Fig 6. Gate Threshold Voltage vs. Junction Temperature



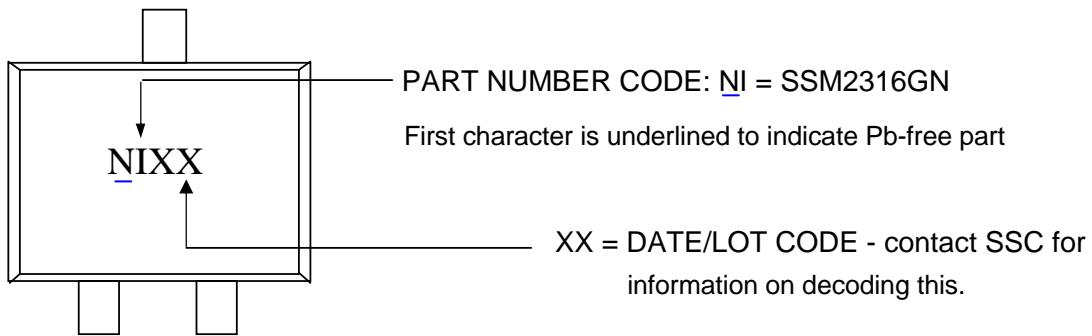
PHYSICAL DIMENSIONS

SOT-23-3


SYMBOL	SOT-23-3	
	MILLIMETERS	
	MIN.	MAX.
A	0.89	1.45
A1	0	0.15
A2	0.70	1.30
b	0.30	0.50
c	0.08	0.25
D	2.65	3.10
E	2.10	3.00
E1	1.19	2.30
e	0.95BSC	
e1	1.90BSC	
L	0.30	0.60
L1	0.60REF	
Θ	0°	8°

*Dimensions do not include mold protrusions.

PART MARKING



PACKING:

Moisture sensitivity level MSL3

3000 pcs in antistatic tape on a reel packed in a moisture barrier bag (MBB).

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