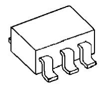
LOW DROPOUT VOLTAGE REGULATOR

■ GENERAL DESCRIPTION

NJM2874/75/76 is a low dropout voltage regulator designed for cellular phone application.

Advanced Bipolar technology achieves low noise, high ripple rejection and low quiescent current.

■ PACKAGE OUTLINE

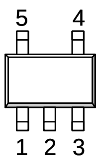


NJM2874F/75F/76F

■ FEATURES

- High Ripple Rejection 75dB typ. (f=1kHz Vo=3V Version)
- Output Noise Voltage Vno=45µVrms typ.
- Output capacitor with 1.0µF ceramic capacitor (Vo≥2.7V)
- Output Current Io(max.)=150mA
- High Precision Output Vo±1%
- Low Dropout Voltage 0.10V typ. (Io=60mA)
- ON/OFF Control (Active High)
- Internal Short Circuit Current Limit
- Internal Thermal Overload Protection
- Bipolar Technology
- Package Outline SOT-23-5

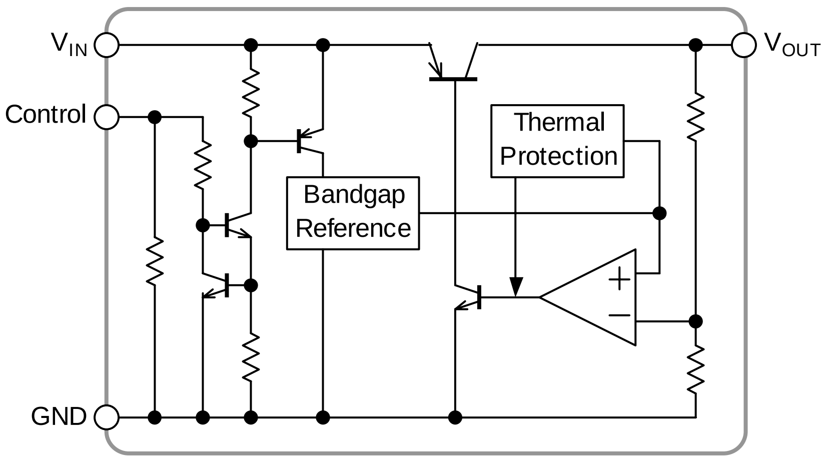
■ PIN CONFIGURATION



PIN FUNCTION

NJM2874F	NJM2875F	NJM2876F
1.CONTROL	1.V_{IN}	1.V_{OUT}
2.GND	2.GND	2.GND
3.NC	3.CONTROL	3.V_{IN}
4.V_{OUT}	4.NC	4.CONTROL
5.V_{IN}	5.V_{OUT}	5.NC

■ EQUIVALENT CIRCUIT



■ OUTPUT VOLTAGE RANK LIST

Device Name	V_{OUT}
NJM287×F21	2.1V
NJM287×F28	2.8V
NJM287×F03	3.0V
NJM287×F33	3.3V
NJM287×F05	5.0V

■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	RATINGS		UNIT
Input Voltage	V_{IN}	+14		V
Control Voltage	V_{CONT}	+14(*1)		V
Power Dissipation	P_D	SOT-23-5	350(*2)	mW
			200(*3)	
Operating Temperature	Topr	-40 ~ +85		°C
Storage Temperature	Tstg	-40 ~ +125		°C

(*1):When input voltage is less than +14V, the absolute maximum control voltage is equal to the input voltage.
(*2):Mounted on glass epoxy board. (114.3x76.2x1.6mm: 2Layer, FR-4)
(*3):Device itself

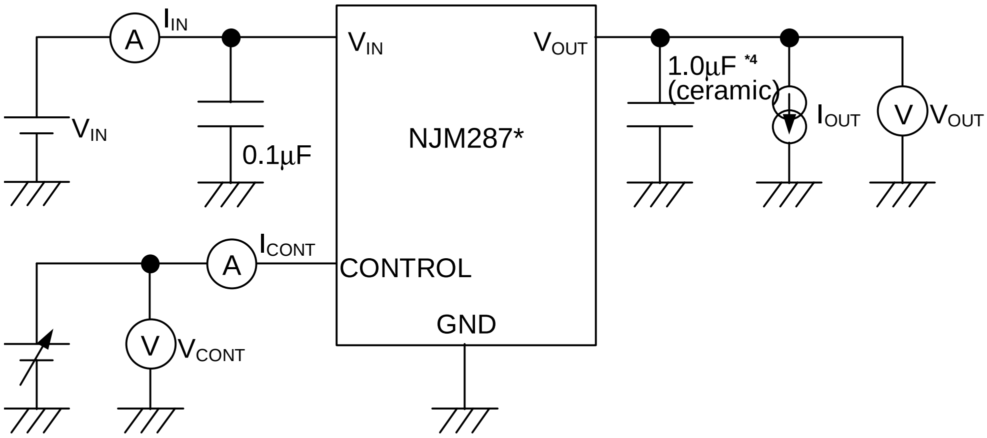
■ ELECTRICAL CHARACTERISTICS

(V_{IN}=Vo+1V, C_{IN}=0.1µF, Co=1.0µF: Vo≥2.7V (Co=2.2µF: Vo≤2.6V), Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	Vo	Io=30mA	-1.0%	–	+1.0%	V
Quiescent Current	I_Q	Io=0mA, expect Icont	–	120	180	µA
Quiescent Current at Control OFF	$I_{Q(OFF)}$	V_{CONT}=0V	–	–	100	nA
Output Current	Io	Vo-0.3V	150	200	-	mA
Line Regulation	$\Delta Vo/\Delta V_{IN}$	V_{IN}=Vo+1V ~ Vo+6V, Io=30mA	–	–	0.10	%/V
Load Regulation	ΔVo/ΔIo	Io=0 ~ 100mA	–	–	0.03	%/mA
Dropout Voltage	ΔV_{I-O}	Io=60mA	–	0.10	0.18	V
Ripple Rejection	RR	ein=200mVrms,f=1kHz, Io=10mA, Vo=3V Version	–	75	–	dB
Average Temperature Coefficient of Output Voltage	ΔVo/ΔTa	Ta=0 ~ 85°C, Io=10mA	–	±50	–	ppm/°C
Output Noise Voltage	V_{NO}	f=10Hz ~ 80kHz, Io=10mA, Vo=3V Version	–	45	–	µVrms
Control Voltage for ON-state	$V_{CONT(ON)}$		1.6	–	–	V
Control Voltage for OFF-state	$V_{CONT(OFF)}$		–	–	0.6	V

The above specification is a common specification for all output voltages.
Therefore, it may be different from the individual specification for a specific output voltage.

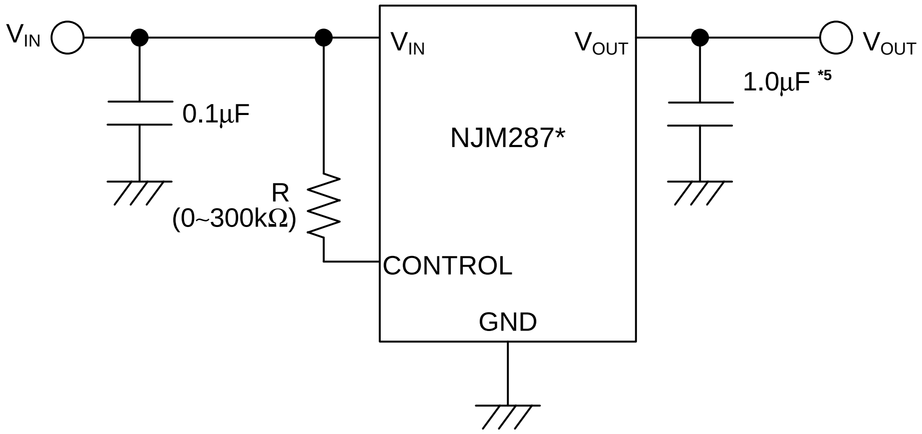
■ TEST CIRCUIT



*4 Vo≤2.6V version: Co=2.2µF(ceramic)

■ TYPICAL APPLICATION

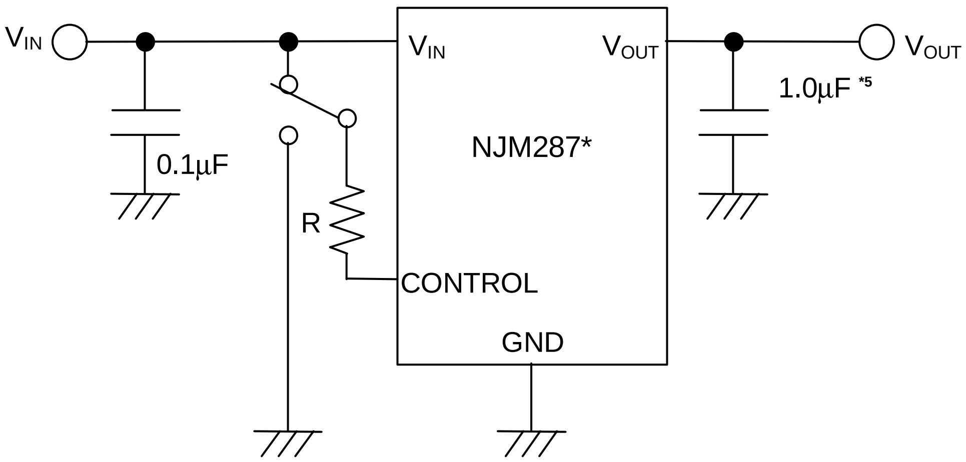
① In case that ON/OFF Control is not required:



*5 Vo≤2.6V version: Co=2.2µF

Connect control terminal to V_{IN} terminal

② In use of ON/OFF CONTROL:



*5 Vo≤2.6V version: Co=2.2µF

State of control terminal:
•"H"→ output is enabled.
•"L" or "open" → output is disabled.

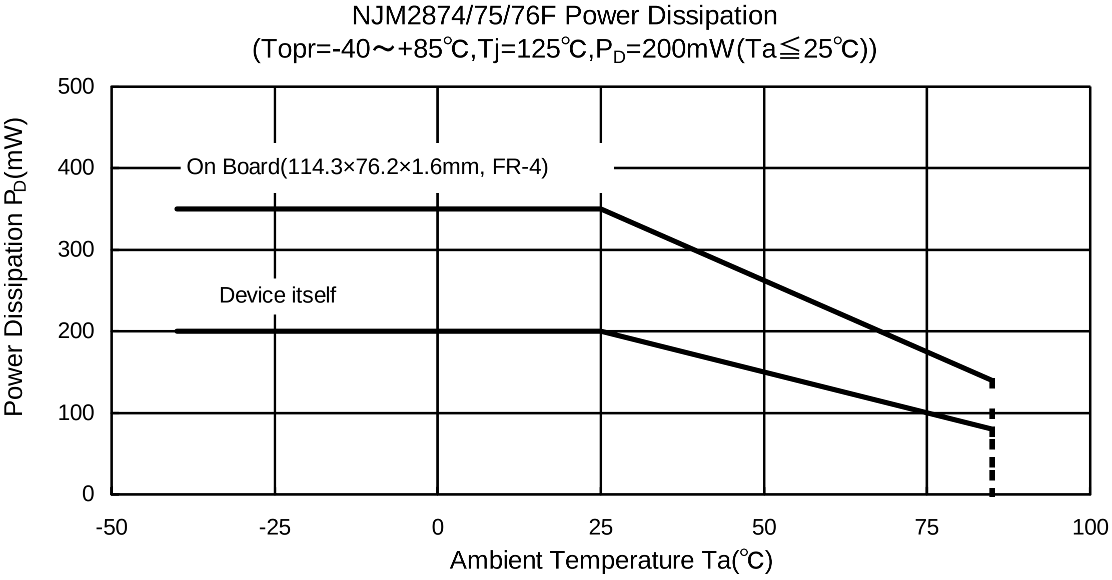
∗Noise bypass Capacitance Cp

Noise bypass capacitance Cp reduces noise generated by band-gap reference circuit. Noise level and ripple rejection will be improved when larger Cp is used. Use of smaller Cp value may cause oscillation. Use the Cp value of 0.01µF greater to avoid the problem.

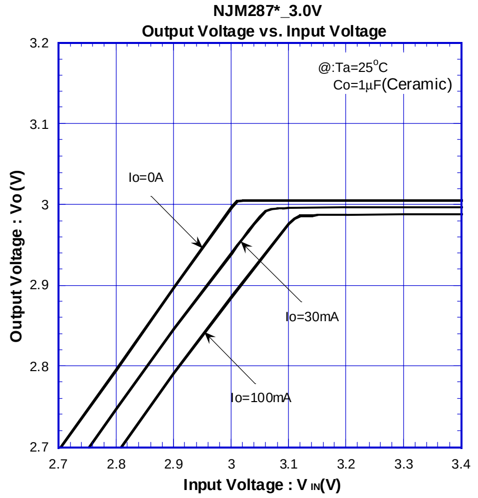
∗In the case of using a resistance "R" between V_{IN} and control.

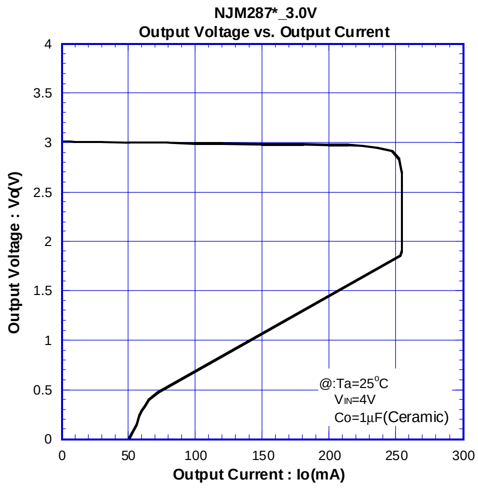
The current flow into the control terminal while the IC is ON state (I_{CONT}) can be reduced when a pull up resistance "R" is inserted between V_{IN} and the control terminal.
The minimum control voltage for ON state ($V_{CONT (ON)}$) is increased due to the voltage drop caused by I_{CONT} and the resistance "R". The I_{CONT} is temperature dependence as shown in the "Control Current vs. Temperature" characteristics. Therefore, the resistance "R" should be carefully selected to ensure the control voltage exceeds the $V_{CONT (ON)}$ over the required temperature range.

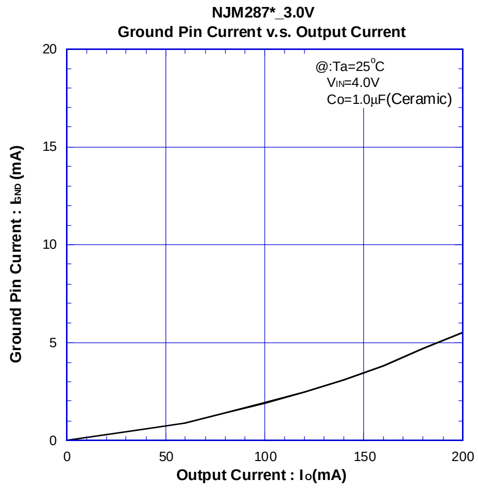
■ POWER DISSIPATION vs. AMBIENT TEMPERATURE

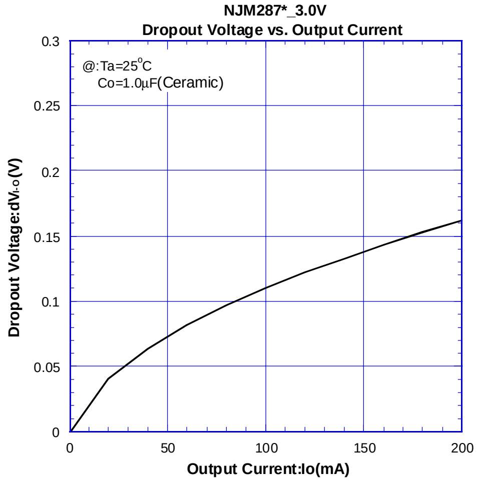


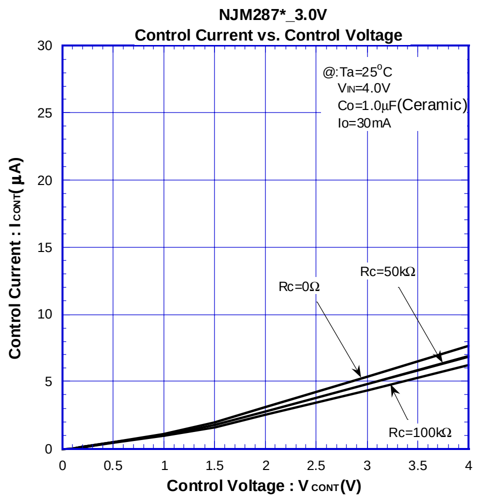
■ ELECTRICAL CHARACTERISTICS

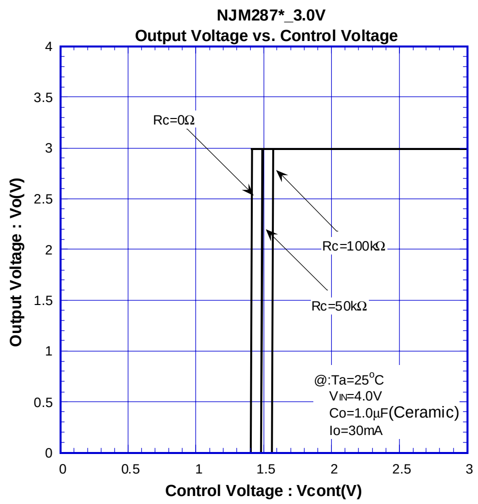






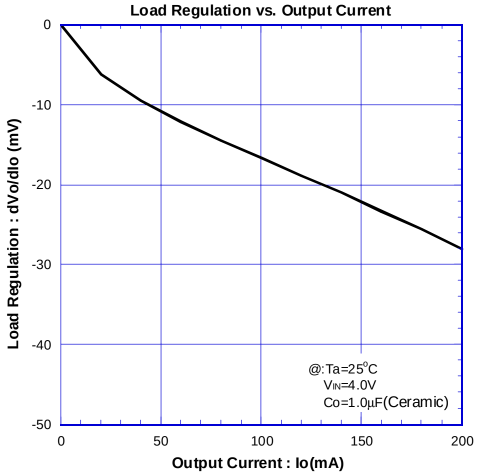




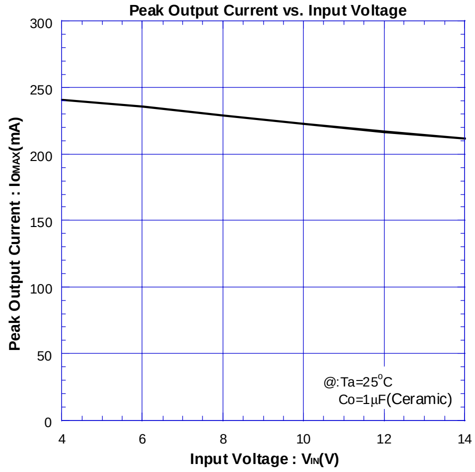


■ ELECTRICAL CHARACTERISTICS

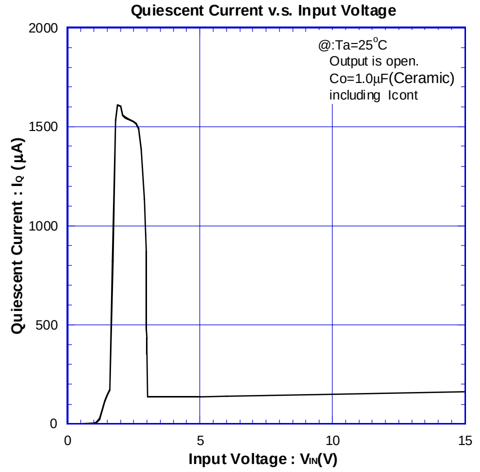
NJM287*_3.0V
Load Regulation vs. Output Current



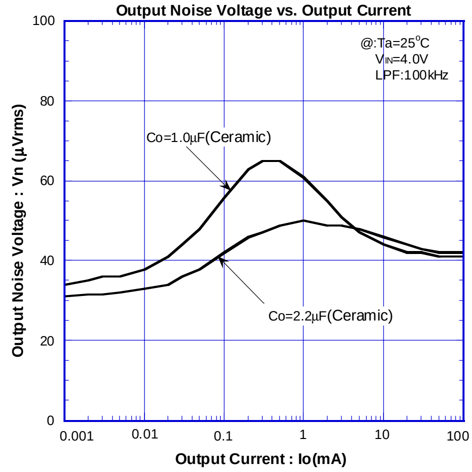
NJM287*_3.0V
Peak Output Current vs. Input Voltage



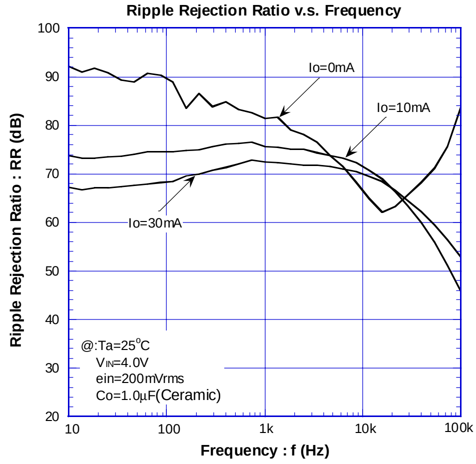
NJM287*_3.0V
Quiescent Current v.s. Input Voltage



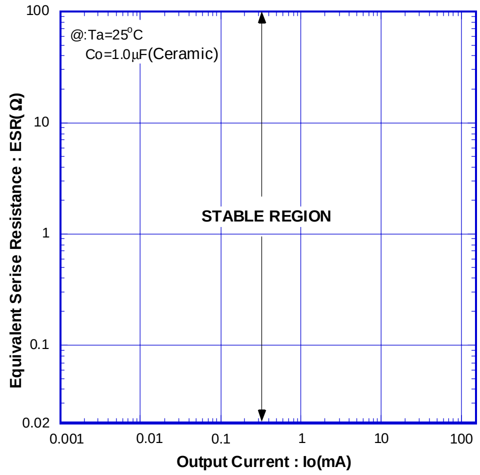
NJM287*_3.0V
Output Noise Voltage vs. Output Current



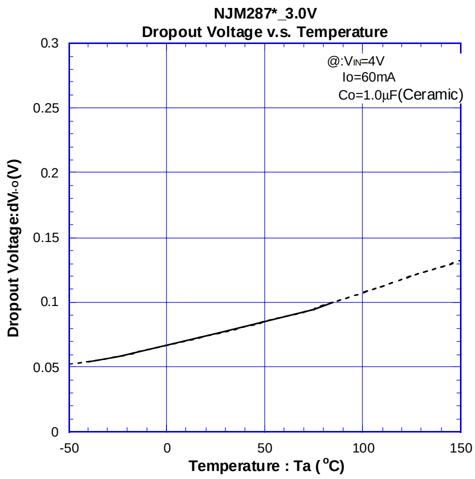
NJM287*_3.0V
Ripple Rejection Ratio v.s. Frequency

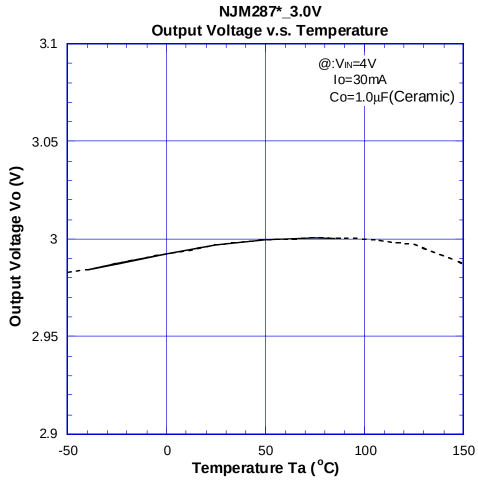


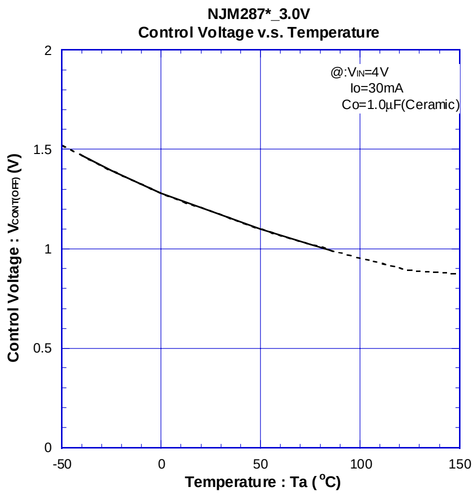
NJM287*_3.0V
Equivalent Serise Resistance vs. Output Current

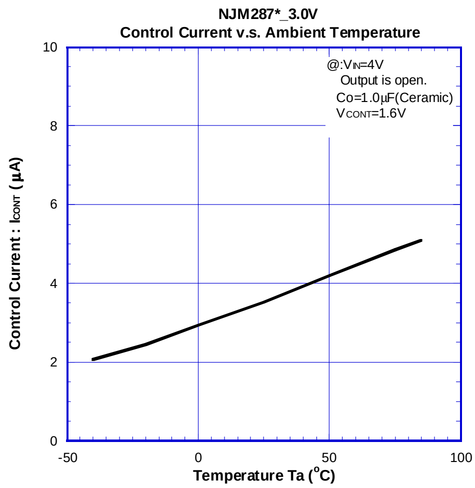


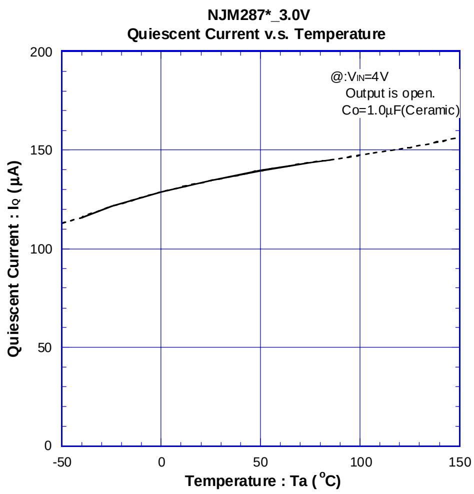
■ ELECTRICAL CHARACTERISTICS

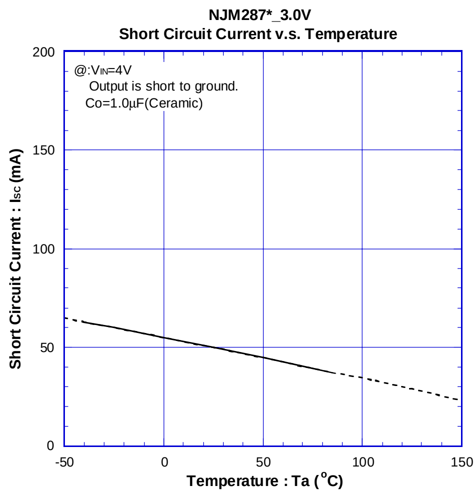












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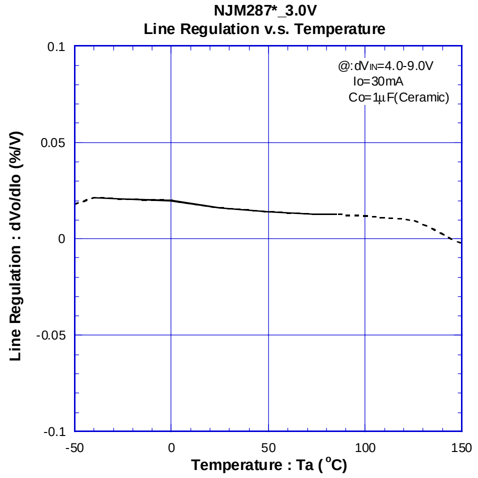

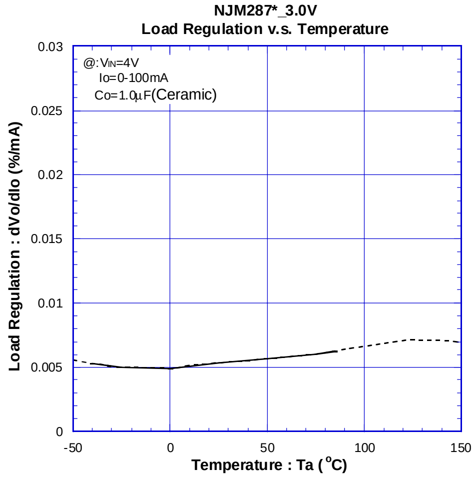

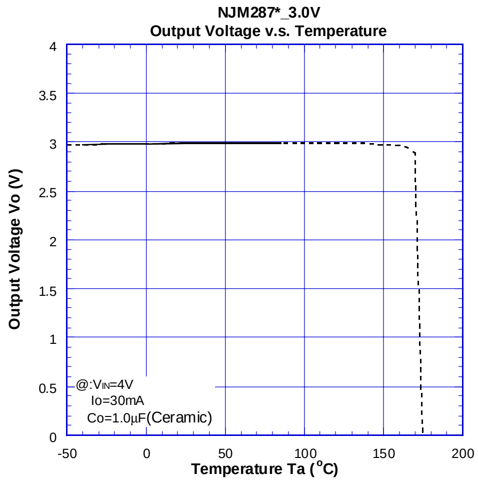

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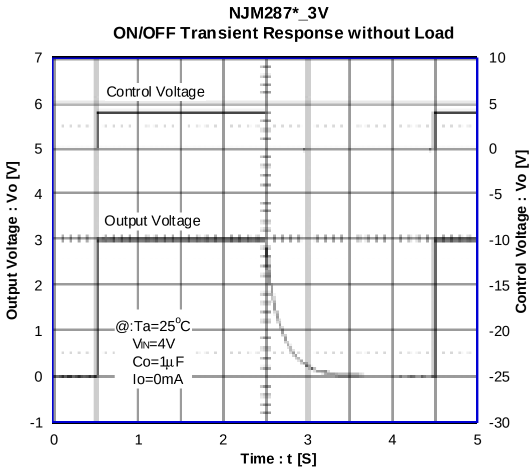

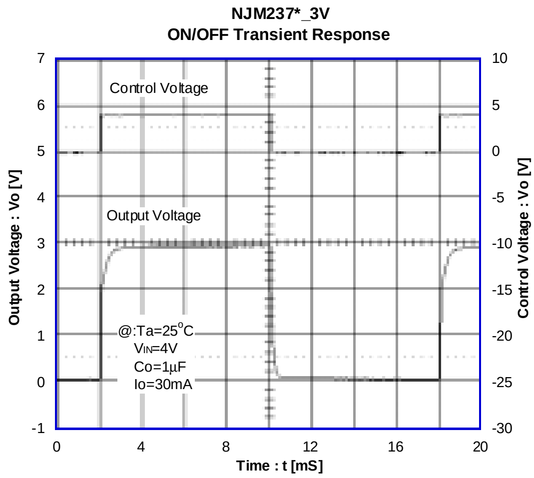

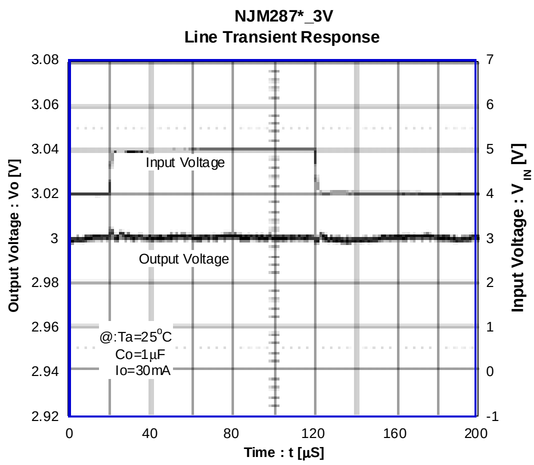

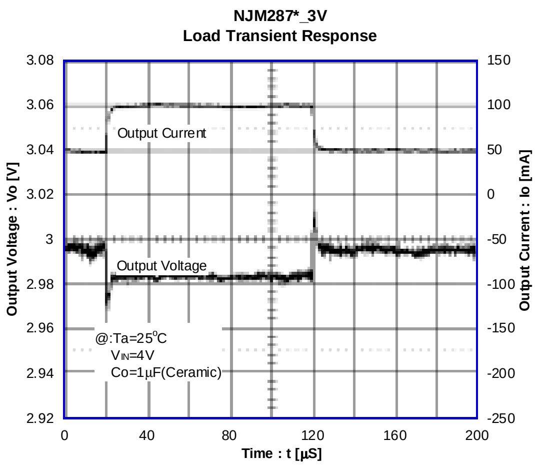

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