TOSHIBA Field Effect Transistor Silicon P Channel MOS Type  $(\pi$ -MOSV)

## **2SJ610**

# Switching Regulator, DC-DC Converter and Motor Drive Applications

• Low drain-source ON resistance:  $RDS(ON) = 1.85 \Omega \text{ (typ.)}$ 

• High forward transfer admittance:  $|Y_{fs}| = 18 \text{ S (typ.)}$ 

• Low leakage current:  $I_{DSS} = -100 \,\mu\text{A} \,(V_{DS} = -250 \,\text{V})$ 

• Enhancement-mode:  $V_{th} = -1.5 \sim -3.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$ 

#### Maximum Ratings (Tc = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	-250	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	-250	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	I <sub>D</sub>	-2.0	А	
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	-4.0		
Drain power dissipation		P <sub>D</sub>	20	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	180	mJ	
Avalanche current		I <sub>AR</sub>	-2.0	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	2.0	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	6.25	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	125	°C/W	

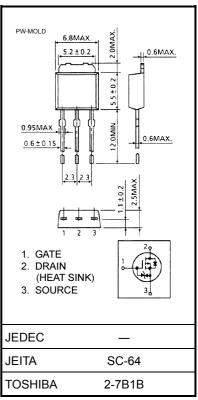
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:  $V_{DD} = -50 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 75 mH,  $I_{AR} = -2.0 \text{ A}$ ,  $R_{C} = 25 \Omega$ 

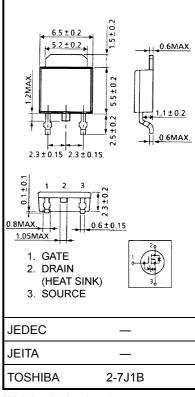
Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

Unit: mm



Weight: 0.36 g (typ.)



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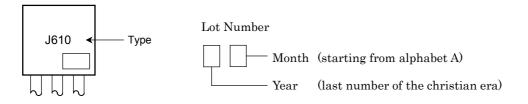
## **Electrical Characteristics (Tc = 25°C)**

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rrent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-off curr	ent	I <sub>DSS</sub>	$V_{DS} = -250 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-100	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-250	_	_	V
Gate threshold v	oltage	V <sub>th</sub>	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-1.5	_	-3.5	V
Drain-source ON	l resistance	R <sub>DS</sub> (ON)	$V_{GS} = -10 \text{ V}, I_D = -1.0 \text{ A}$	_	1.85	2.55	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -1.0 \text{ A}$	0.5	1.8	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	381	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	52	_	
Output capacitance		Coss		_	157	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $V_{OD}$ $V_{DD} \simeq 100 \text{ V}$ $V_{DD} \simeq 100 \text{ V}$	_	5	_	
	Turn-on time	t <sub>on</sub>		_	20		ne
	Fall time	t <sub>f</sub>		_	6		ns
	Turn-off time	t <sub>off</sub>		_	36		
Total gate charge		Qg	$V_{DD} \simeq -200 \text{ V}, V_{GS} = -10 \text{ V},$ $I_{D} = -2.0 \text{ A}$	_	24	_	nC
Gate-source charge		Q <sub>gs</sub>		_	11	_	
Gate-drain charge		Q <sub>gd</sub>	- 2.0 A	_	13	_	

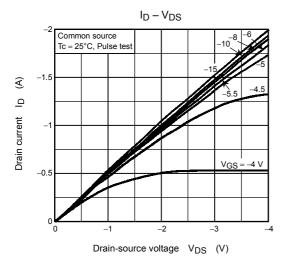
## **Source-Drain Ratings and Characteristics (Tc = 25°C)**

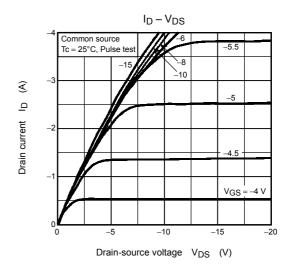
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	$I_{DR}$	_	_	_	-2.0	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	-4.0	Α
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = -2.0 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	2.0	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = -2.0 \text{ A}, V_{GS} = 0 \text{ V},$	_	120	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	540	_	nC

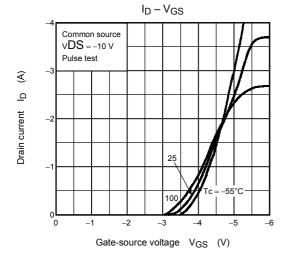
## Marking

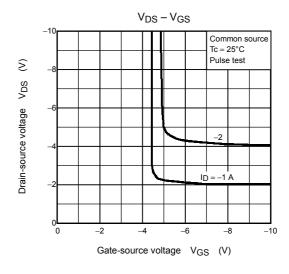


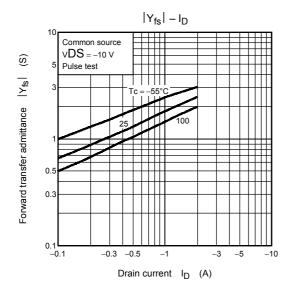
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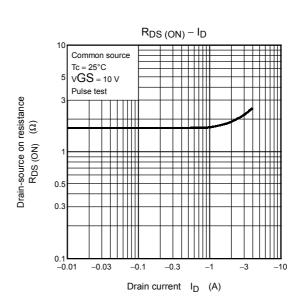




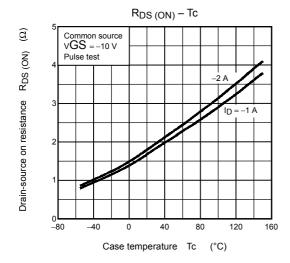


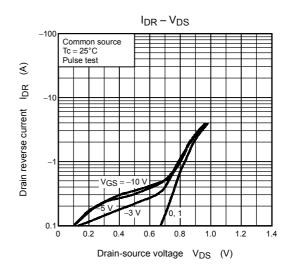


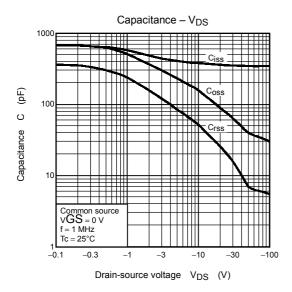


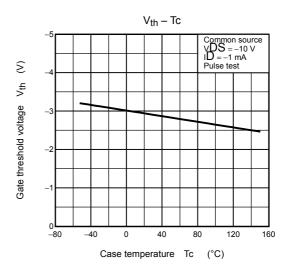


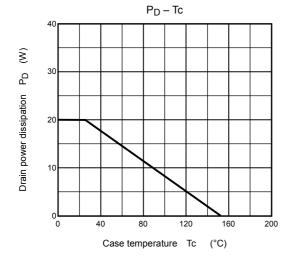
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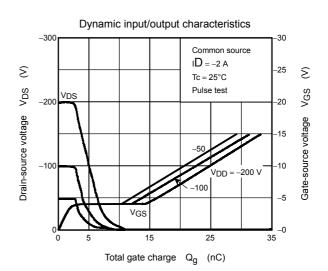


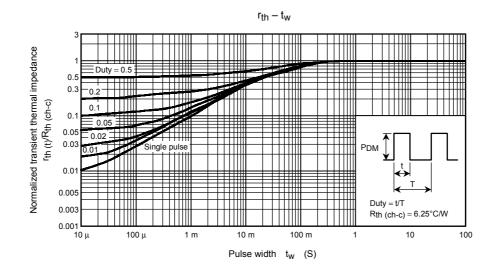


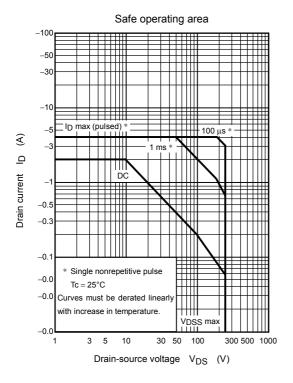


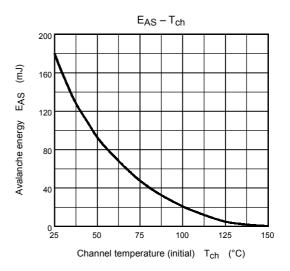


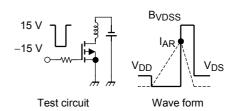












 $R_G = 25~\Omega$   $V_{DD} = -50~V,~L = 75~mH$ 

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