S-5723A Series

HIGH-SPEED BIPOLAR DETECTION TYPE HALL IC

www.sii-ic.com

Rev.3.0_00

The S-5723A Series, developed by CMOS technology, is a bipolar detection type Hall IC with a high-sensitivity and high-speed detection.
The output voltage changes when the S-5723A Series detects the intensity level of flux density and a polarity change. Using the S-5723A Series with a magnet makes it possible to detect rotation in various devices.
High-density mounting is possible by using the small SOT-23-3 package.

■ Features

- Built-in chopping stabilized amplifier
- Detection of bipolar magnetic fields
- Applicable in various devices with wide range of option
  - Detection logic for magnetism : Level “L” at S pole detection, level “H” at S pole detection
  - Output form : Nch open drain output, CMOS output
- Wide power supply voltage range : 2.4 V to 5.5 V
- High-speed detection : Operating cycle 132 μs typ., 240 μs max.
- Operating temperature range : –40°C to +85°C
  Small dependency magnetic characteristics against temperature
- Lead-free, Sn 100%, halogen-free[*1]

***1.** Refer to “■ **Product Name Structure**” for details.

■ Applications

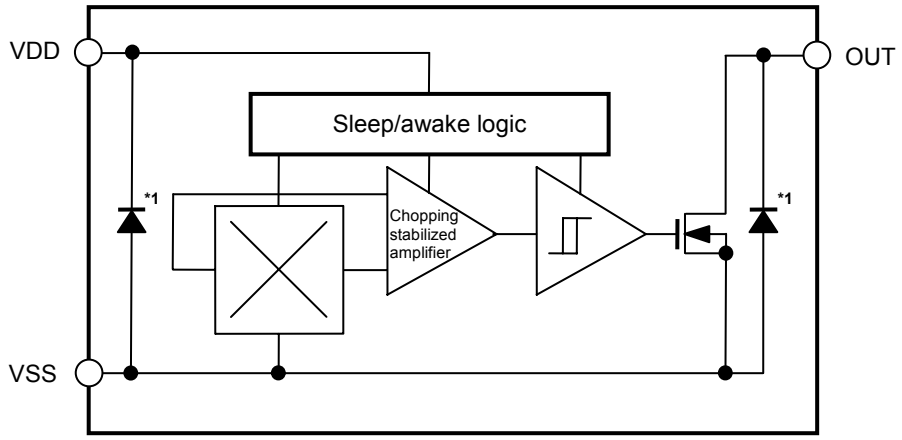
- Playthings, portable games
- Home appliances

■ Package

- SOT-23-3

■ Block Diagrams

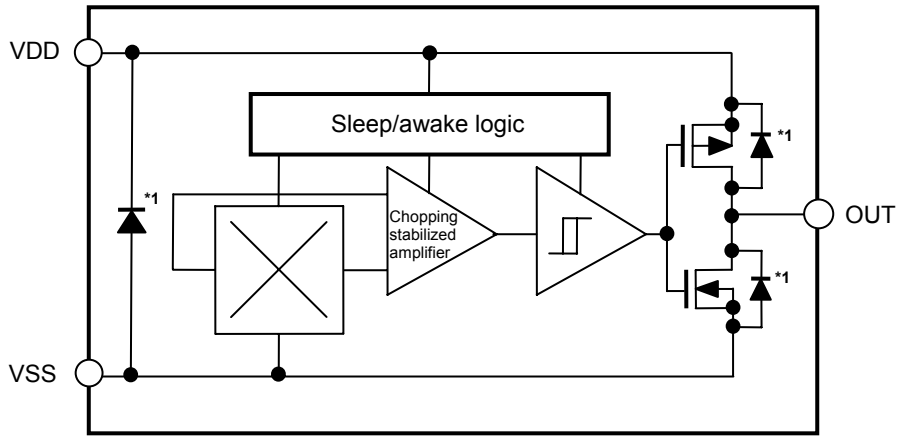
1. Nch open drain output product



***1.** Parasitic diode

Figure 1

2. CMOS output product

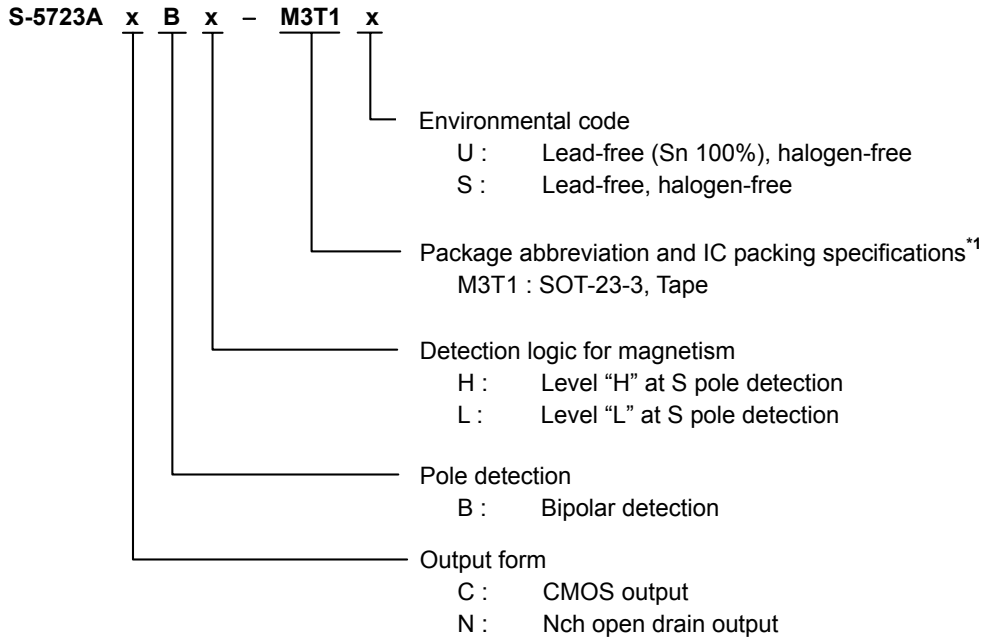


***1.** Parasitic diode

Figure 2

■ Product Name Structure

1. Product name



***1.** Refer to the tape specifications at the end of this book.

2. Package

Package Name	Drawing Code		
	Package	Tape	Reel
SOT-23-3	MP003-C-P-SD	MP003-C-C-SD	MP003-Z-R-SD

3. Product name list

Table 1

Output Form	Pole Detection	Detection Logic for Magnetism	Product Name
Nch open drain output	Bipolar detection	Level "L" at S pole detection	S-5723ANBL-M3T1y

Remark 1. Please contact our sales office for products with specifications other than the above.
2. y: S or U
3. Please select products of environmental code = U for Sn 100%, halogen-free products.

■ Pin Configuration

SOT-23-3
Top view

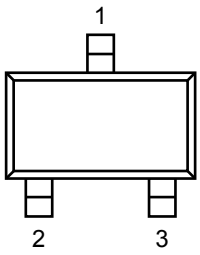


Figure 3

Table 2

Pin No.	Symbol	Pin Description
1	VSS	GND pin
2	VDD	Power supply pin
3	OUT	Output pin

■ Absolute Maximum Ratings

Table 3

(Ta = 25°C unless otherwise specified)

Item		Symbol	Absolute Maximum Rating	Unit
Power supply voltage		V_{DD}	V_{SS} − 0.3 to V_{SS} + 7.0	V
Output voltage	Nch open drain output	V_{OUT}	V_{SS} − 0.3 to V_{SS} + 7.0	V
	CMOS output		V_{SS} − 0.3 to V_{DD} + 0.3	V
Power dissipation		P_D	430*1	mW
Operating ambient temperature		T_{opr}	−40 to +85	°C
Storage temperature		T_{stg}	−40 to +125	°C

***1.** When mounted on board

[Mounted board]

(1) Board size : 114.3 mm × 76.2 mm × t1.6 mm

(2) Name : JEDEC STANDARD51-7

Caution The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

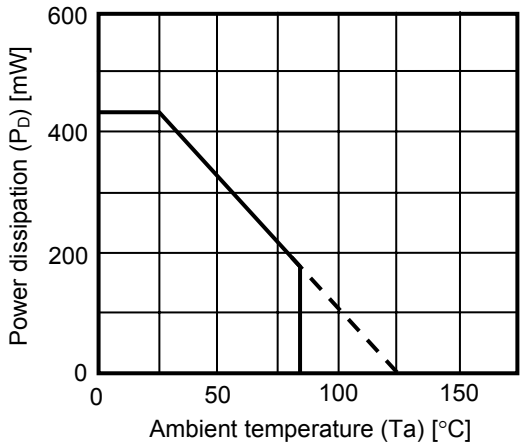


Figure 4 Power Dissipation of Package (When Mounted on Board)

■ Electrical Characteristics

Table 4

($Ta = 25°C$, $V_{DD} = 3.0$ V, $V_{SS} = 0$ V unless otherwise specified)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	Test Circuit
Power supply voltage	V_{DD}	–	2.4	3.0	5.5	V	–
Average current consumption	I_{DD}	Average value	–	950	1200	μA	1
Output current	I_{OUT}	Output transistor Nch, $V_{OUT} = 0.4$ V	1	–	–	mA	2
		CMOS output Output transistor Pch, $V_{OUT} = V_{DD} - 0.4$ V	–	–	–1	mA	2
Output leakage current	I_{LEAK}	Nch open drain output Output transistor Nch, $V_{OUT} = 5.5$ V	–	–	1	μA	2
Awake mode time	t_{AW}	–	–	121	–	μs	–
Sleep mode time	t_{SL}	–	–	11	–	μs	–
Operating cycle	t_{CYCLE}	$t_{AW} + t_{SL}$	–	132	240	μs	–

■ Magnetic Characteristics

Table 5

($Ta = 25°C$, $V_{DD} = 3.0$ V, $V_{SS} = 0$ V unless otherwise specified)

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit	Test Circuit
Operating point *1	S pole	B_{OP}	–	0.5	3.0	5.3	mT	3
Release point *2	N pole	B_{RP}	–	–5.3	–3.0	–0.5	mT	3
Hysteresis width *3		B_{HYS}	$B_{HYS} = B_{OP} - B_{RP}$	–	6.0	–	mT	3

***1.** B_{OP} : Operating point

The operating point is the value of magnetic flux density when the detection logic for magnetism is "L" when the S pole is detected, and when the output voltage (V_{OUT}) is inverted from "H" to "L" after the magnetic flux density applied to the S-5723A Series by the magnet (S pole) is increased (by moving the magnet closer).

V_{OUT} retains the state until a magnetic flux density of the N pole higher than B_{RP} is applied.

***2.** B_{RP} : Release point

The release point is the value of magnetic flux density when the detection logic for magnetism is "L" when the S pole is detected, and when the output voltage (V_{OUT}) is inverted from "L" to "H" after the magnetic flux density applied to the S-5723A Series by the magnet (N pole) is increased (by moving the magnet closer).

V_{OUT} retains the state until a magnetic flux density of the S pole higher than B_{OP} is applied.

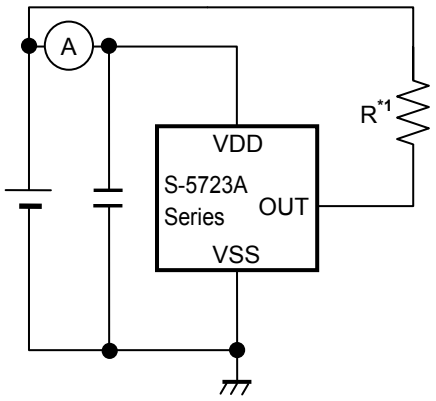
***3.** B_{HYS} : Hysteresis width

B_{HYS} is the difference between B_{OP} and B_{RP}.

Remark The unit of magnetic density mT can be converted by using the formula 1 mT = 10 Gauss.

■ Test Circuits

1.



1. Resistor (R) is unnecessary for the CMOS output product.

Figure 5

2.

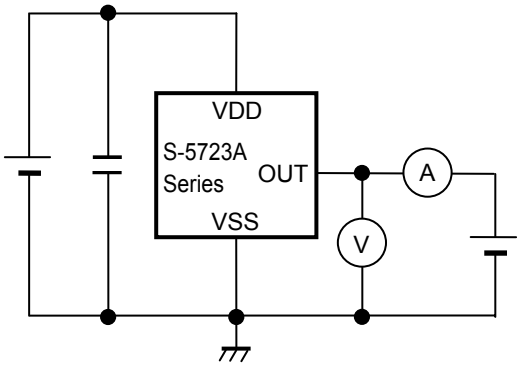
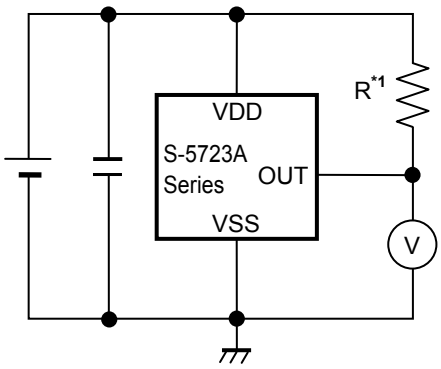


Figure 6

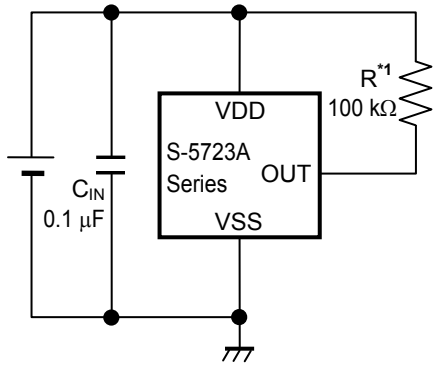
3.



1. Resistor (R) is unnecessary for the CMOS output product.

Figure 7

■ Standard Circuit



***1.** Resistor (R) is unnecessary for the CMOS output product.

Figure 8

Caution The above connection diagram and constant will not guarantee successful operation. Perform thorough evaluation using the actual application to set the constant.

■ Operation

1. Direction of applied magnetic flux and position of Hall sensor

The S-5723A Series detects the flux density which is vertical to the marking surface.
Figure 9 shows the direction in which magnetic flux is being applied.

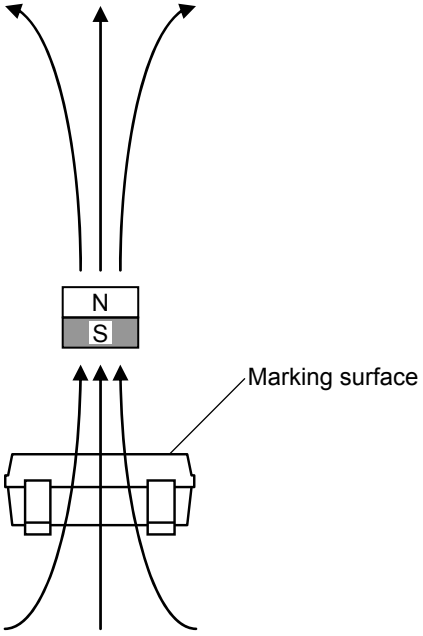


Figure 9

Figure 10 shows the position of Hall sensor.
The center of this Hall sensor is located in the area indicated by a circle, which is in the center of a package as described below.
The following also shows the distance (typ. value) between the marking surface and the chip surface of a package.

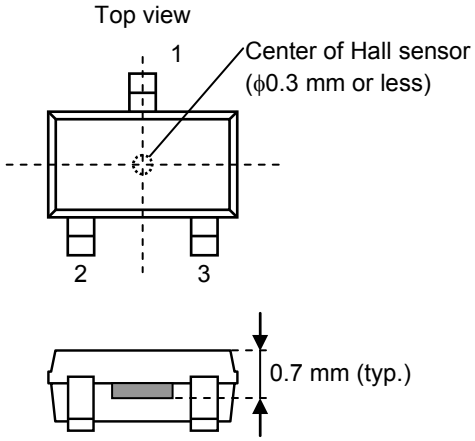


Figure 10

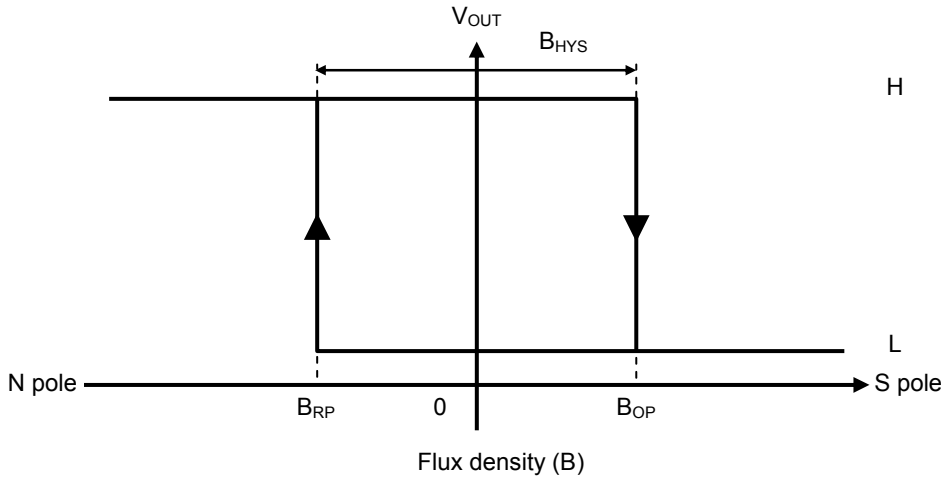
2. Basic operation

The S-5723A Series changes the output voltage (V_{OUT}) according to the level of the magnetic flux density (N or S pole) applied by a magnet.
The following explains the operation when the magnetism detection logic is “L” when the S pole is detected.

When the magnetic flux density of the S pole perpendicular to the marking surface exceeds B_{OP} after the S pole of a magnet is moved closer to the marking surface of the S-5723A Series, V_{OUT} changes from “H” to “L”. When the N pole of a magnet is moved closer to the marking surface of the S-5723A Series and the magnetic flux density of the N pole is higher than B_{RP}, V_{OUT} changes from “L” to “H”. While the magnetic field is not applied, V_{OUT} retains the state.

When the power is turned on, the output voltage (V_{OUT}) is “H” or “L”.
Definition of the magnetic field is performed every operating cycle indicated in “■ **Electrical Characteristics**”.
Figure 11 shows the relationship between the magnetic flux density and V_{OUT}.

(1) Products level “L” at S pole detection



(2) Products level “H” at S pole detection

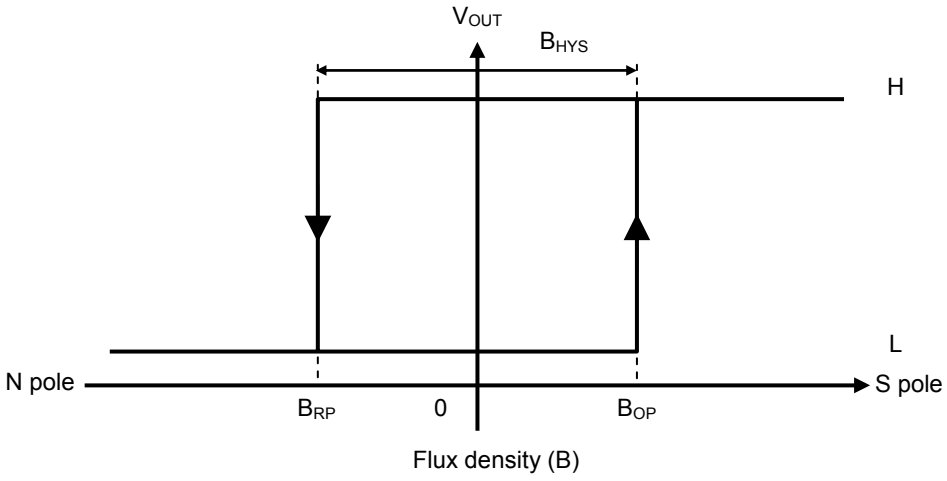


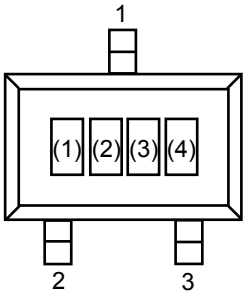
Figure 11

■ Precautions

- If the impedance of the power supply is high, the IC may malfunction due to a supply voltage drop caused by through-type current. Take care with the pattern wiring to ensure that the impedance of the power supply is low.
- Note that the IC may malfunction if the power supply voltage rapidly changes.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- Large stress on this IC may affect on the magnetic characteristics. Avoid large stress which is caused by bend and distortion during mounting the IC on a board or handle after mounting.
- When designing for mass production using an application circuit described herein, the product deviation and temperature characteristics of the external parts should be taken into consideration. SII shall not bear any responsibility for patent infringements related to products using the circuits described herein.
- SII claims no responsibility for any disputes arising out of or in connection with any infringement by products including this IC of patents owned by a third party.

■ Marking Specifications

SOT-23-3
Top view

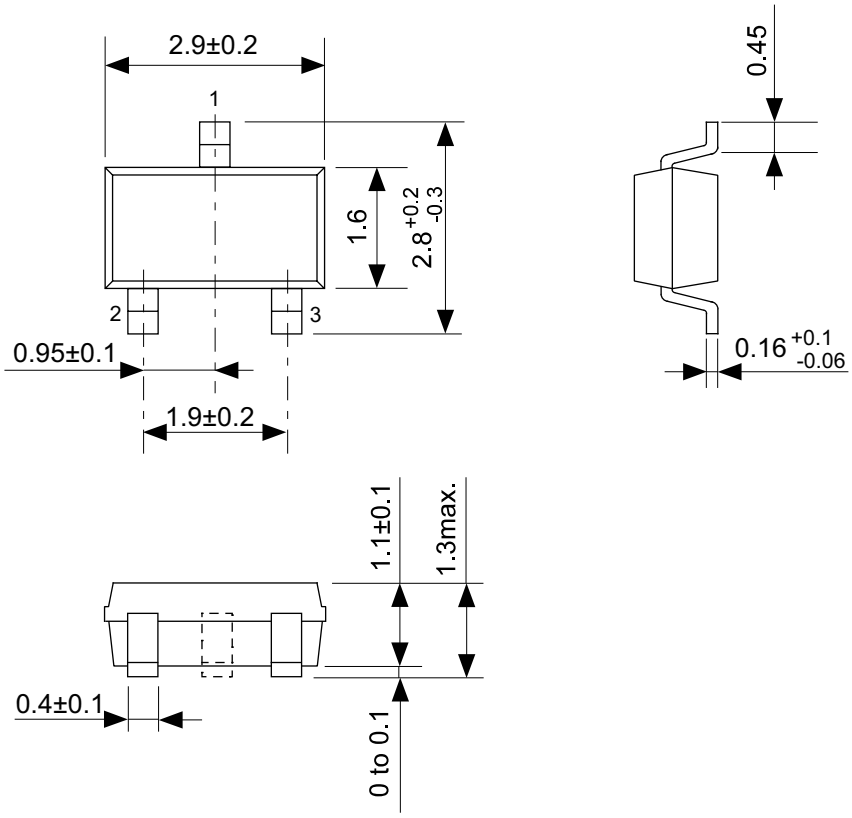


(1) to (3) : Product code (Refer to **Product name vs. Product code**.)
(4) : Lot number

Product name vs. Product code

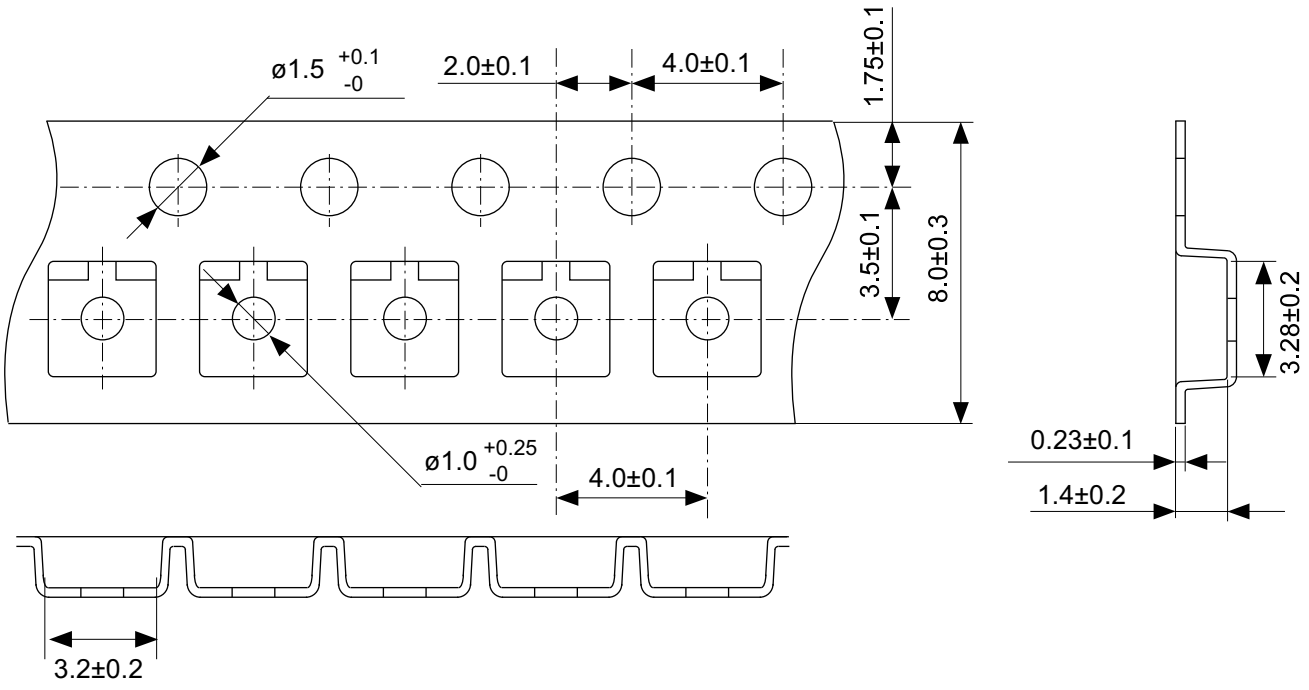
Product Name	Product Code		
	(1)	(2)	(3)
S-5723ANBL-M3T1y	T	5	D

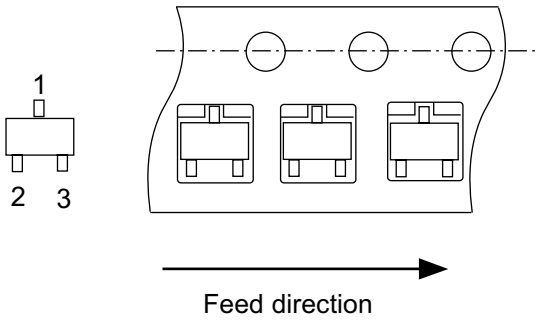
Remark 1. Please contact our sales office for products with specifications other than the above.
2. y: S or U
3. Please select products of environmental code = U for Sn 100%, halogen-free products.



No. MP003-C-P-SD-1.0

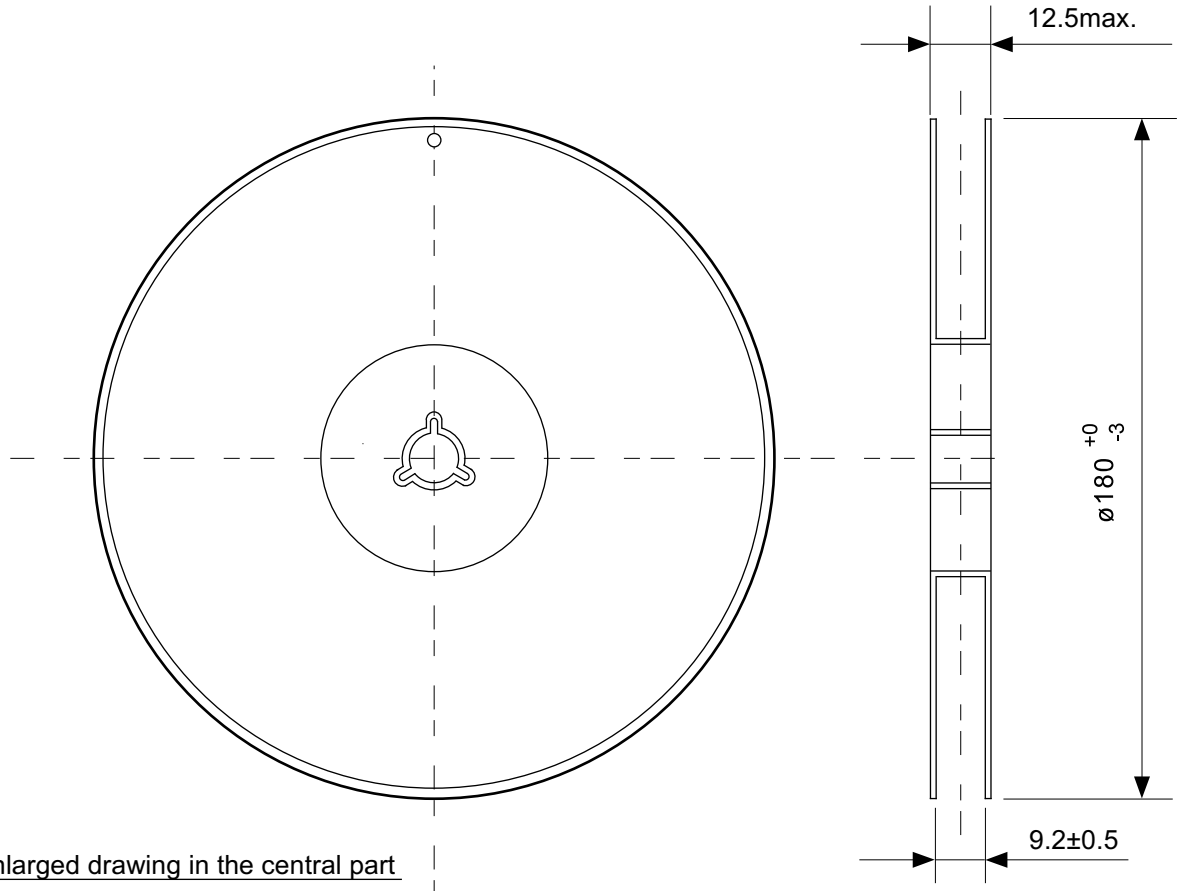
TITLE	SOT233-C-PKG Dimensions
No.	MP003-C-P-SD-1.0
SCALE	
UNIT	mm
Seiko Instruments Inc.	



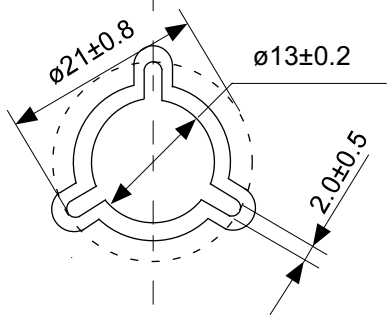


No. MP003-C-C-SD-2.0

TITLE	SOT233-C-Carrier Tape
No.	MP003-C-C-SD-2.0
SCALE	
UNIT	mm
Seiko Instruments Inc.	



Enlargement drawing in the central part



No. MP003-Z-R-SD-1.0

TITLE	SOT233-C-Reel		
No.	MP003-Z-R-SD-1.0		
SCALE		QTY.	3,000
UNIT	mm		
Seiko Instruments Inc.			

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