PC35720NIT

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■ Features

- 1. Low input current type. (I_F=0.1mA)
- 2. High resistance to noise due to high common rejection voltage. (CMR:MIN. $10kV/\mu s$)
- 3. Mini-flat package.
- 4. Isolation voltage. (V_{iso (rms)}:3.75kV)

■ Applications

- 1. Programmable controllers.
- 2. Facsimiles.
- 3. Telephones.

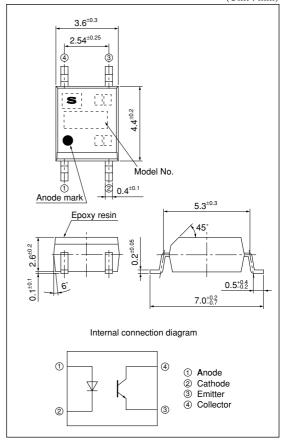
■ Absolute Maximum Ratings (T _a =25°C)									
	Parameter	Symbol	Rating	Unit					
Input	Forward current	I_F	10	mA					
	*1 Peak forward current	I_{FM}	200	mA					
	Reverse voltage	V_R	6	V					
	Power dissipation	P	15	mW					
Output	Collector-emitter voltage	V_{CEO}	70	V					
	Emitter-collector voltage	V_{ECO}	6	V					
	Collector current	I_C	50	mA					
	Collector power dissipation	P_{C}	150	mW					
Total	power dissipation	P _{tot}	170	mW mW					
Operating temperature		T_{opr}	-30 to +100	°C					
Storage temperature		T_{stg}	-40 to +125	°C					
*2 Isolat	ion voltage	V _{iso (rms)}	3.75	kV					
*3 Solde	ring temperature			°C					

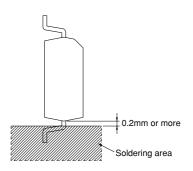
^{*1} Pulse width≤100µs, Duty ratio=0.001

Low Input Current Type Photocoupler

■ Outline Dimensions







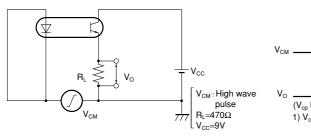
^{*2 40} to 60%RH, AC for 1 minute, f=60Hz

^{*3} For 10s

■ Electro-optical Characteristics								$(T_a=25^{\circ}C)$
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit	
Input	Forward voltage		V_F	I _F =5mA	_	1.2	1.4	V
	Reverse current		I_R	V _R =4V	_	-	10	μΑ
	Terminal capacitance		Ct	V=0, f=1kHz	_	30	250	pF
Ħ	Collector dark	llector dark current		$V_{CE} = 50V, I_{F} = 0$	-	_	100	nA
Output	Collector-emitter breakdown voltage		BV _{CEO}	I _C =0.1mA, I _F =0	70	_	_	V
	Emitter-collector breakdown voltage		BV _{ECO}	$I_{E}=10\mu A, I_{F}=0$	6	_	_	V
Transfer characteristics	Collector current		$I_{\rm C}$	I _F =0.1mA, V _{CE} =5V	0.1	-	0.5	mA
	Collector-emitter saturation voltage		V _{CE (sat)}	I _F =5mA, I _C =1mA	-	0.1	0.3	V
	Isolation resistance		R _{ISO}	DC500V 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	_	Ω
	Floating capacitance		$C_{\rm f}$	V=0, f=1MHz	_	0.6	1.0	pF
	Response time	Rise time	t _r	V_{CE} =2V, I_{C} =2mA, R_{L} =100 Ω	_	4	18	μs
		Fall time	$t_{\rm f}$		_	3	18	μs
	*4 Common mode rejection voltage C1		CMR	T _a =25°C, R _L =470Ω, V _{CM} =1.5kV (peak), I _F =0mA, V _{CC} =9V, Vnp=100mV	10	_	_	kV/μs

^{*4} Refer to Fig.1.

Fig.1 Test Circuit for Common Mode Rejection Voltage



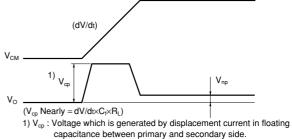


Fig.2 Forward Current vs. Ambient Temperature

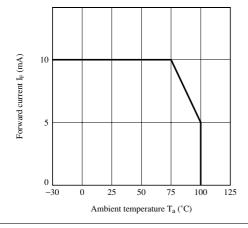
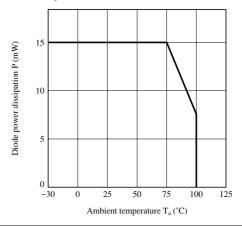


Fig.3 Diode Power Dissipation vs. Ambient Temperature



SHARP PC35720NIT

Fig.4 Collector Power Dissipation vs. Ambient Temperature

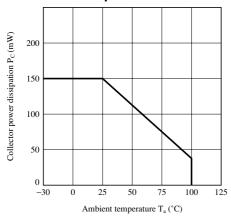


Fig.6 Peak Forward Current vs. Duty Ratio

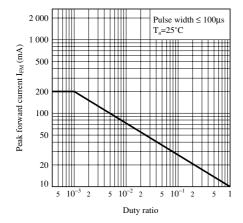
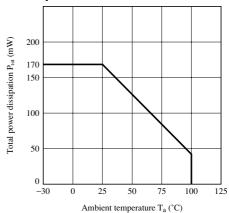


Fig.5 Total Power Dissipation vs. Ambient Temperature



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