

Photo Transistor

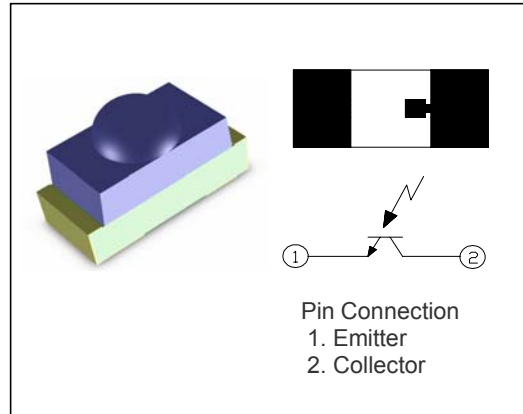
KDT-6315S

Description

The KDT-6315S is a high-sensitivity & surface mount type silicon phototransistor. It's ideal for various kinds of optical transistor such as touch panels for C/D, ATM, Car navigation system and even AV Instrument and various types of disk driver.

Features

- Compact and thin package
- SMD type
- Reflow soldering
- RoHS & High reliability package



Applications

- Touch screen for ATM
- Touch screen for Car navigation system
- Touch screen for FA equipment
- AV instrument
- Various types of disk driver

Absolute Maximum Ratings

[T_A = 25°C]

Parameter	Symbol	Min.	Max.	Max.
Collector-Emitter Voltage	V _{CEO}	-	35	V
Emitter-Collector Voltage	V _{ECO}	-	6	V
Collector Current	I _C	-	20	mA
Collector Power Dissipation	P _C	-	75	mW
Operating Temperature	T _{opr.}	-20	85	°C
Storage Temperature	T _{stg.}	-30	85	°C
Soldering Temperature*1	T _{sol}	-	260	°C

*1 : MAX 10s

The contents of this data sheet are subject to change without advance notice for the purpose of improvement.
When using this product, would you please refer to the latest specifications.

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Electrical Characteristics

[T_A = 25°C]

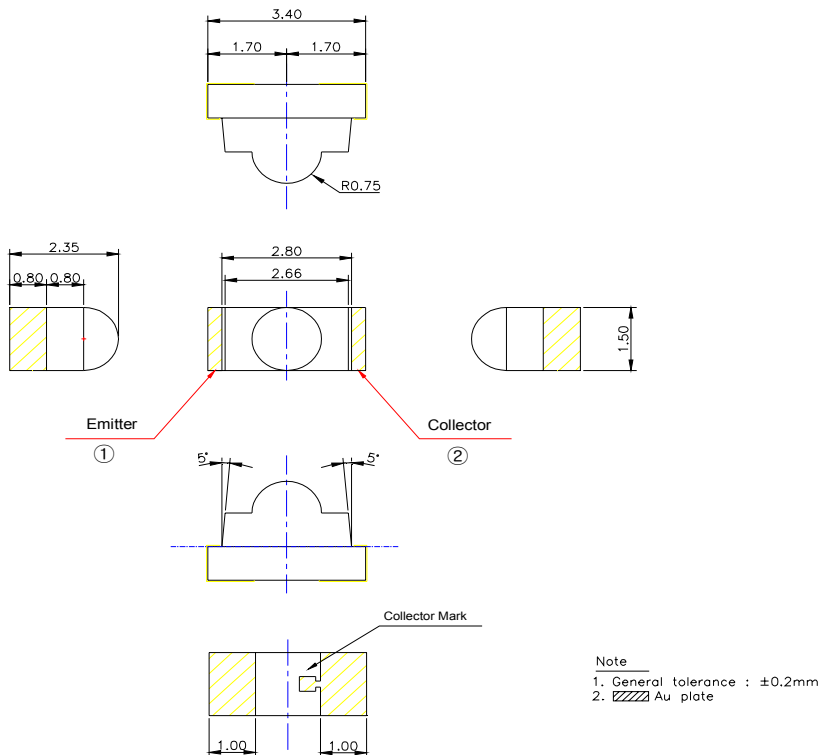
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Dark Current	I _{CEO}	Ev=0, V _{CE} =20V	-	1.0	100	nA
Collector Current	I _C	Ev=1000lx, V _{CE} =5V	1.45	2.2	-	mA
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	Ev=1000lx, I _C =0.8mA	-	0.15	0.4	V
Spectral Sensitivity	λ	-	700	-	1050	nm
Peak Sensitivity Wavelength	λ _P	-	-	880	-	nm
Collector-Emitter Breakdown Voltage	BV _{CEO}	Ev=0, I _{CE} =0.1mA	35	90	-	V
Emitter-Collector Breakdown Voltage	BV _{ECO}	Ev=0, I _{EC} =0.01mA	6	7.5	-	V
Half Angle	Δθ	-	-	±15	-	deg

Ordering Information

Part Number	Packaging Type	Quantity
KDT-6315S	Tape and Reel	4,000

Package Outline Dimensions

(Unit : mm)



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