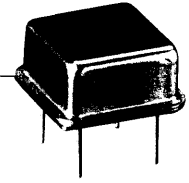


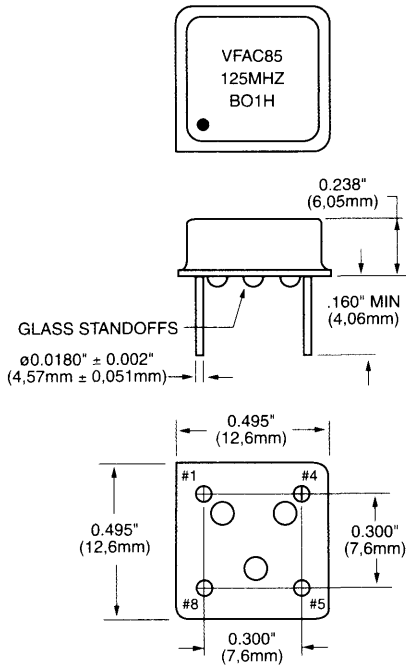
VFAC85



ACMOS/TTL Compatible Half Size DIP Clock Oscillators

FEATURES

- Wide Frequency Range
- Extended Temperature Ranges
- Tight Symmetry Available
- Common Footprint
- Gull-Wing Package Available



All dimensions are typical unless otherwise specified.

Creating a Part Number

VFAC85 [] [] - [] - **FREQ.**

FREQUENCY STABILITY	
Code	Specification
S	±20 ppm
A	±25 ppm
B	±50 ppm
	+100 ppm (std.)
C	±500 ppm

DUTY CYCLE	
Code	Specification
NH	±2.5%
H	±5%
	±10% (std.)

LEAD CONFIGURATION	
Code	Specification
GR	Gull Wing
G	Gull Wing
	Through Hole (std.)

OPERATIONAL TEMP. RANGE	
Code	Specification
1	0°C to +70°C (std.)
	-40°C to +85°C
2	-55°C to +125°C

INPUT VOLTAGE	
Code	Specification
L	3.3 Volt ±5%
	5.0 Volt ±5% (std.)

Example: VFAC85SHHL-1GR-50MHz: Frequency Stability ±20ppm, Duty Cycle ±2.5%, Input Voltage 3.3 Volt ±5%, Operating Temperature -40°C to +85°C, Gull Wing, Frequency 50.000MHz.

Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
Input Break Down Voltage	V _{cc}		-0.5		7.0	V	
Storage Temp.	T _s		-55		+125	°C	
Frequency Range	F		2		200	MHz	
Frequency Stability	ΔF/F	Overall conditions including: calibration, temp., aging 10 yrs, shock, vibration			±100	ppm	1
Input Voltage	V _{cc}		4.75 3.15	5.00 3.30	5.25 3.45	V	Std. LV Opt.
Input Current	I _{cc}	No load, 100MHz			60	mA	2
Load		10 TTL gates or 50pF MAX, AC coupled 50 Ohm termination recommended					
Duty Cycle		@1.4V	40	50	60	%	3
Rise/Fall Time	T _r /T _f	50Ω, 15pf		1	3	ns	20% to 80%
Logic "1" Level	V _{oh}	Max Load	0.9V _{cc}				
Logic "0" Level	V _{ol}	Max Load			0.1V _{cc}		
Start-up Time	T _s			2	10	ms	
Phase Jitter		1σ			1	ps	f _j >1KHz
Tristate Function		Input HIGH (>2.5V) or floating: Input LOW (<0.5V):	ACTIVE INFINITE IMPEDANCE				
Enable/Disable Time	T _e /T _d				100	ns	
Operating Temperature Range	0°C to +70°C (-40°C to +85°C, and -55°C to +125°C available)						
Mechanical Shock	Per MIL-STD-202, Method 213, Cond. E						
Thermal Shock	Per MIL-STD-883, Method 1011, Cond. A						
Vibration	Per MIL-STD-883, Method 2007, Cond. A						
Soldering Conditions	260°C, for 10s, Max.						
Hermetic Seal	Leak rate less than 5 × 10 ⁻⁸ atm.cc/s of helium						
Pin Out	Pin #1-Tristate Control Pin #5-Output		Pin #4-Ground, Case Pin #8-Vcc				

Notes:
 1. Standard frequency stability, others available.
 2. Current is load and frequency dependent.
 3. ±5% and ±2.5% duty cycle available.
 All specifications are subject to change without notice.