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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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RNB4580 Series

Dual Low Noise Operational Amplifier

REA03D0004-0200

Rev.2.00

Dec 24, 2008

Description

RNB4580 is dual bipolar low noise amplifiers designed for audio systems to improve tone control, audio pre-amplifier and industrial measurement tools. It also suitable for head phone amplifier at higher output current.

This product features internal frequency compensation, low noise, low distortion, high gain and high bandwidth. It also can operate under dual power supply voltage up to ± 18 V or single power supply up to 36 V.

The IC can be applied for the handy type set operational amplifier of general purpose in application of low voltage single supply type, which is properly biased of the input low voltage source.

Features

- Wide bandwidth: 15 MHz
- High speed: 7 V/ μ s
- Low input noise voltage: 0.7 μ V_{rms}
- Large DC voltage gain: 110 dB
- Operating voltage: ± 2 V to ± 18 V
- Package outline available in Pb free lead frame:
DP-8
SOP-8 (JEITA)
SOP-8 (JEDEC)

Applications

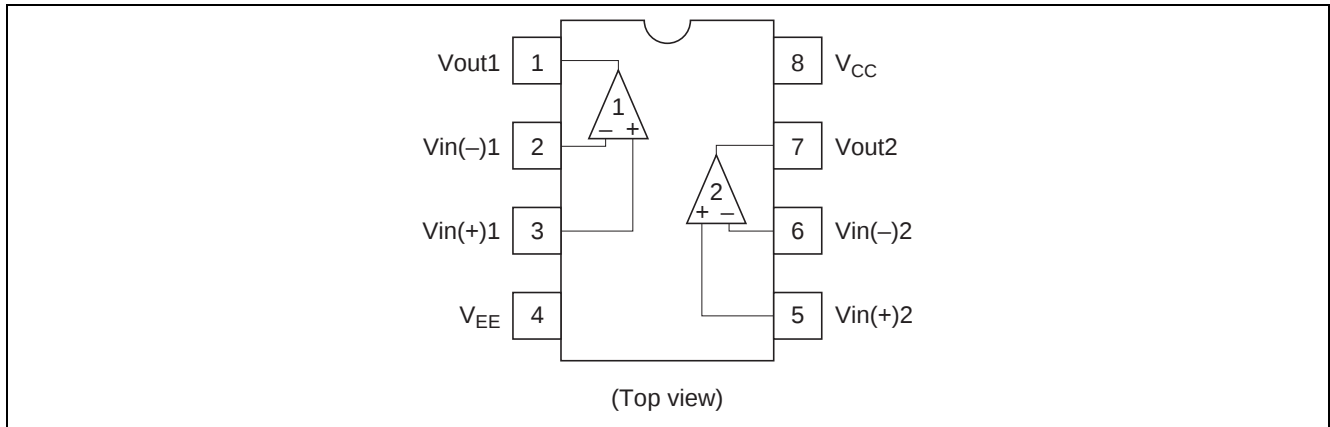
- Audio AC-3 decoder system
- Audio amplifier
- Pre-amp
- Active filter

Ordering Information

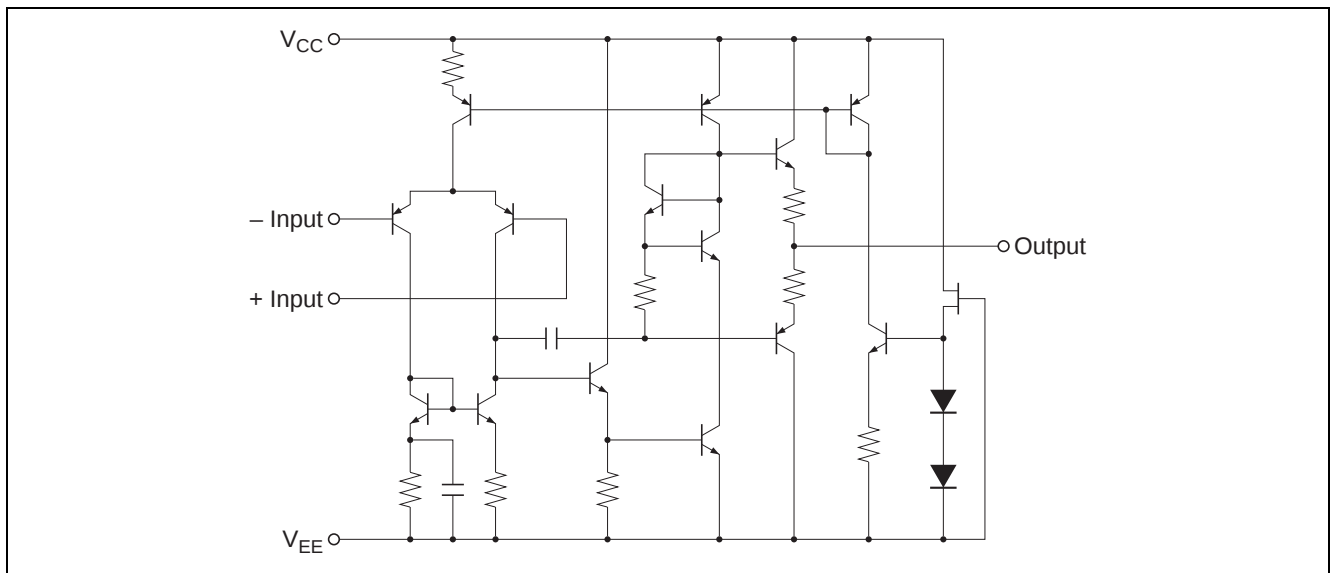
Part No.	Application	Package Code (Package Name)	Packing Abbreviation (Quantity)
RNB4580	Commercial use	PRDP0008AF-B (DP-8FV)	— (50 pcs/stick 1,000 pcs/box)
RNB4580F		PRSP0008DE-B (FP-8DGV)	EL (2,500 pcs/reel)
RNB4580RP		PRSP0008DD-C (FP-8DCV)	EL (2,500 pcs/reel)

Note: This product is designed for consumer use and not for automotive and industry.

Pin Arrangement



Circuit Schematic (1/2)



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings			Unit
		RNB4580	RNB4580F	RNB4580RP	
Supply Voltage	V _{CC}	18	18	18	V
	V _{EE}	-18	-18	-18	V
Differential input voltage	V _{IN} (diff)	±30	±30	±30	V
Common mode input voltage	V _{CM} *3	±15	±15	±15	V
Power dissipation	P _T	670 *1	385 *2	385 *2	mW
Operating temperature	Topr	-40 to +85	-40 to +85	-40 to +85	°C
Storage temperature	Tstg	-55 to +125	-55 to +125	-55 to +125	°C

- Notes: 1. This is the allowable value up to Ta = 45°C. Derate by 8.3 mW/°C above that temperature.
 2. These are the allowable values up to Ta = 60°C mounting on 40mm × 40mm × 1.6mm (t) 10% wiring density glass epoxy board. Derate by 5.9 mW/°C above that temperature.
 3. If the supply voltage is less than ±15 V, input voltage should be less than supply voltage.

Electrical Characteristics

(Ta = 25°C, V_{CC} = +15 V, V_{EE} = -15 V, unless otherwise specified)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Input offset voltage	V _{IO}	—	0.5	3	mV	R _S ≤ 10 kΩ
Input offset current	I _{IO}	—	5	100	nA	
Input bias current	I _{IB}	—	150	500	nA	
Supply current	I _{CC}	—	4	7	mA	
Power supply rejection ratio	PSRR	80	110	—	dB	R _S ≤ 10 kΩ
Voltage gain	A _V	90	110	—	dB	R _L ≥ 2 kΩ, V _O = ±10 V
Common mode rejection ratio	CMR	80	110	—	dB	R _S ≤ 10 kΩ, V _{CM} = 0 V to V _{CC} = -1.5 V
Output sink current	I _{OSINK}	—	80	—	mA	V _{IN(-)} = 1 V, V _{IN(+)} = 0 V, V _O = 2 V
Output source current	I _{OSOURCE}	—	45	—	mA	V _{IN(-)} = 0 V, V _{IN(+)} = 1 V, V _O = 2 V
Input common mode voltage range	V _{ICM}	±12	±13.5	—	V	
Slew rate	SR	—	7	—	V/μs	
Equivalent input noise voltage	V _{NI}	—	0.7	—	μVrms	RIAA, R _S = 2.2 kΩ, 30 kHz LPF
Gain bandwidth product	GBP	—	15	—	MHz	f = 10 kHz, R _L = 2 kΩ
Total harmonic distortion	THD	—	0.0005	—	%	A _V = 20 dB, V _O = 5 V, R _L = 2 kΩ, f = 1 kHz

Table of Graphs

Electrical Characteristics		Figure
Open loop voltage gain	vs. Frequency f	1
Output swing voltage	vs. Frequency f	2
Output swing voltage	vs. Load resistance R_L	3
Equivalent input noise voltage	vs. Frequency f	4
Input offset voltage	vs. Temperature T_a	5
Total harmonic distortion	vs. Output Voltage V_o	6
Maximum output voltage swing	vs. Operating voltage V	7
Operating current	vs. Temperature T_a	8

Typical Characteristics Curves

Figure 1. Open Loop Voltage Gain vs. Frequency

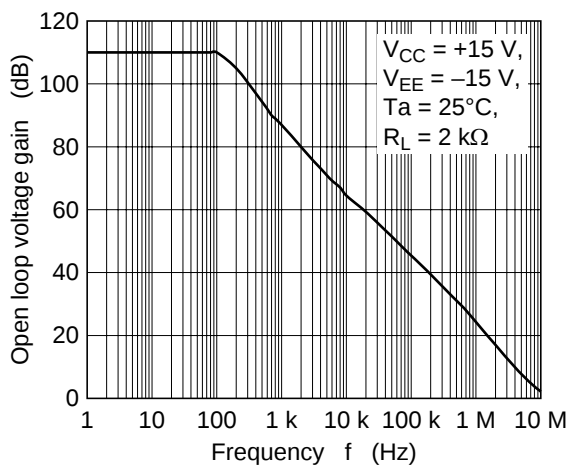


Figure 2. Output Swing Voltage vs. Frequency

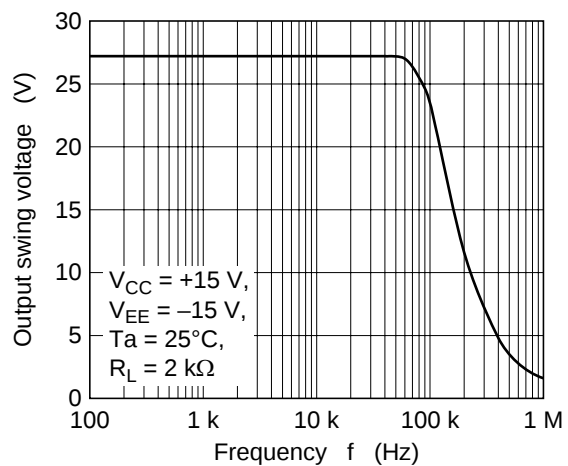


Figure 3. Output Swing Voltage vs. Load Resistance

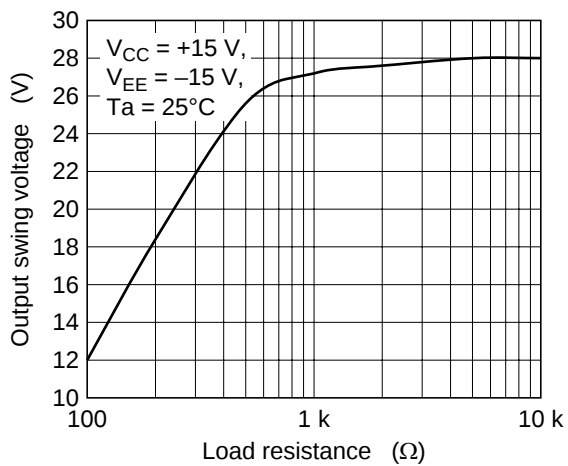


Figure 4. Equivalent Input Noise Voltage vs. Frequency

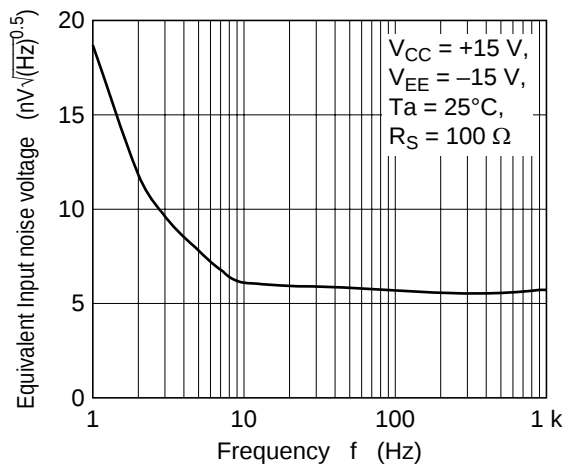


Figure 5. Input Offset Voltage vs. Temperature

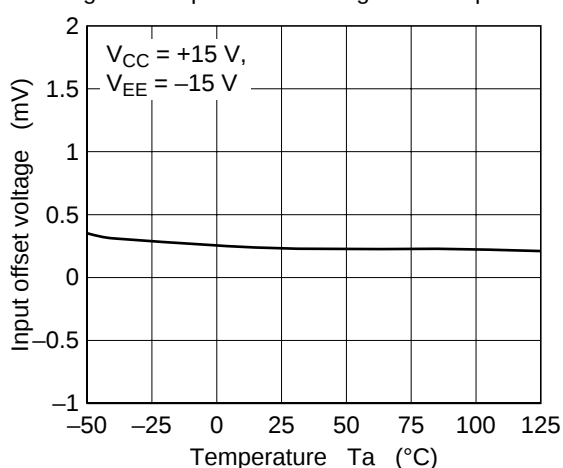
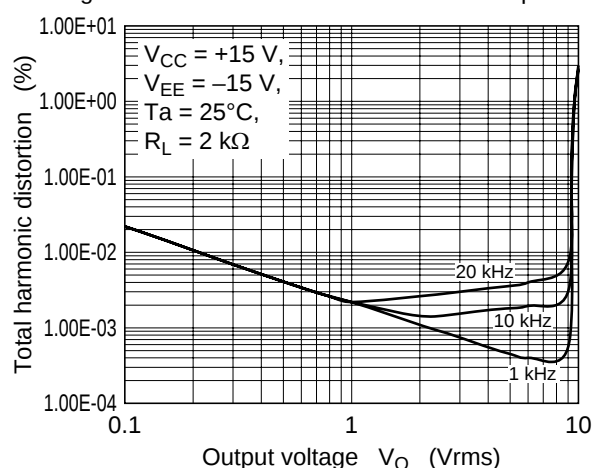


Figure 6. Total Harmonic Distortion vs. Output Voltage



Typical Characteristics Curves (cont.)

Figure 7. Maximum Output Swing Voltage vs. Operating Voltage

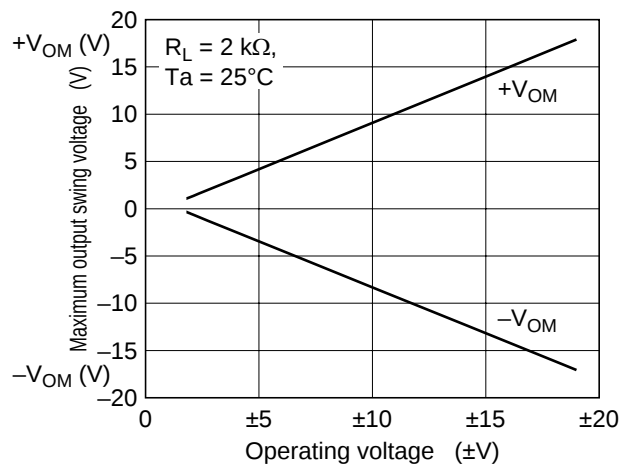
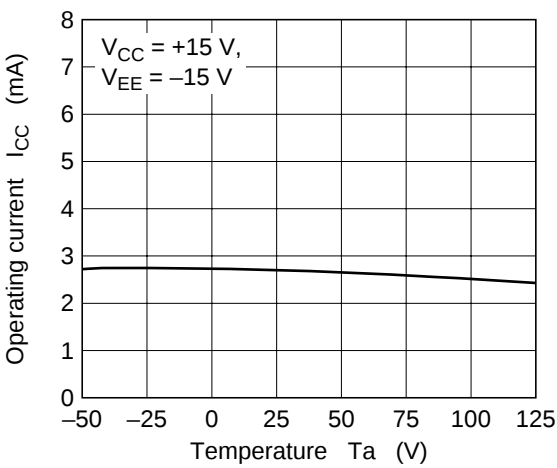
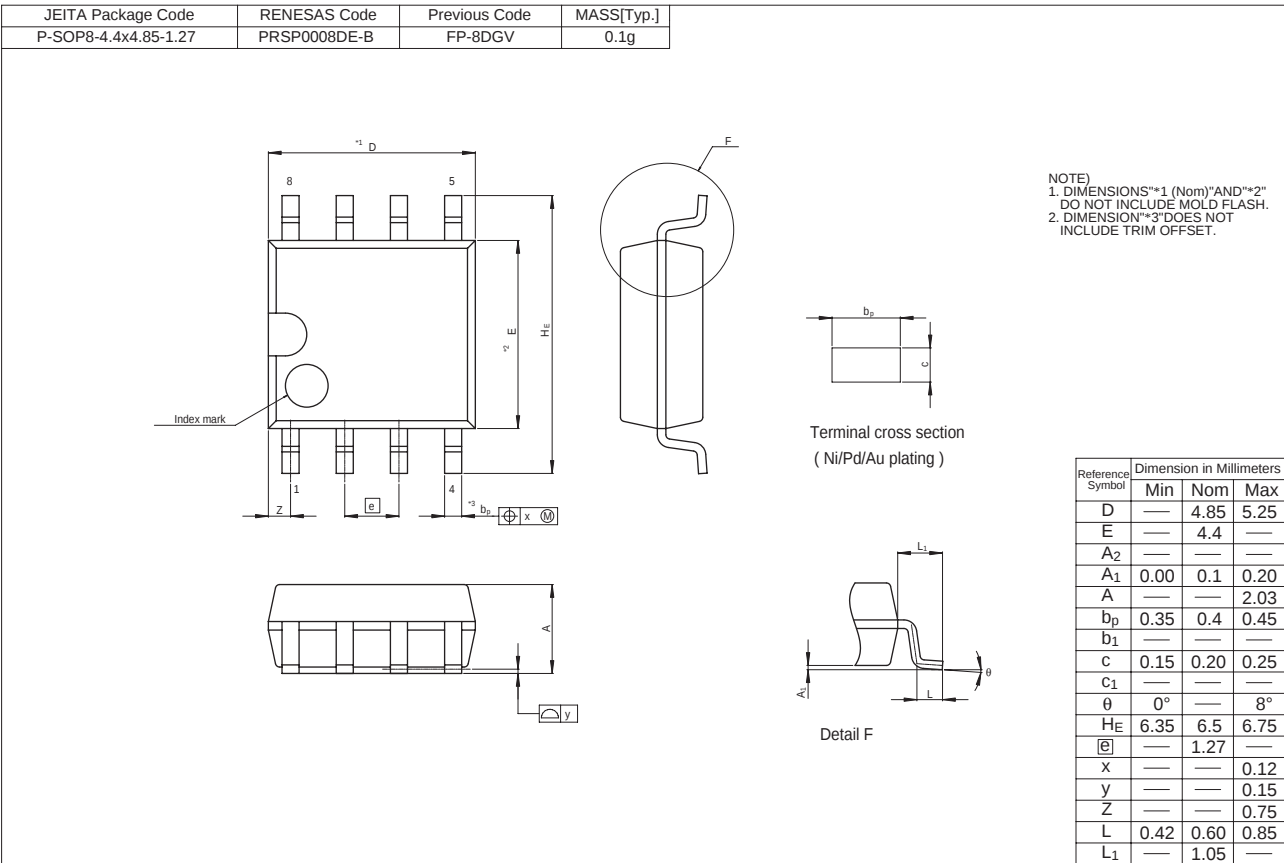
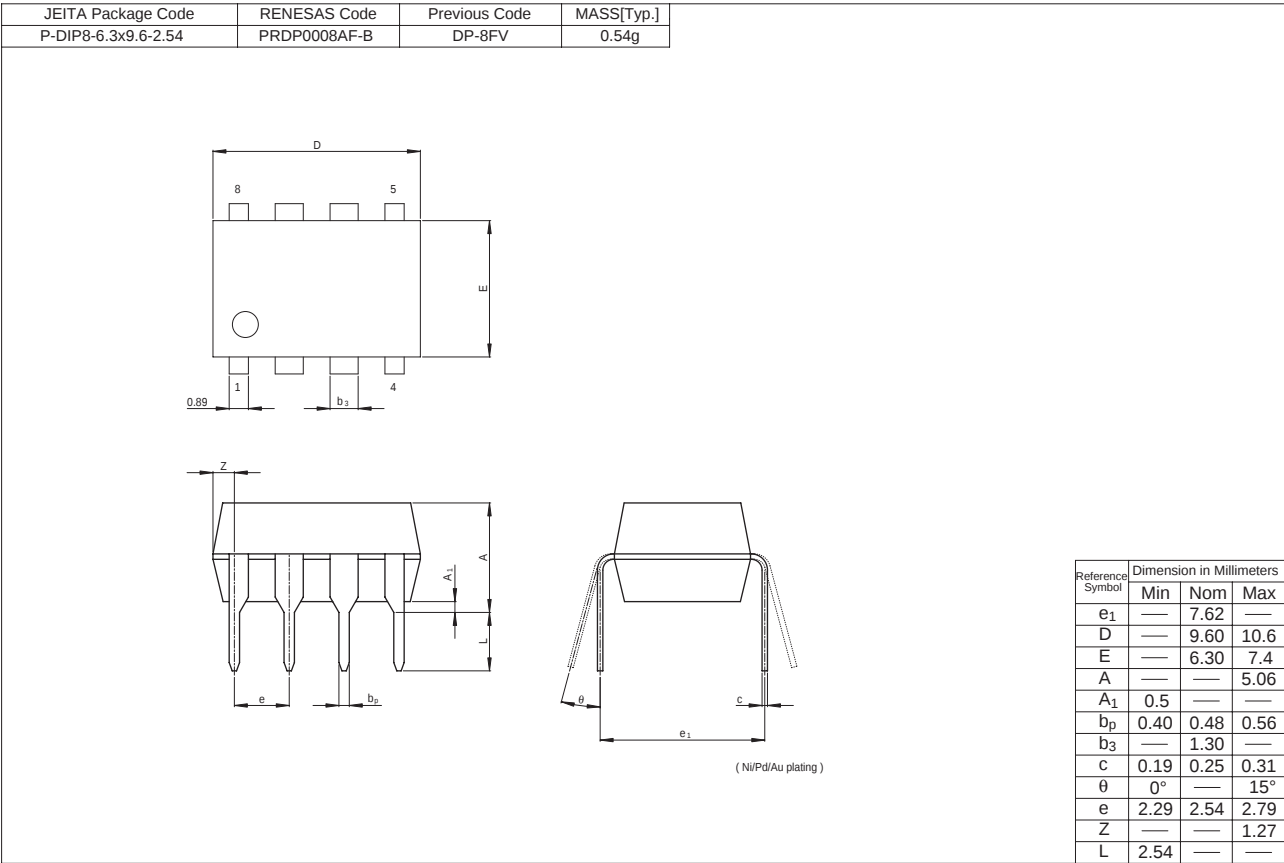


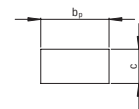
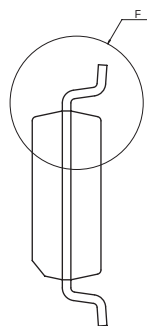
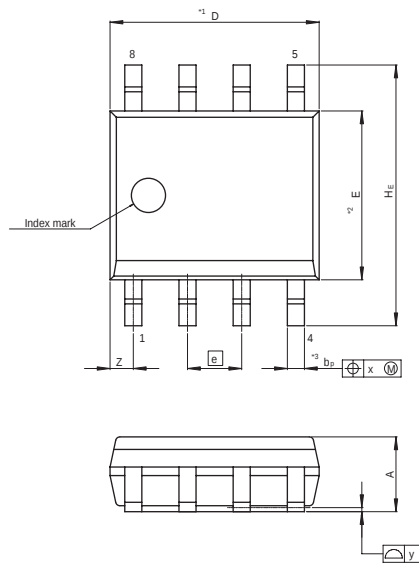
Figure 8. Operating Current vs. Temperature



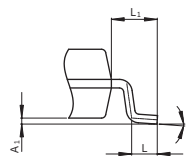
Package Dimensions



JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-SOP8-3.95x4.9-1.27	PRSP0008DD-C	FP-8DCV	0.085g



Terminal cross section
(Ni/Pd/Au plating)



Detail F

NOTE)
1. DIMENSIONS**1 (Nom)"AND"*2"
DO NOT INCLUDE MOLD FLASH.
2. DIMENSION**3"DOES NOT
INCLUDE TRIM OFFSET.

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	—	4.90	5.30
E	—	3.95	—
A ₂	—	—	—
A ₁	0.10	0.14	0.25
A	—	—	1.75
b _p	0.34	0.40	0.46
b ₁	—	—	—
c	0.15	0.20	0.25
c ₁	—	—	—
θ	0°	—	8°
H _E	5.80	6.10	6.20
Ⓢ	—	1.27	—
x	—	—	0.25
y	—	—	0.10
z	—	—	0.75
L	0.40	0.60	1.27
L ₁	—	1.08	—

Notes:

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Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510