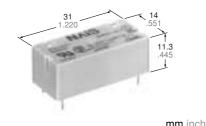




## IC DRIVABLE PC BOARD RELAY FOR FIELD LOAD SWITCHING

# ST RELAYS



## FEATURES

- Sealed to meet the combination process of automatic wave soldering and cleaning needs
- Latching types available
- High switching capacity and high sensitivity in subminiature size
  150 mW pick-up, 8 A inrush capacity:
  51 A for 1a1b, 35 A for 2a
- High shock and vibration resistance Shock: 20 G, Vibration: 10 to 55 Hz at double amplitude of 2 mm

5 to 85% B.H.

Approx. 10g .353 oz

## **SPECIFICATIONS**

## Contacts

Arrangement			1 Form A 1 Form B	2 Form A	
Contact mate	erial		Gold flash over silver alloy		
Initial contac	t resistance	, max.	30 mΩ		
Rating (resistive)	Max. switching power		2,000 VA, 150 W		
	Max. switching voltage		380 V AC, 250 V DC		
	Max. switc	hing current	8 A		
	Min. switcl	ning capacity#1	100 mA, 5 V DC		
HP rating			1/4 HP 125, 250 V AC		
Inrush current capability			51 A (TV-3 equivalence) for 1a1b 35 A (TV-1 equivalence) for 2a		
Expected life (min. operations)	Mechanical (at 180 cpm)		107		
	Electrical	8 A 250 V AC (resistive)	10⁵		
		5 A 30 V DC (resistive)	2×105		
		3 A 100 V AC (lamp)	$3 \times 10^4$	_	
		1 A 100 V AC (lamp)	—	3 × 104	

## Coil (polarized) (at 25°C 77°F)

Single side stable	Nominal operating power	Approx. 240 mW
Latching	Nominal set and reset power	Approx. 240 mW

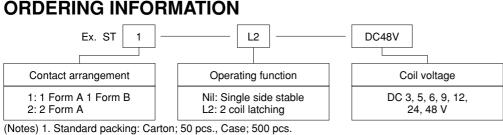
#1 This value can change due to the switching frequency, environmental conditions, and desired reliability level, therefore it is recommended to check this with the actual load.

### Remarks

- \*1 Measurement at same location as "Initial breakdown voltage" section
- \*2 Detection current: 10 mA
- $^{\star_3}$  Wave is standard shock voltage of  $\pm 1.2 \times 50 \mu s$  according to JEC-212-1981
- \*4 Excluding contact bounce time
- $^{*5}$  Half-wave pulse of sine wave: 11ms; detection time: 10  $\mu s$
- \*6 Half-wave pulse of sine wave: 6ms
- \*7 Detection time: 10µs
- \*® Refer to 6. Conditions for operation, transport and storage mentioned in AMBIENT ENVIRONMENT

# TYPICAL APPLICATIONS

Sequence controllers, facsimiles, telephone controls, remote control security devices and security equipment.



2. 1 coil latching type available.

#### Max. operating speed 20 cpm (at rated load) Initial insulation resistance\*1 1,000 MΩ (at 500 V DC) Between contact sets 2,000 Vrms Initial breakdown Between open contacts 1,200 Vrms voltage\*2 Between contacts and coil 3,750 Vrms Surge voltage between coil and Min. 6,000 V contact\*3 Operate time\*4 Max. 15 ms (Approx. 10 ms) (at nominal voltage) Release time (without diode)\*4 Max. 10 ms (Approx. 8 ms) (at nominal voltage) Set time\*4 (latching) Max. 10 ms (Approx. 8 ms) (at nominal voltage) Reset time\*4 (latching) Max. 10 ms (Approx. 8 ms) (at nominal voltage) Temperature rise Max. 55°C with nominal coil voltage (at 60°C) and at 8 A switching current Functional\*5 Min. 196 m/s<sup>2</sup> {20 G} Shock resistance Min. 980 m/s<sup>2</sup> {100 G} Destructive\*6 117.6 m/s<sup>2</sup> {12 G}, 10 to 55 Hz Functional\*7 at double amplitude of 2 mm Vibration resistance 176.4 m/s<sup>2</sup> {18 G}, 10 to 55 Hz Destructive at double amplitude of 3 mm Conditions for operation, Ambient -40°C to +60°C transport and storage\*8 -40°Fto +140°F temp. (Not freezing and condens-

Humidity

ing at low temperature)

Unit weight

Characteristics (at 25°C 77°F 50% Relative humidity)

<sup>\*</sup> Specifications will vary with foreign standards certification ratings.

# ST

# TYPES AND COIL DATA (at 20°C 68°F)

## Single side stable

Part No.		Nominal	Pick-up voltage,	Drop-out	Maximum	Coil resistance.	Nominal
1 Form A 1 Form B	2 Form A	voltage, V DC	V DC (max.)	voltage, V DC (min.)	allowable voltage, V DC (60°C 140°F)	$\Omega$ (±10%)	operating current, mA
ST1-DC3V	ST2-DC3V	3	2.4	0.3	4.5	38	78.9
ST1-DC5V	ST2-DC5V	5	4.0	0.5	7.5	105	47.6
ST1-DC6V	ST2-DC6V	6	4.8	0.6	9.0	150	40
ST1-DC9V	ST2-DC9V	9	7.2	0.9	13.5	360	25
ST1-DC12V	ST2-DC12V	12	9.6	1.2	18.0	600	20
ST1-DC24V	ST2-DC24V	24	19.2	2.4	36.0	2,400	10
ST1-DC48V	ST2-DC48V	48	38.4	4.8	72.0	9,000	5.3

## 2 coil latching

1 Form A	t No. 2 Form A	Nominal voltage, V DC	Set and reset voltage,	Maximum allowable voltage, V DC (60°C 140°F)	Coil resistance, $\Omega$ (±10%)	Nominal operating
1 Form B		_	V DC (max.)	, , , , , , , , , , , , , , , , , , ,	, ,	current, mA
ST1-L2-DC3V	ST2-L2-DC3V	3	2.4	4.5	40	75
ST1-L2-DC5V	ST2-L2-DC5V	5	4.0	7.5	110	45.5
ST1-L2-DC6V	ST2-L2-DC6V	6	4.8	9.0	155	38.7
ST1-L2-DC9V	ST2-L2-DC9V	9	7.2	13.5	360	25
ST1-L2-DC12V	ST2-L2-DC12V	12	9.6	18.0	640	18.8
ST1-L2-DC24V	ST2-L2-DC24V	24	19.2	36.0	2,400	10
ST1-L2-DC48V	ST2-L2-DC48V	48	38.4	72.0	10,200	4.7

PC board pattern

(Copper-side view)

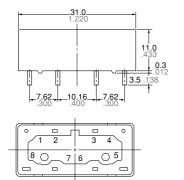
8-DIA. 1.4

2 coil latching types only.

2.54

Tolerance: ±0.1 ±.004

## DIMENSIONS



General tolerance:  $\pm 0.2 \pm .008$ 

10.16

mm inch

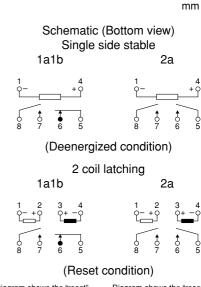
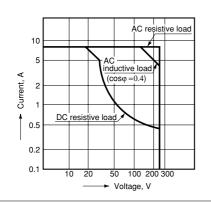


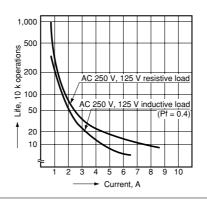
Diagram shows the "reset" position when terminals 3 and 4 are energized. Energize terminals 1 and 2 to transfer contacts. Diagram shows the "reset" position when terminals 3 and 4 are energized. Energize terminals 1 and 2 to transfer contacts.

## **REFERENCE DATA**

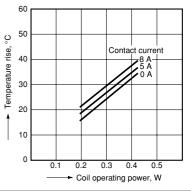




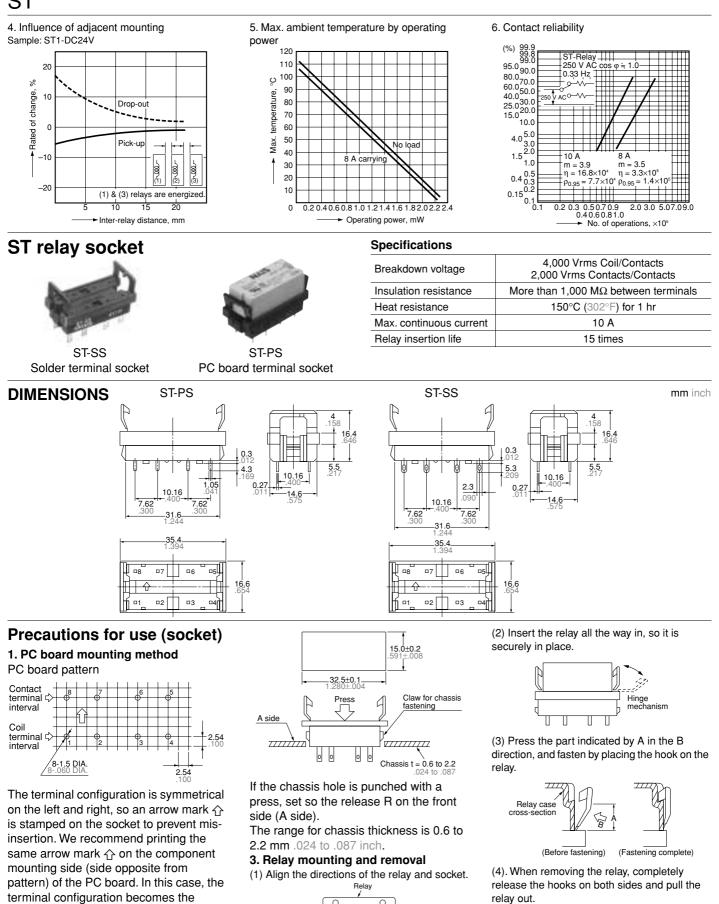
2. Life curve



3. Coil temperature rise Sample: ST1-DC24V







# For Cautions for Use, see Relay Technical Information

terminal nos. noted near the drilling

holes.

2. Chassis cutout

Chassis cutting dimensions