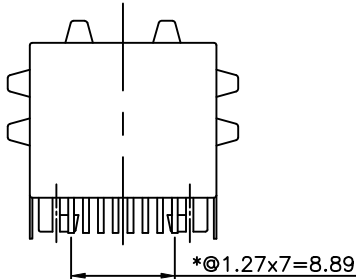
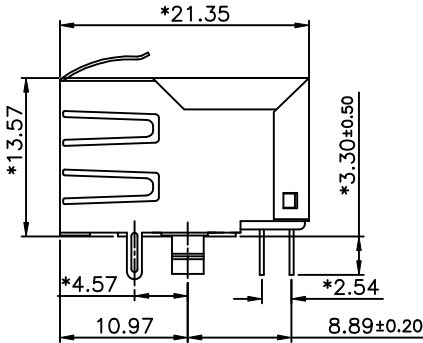
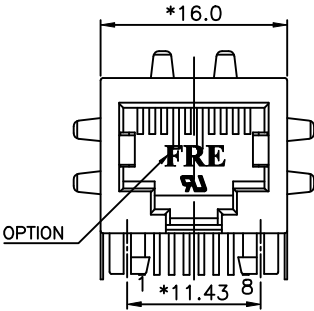


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



- NOTES:
- ELECTRICAL:
- 1. VOLTAGE RATING : 125VAC RMS.
 - 2. CURRENT RATING : 1.5AMP.
 - 3. CONTACT RESISTANCE : 40 MILLIOHMS MAX.
 - 4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
 - 5. DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.
 - 6. CATEGORY 5 CHARACTERISTIC:

Frequency	Near-End Cross Talk dB, MIN.	Return Loss dB, MIN.	Attenuation dB, MAX.
1.0 MHZ	65	23	0.1
4.0 MHZ	65	23	0.1
8.0 MHZ	62	23	0.1
10.0 MHZ	60	23	0.1
16.0 MHZ	56	23	0.2
20.0 MHZ	54	23	0.2
25.0 MHZ	52	14	0.2
31.25 MHZ	50	14	0.2
62.5 MHZ	44	14	0.3
100.0 MHZ	40	14	0.4

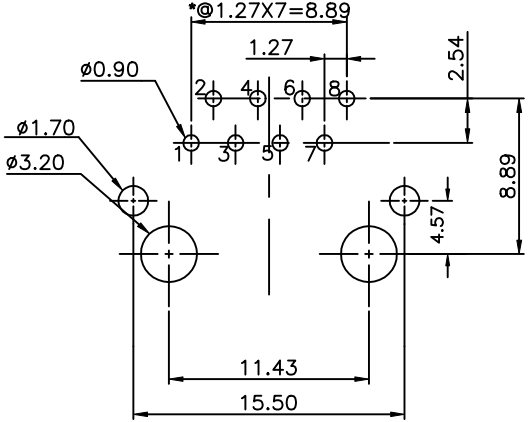
- MECHANICAL:
- 1. HOUSING MATERIAL : FR52 UL94V-0.
 - 2. CONTACT MATERIAL : PHOSPHOR BRONZE t=0.35mm.
 - 3. CONTACT PLATING : GOLD PLATING OVER NICKEL.
 - 4. OPERATING LIFE : 750 CYCLES MIN.
 - 5. PCB RETENTION PRE-SOLDER : 1 LB MIN.
 - 6. PCB RETENTION POST-SOLDER: 10 LBS MIN.

- ENVIRONMENTAL:
- 1. STORAGE : -40°C TO +85°C.
 - 2. OPERATION: 0°C TO +70°C.
 - 3. WAVE SOLDERING TEMPERATURE: 255°C~265°C (5~10SECONDS)

MATES WITH MODULAR PLUG CONFORMING TO FCC PART 68, SUBPART F.

PART NUMBER: E6588-XBL18X-L

- 1-W/FRE LOGO
- 2-W/O FRE LOGO
- 1-3u"
- 3-15u"
- 2-6u"
- 4-30u"
- 5-50u"



PC Board Layout Component Side Shown

DETACHED LISTS	MM (INCH)	DFTO YICAN	DATE 08/19/05	FULL RISE ELECTRONIC CO., LTD	
	TOLERANCES EXCEPT AS NOTED	CHKD SAMMY	DATE 2005/08/19		
	MM	MFO	DATE	TITLE	
	.0 ±0.2	APPVLLUSHENG	DATE 2005/08/19	CAT.5 SHIELDED PCB JACK	
	.00 ±0.15	MATERIAL :		DRAWING NO. GE653090-LA	
ANGLES ± 0.5	±	QT*Y :		/PART NO. SEE NOTE	
THIRD ANGLE PROJECTION	±	FINISH :		DO NOT SCALE DRAWING	
		SCALE :		SHEET 1 OF 1	