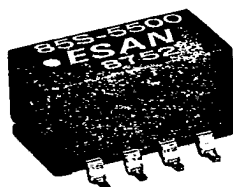


Surface Mounting Delay Line (Active)

85S Series

- 5 Tap Output
- 8 Pin DIL Package
- TTL Compatible

**Specifications:**

- Delay tolerance : $\pm 5\%$ or 2ns
whichever is greater
- Rise time : 4ns max.
- Temp. coefficient : 100 ppm/ $^{\circ}\text{C}$
- Operating Temp. Range: -0°C to 70°C
- Minimum Pulse Width : 40% of total delay
- Supply Voltage : 5Vdc $\pm 5\%$
- Logic 1 Input current : 50 μA max.
- Logic 0 Input current : -2 mA max.
- Logic 1 Output : 2.75V min.
- Logic 0 Output : 0.4V max.
- Logic 1 Fan out : 20/TAP max.
- Logic 0 Fan out : 10/TAP max.
- Power Dissipation : 375 mW max.

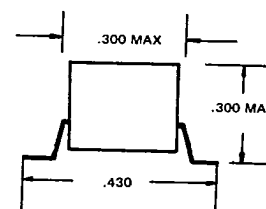
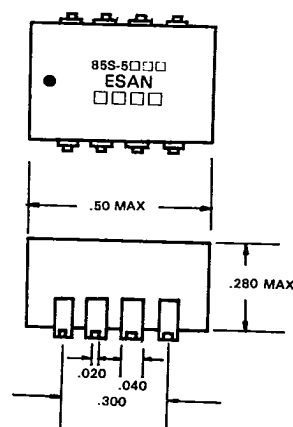
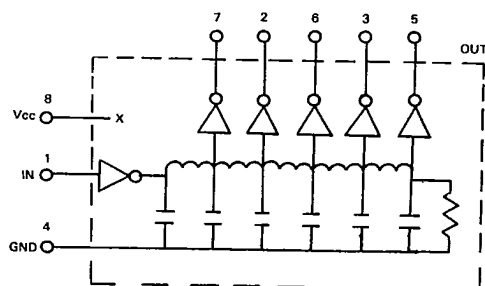
Test Conditions:

- Input pulse voltage : 3.2V
- Input pulse width : $1.2 \times$ total delay
- Input pulse spacing : $2 \times$ total delay
- Input pulse rise time : 2 ns
- Rise time : 0.75V to 2.4V
- Time delay measured : @1.5V level
- All measurement : @Vcc = 5.0V, $T_A = 25^{\circ}\text{C}$

Features:

- Schottky TTL interface
- Low power schottky available named "85SL-5xxx" series
- Both Leading and Trailing edge available under request named "85SB-5xxx" series

PART NO.	TOTAL DELAY (ns)	TAP TO TAP (ns)
85S-5250	25	5
85S-5500	50	10
85S-5750	75	15
85S-5101	100	20
85S-5125	125	25
85S-5151	150	30
85S-5201	200	40
85S-5251	250	50
85S-5301	300	60
85S-5401	400	80
85S-5501	500	100



Unit: Inch