PJA3431

20V P-Channel Enhancement Mode MOSFET – ESD Protected

Voltage	-20 V	Current	-1.5A

SOT-23 Unit: inch(mm)

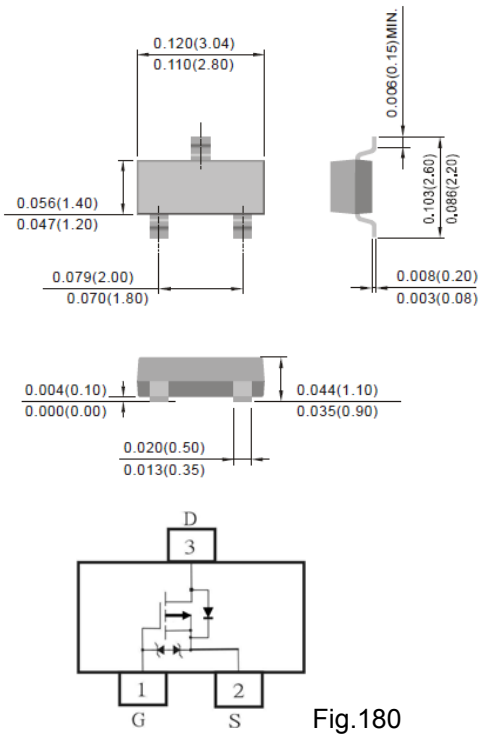

Fig.180
(TOP VIEW)

Features

- $R_{DS(ON)}$, V_{GS}@-4.5V, I_D@-1.5A<325mΩ
- $R_{DS(ON)}$, V_{GS}@-2.5V, I_D@-1.2A<420mΩ
- $R_{DS(ON)}$, V_{GS}@-2.5V, I_D@-0.5A<600mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected
- Lead free in comply with EU RoHS 2011/65/EU directives.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams
- Marking : A31

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	±8	V
Continuous Drain Current		I_D	-1.5	A
Pulsed Drain Current (Note 4)		I_{DM}	-4	A
Power Dissipation	T_a=25°C	P_D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Thermal resistance - Junction to Ambient (Note 3)		$R_{\theta JA}$	100	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=-250uA	-0.5	-0.64	-1.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=-4.5V, I_D=-1.5A	-	240	325	mΩ
		V_{GS}=-2.5V, I_D=-1.2A	-	295	420	
		V_{GS}=-1.8V, I_D=-0.5A	-	405	600	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-20V, V_{GS}=0V	-	-0.02	-1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0V	-	±3.5	±10	uA
Dynamic						
Total Gate Charge	Q_g	V_{DS}=-10V, I_D=-1.5A, V_{GS}=-4.5V (Note 1,2)	-	1.7	-	nC
Gate-Source Charge	Q_{gs}		-	0.35	-	
Gate-Drain Charge	Q_{gd}		-	0.43	-	
Input Capacitance	Ciss	V_{DS}=-10V, V_{GS}=0V, f=1.0MHZ	-	165	-	pF
Output Capacitance	Coss		-	25	-	
Reverse Transfer Capacitance	Crss		-	14.7	-	
Switching						
Turn-On Delay Time	$td_{(on)}$	V_{DD}=-10V, I_D=-1.5A, V_{GS}=-4.5V, R_G=6Ω (Note 1,2)	-	11	-	ns
Turn-On Rise Time	tr		-	38	-	
Turn-Off Delay Time	$td_{(off)}$		-	130	-	
Turn-Off Fall Time	tf		-	75	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	-1.6	A
Diode Forward Voltage	V_{SD}	I_S=-1.6A, V_{GS}=0V		-1.03	-1.2	V

NOTES:

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.

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TYPICAL CHARACTERISTIC CURVES

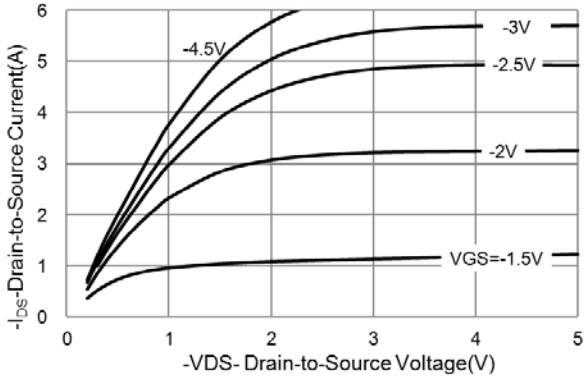

Fig.1 On-Region Characteristics

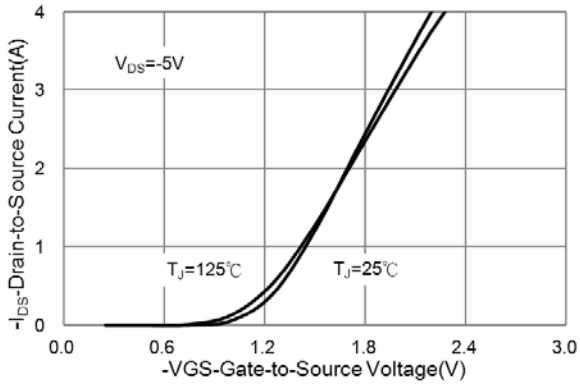

Fig.2 Transfer Characteristics

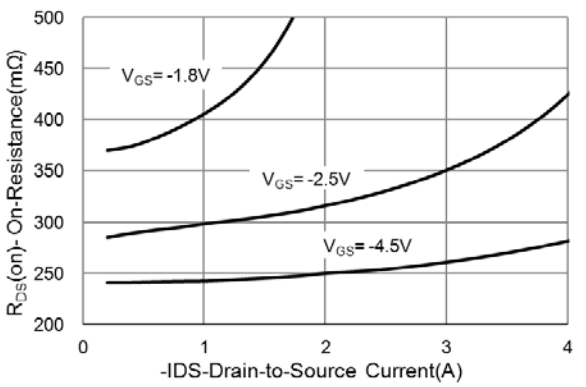

Fig.3 On-Resistance vs. Drain Current

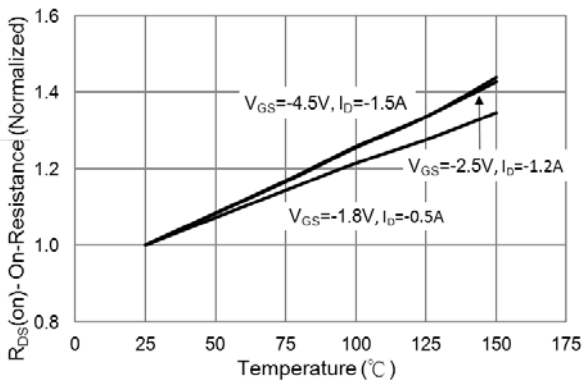

Fig.4 On-Resistance vs. Junction temperature

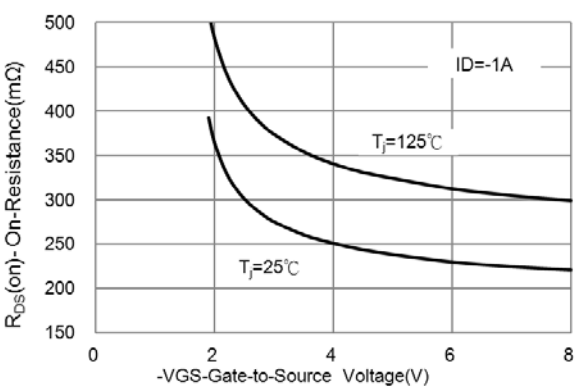

Fig.5 On-Resistance Variation with VGS.

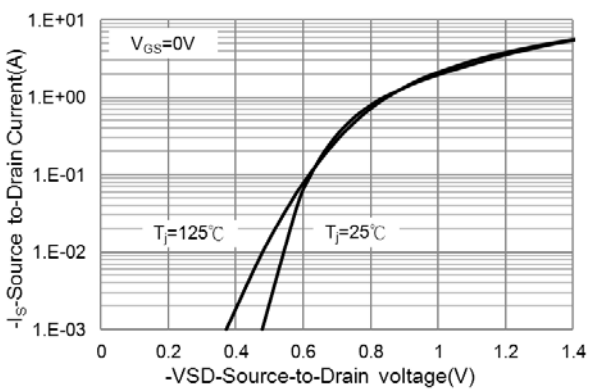

Fig.6 Body Dlode CharacterIsIcs

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TYPICAL CHARACTERISTIC CURVES

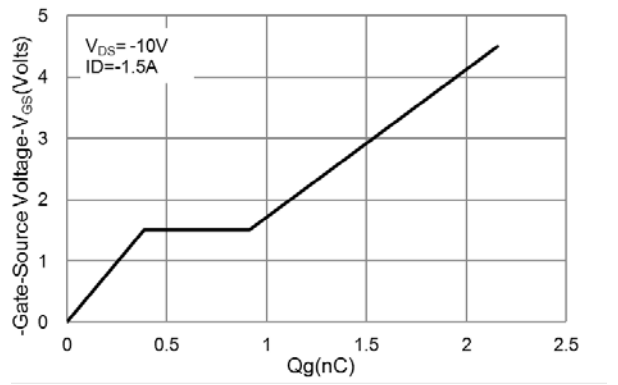

Fig.7 Gate-Charge Characteristics

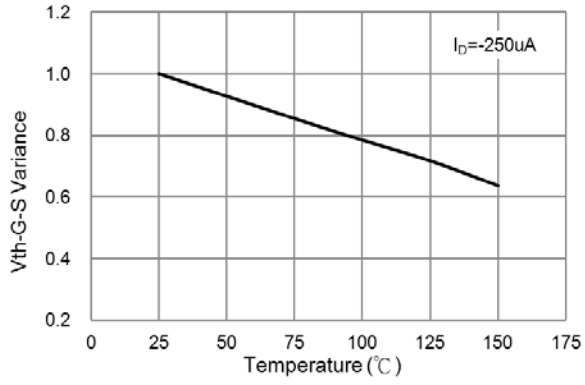

Fig.8 Threshold Voltage Variation with Temperature.

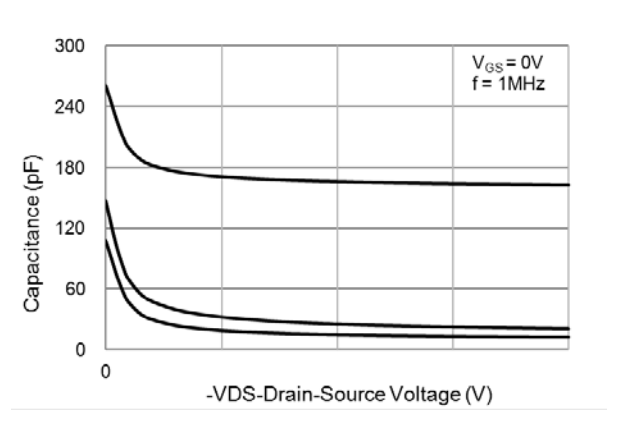

Fig.9 Capacitance vs. Drain-Source Voltage.

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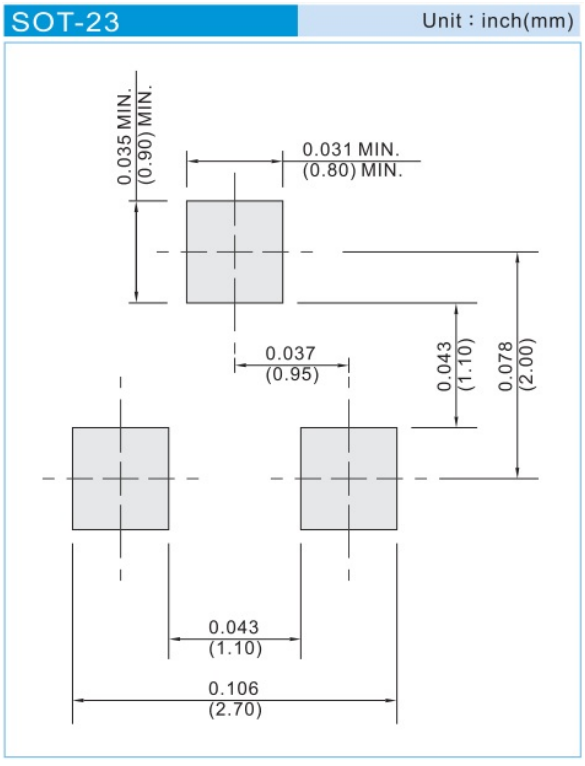
PART NO PACKING CODE VERSION

PART NO PACKING CODE VERSION	Package Type	Packing type	Marking	Version
PJA3431_R1_00001	SOT-23	3K pcs / 7” reel	A31	Halogen free
PJA3431_R2_00001	SOT-23	12K pcs / 13” reel	A31	Halogen free

MOUNTING PAD LAYOUT



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