LOW PROFILE PC Board Mount OCXOs

FEATURES:

■ CO-740: 2" x 2" x ½"; pin for pin compatible with our

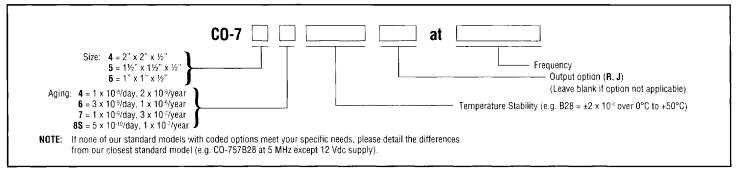


Contact factory for better temperature stability requirements

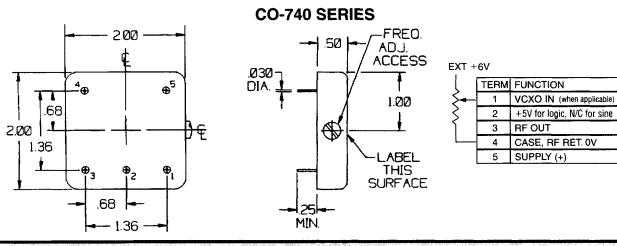
CO-711 series ■ CO-750: 1½" x 1½" x ½" **CO-760 SERIES** CO-740 SERIES **CO-750 SERIES** 1½" x 1½" x ½" ■ CO-760: 1" x 1" x ½" 2" x 2" x ½" 1" x 1" x ½" Sinewave Output Sinewave Output 5V Logic FREQUENCY 5 MHz - 25 MHz AT cut Crystal SC/IT cut Crystal AT cut Crystal SCAT out Crystal AT cut Crystal **STABILITY** Temperature A58: ±5 x 10-8 A28: ±2 x 10-8 A58: ±5 x 10-8 A28: ±2 x 10-8 A17: $\pm 1 \times 10^{-7}$ +15°C to +35°C A18: ±1 x 10-8 A59: ±5 x 10⁻⁹ A18: ±1 x 10-8 A59: ±5 x 109 (Temp Range A) 0° C to $+50^{\circ}$ C B17: ±1 x 10-7 **B58**: ±5 x 10⁻⁸ (Temp Range B) B17: ±1 x 10-7 B58: ±5 x 10-8 **B27**: ±2 x 10⁻⁷ B28: ±2 x 10-8 B18: ±1 x 10-8 B28: ±2 x 108 B18: ±1 x 10-8 D27: ±2 x 10-7 (Temp Range D) -20° C to $+70^{\circ}$ C **D27**: $\pm 2 \times 10^{-7}$ D17: ±1 x 10-7 D17: ±1 x 10-7 **D57**: ±5 x 10⁻⁷ D58: ±5 x 10-8 D28: ±2 x 10-8 D58: ±5 x 10⁻⁸ D28: ±2 x 10° $(\pm 2.5 \times 10^{-7} \text{ O}/70^{\circ}\text{C} \text{ avail. on special order})$ (Temp Range E) -40° C to $+75^{\circ}$ C E57: ±5 x 10-7 E27: ±2 x 10-7 E57: ±5 x 10-7 E27: ±2 x 10-7 N/A E17: ±1 x 10-7 E58: ±5 x 10-8 E17: ±1 x 10-7 E58: ±5 x 10-8 CO-754: 1 x 10 3/day 2 x 10 5/year CO-757: 1 x 10 9/day 3 x 10 7/year CO-758\$: 5 x 10 *9/day 1 x 10 */year (5 x 10 */year optional) CO-748S: 5 x 10⁻¹⁰/day **Aging Rate** CO-744: 1 x 10-8/day CO-764: 1 x 10⁻⁸/day (2 x 10⁻⁶/year) 2 x 10⁻⁶/year **CO-747**: 1 x 10⁻⁹/day 3 x 10⁻⁷/year 1 x 10-7/year (5 x 10-8/year optional) CO-766: 3 x 10-9/day (1 x 10-6/year) vs Supply 5 x 10⁻⁹ per percent 2 x 10-9 per percent 5 x 10⁻⁹ per percent 2 x 10° per percent 1 x 10-7 per percent

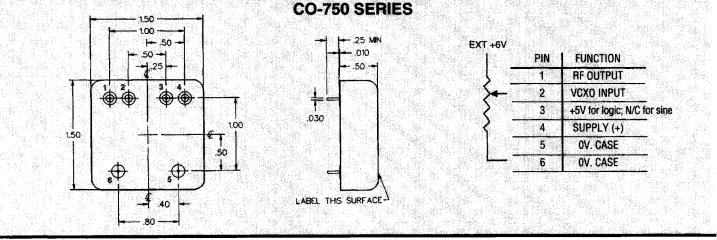
Short Term (Allan Variance)	5 x 10 ⁻¹¹ /second	1 x 10 ⁻¹¹ /second	5 x 10 11/second	1 x 10 ⁻¹⁷ /second	1 x 10 [.] 9/s	econd	
OUTPUT / SUPPLY VOLTAGE	R: J:	Output >0.5Vrms/50Ω (+7 dBm) >1.0Vrms/50Ω (+13 dBm) **HCMOS/TTL *Any rives 3 TTL loads, 10LS TTI	* + 15Vdc & + 5Vdc y voltage in 12-24 Vdc rang	. ,	Output **Std: HCMOS/TTL **Drives 3 TTL loads, 10 HCMOS (output is fron		
Harmonics (Sinewave Output)		>20 dB below desired output is used, sub-harmonic	ut. If internal multiplication	and the state of t	N/A		
Input Power	5 watts at turn-on; <2.5 watts stabilized @ 25°C Higher power required for -20°C to +70°C and lower power needed for 0°C to 50°C. 3 watts at turn-on; <2.5 wat						
PHASE NOISE (typical) (Sinewave Output 5-12 MHz) Offset 10 Hz 100 Hz 1 kHz 10 kHz 50 kHz	Phase Noise -105 dBc/Hz -135 dBc/Hz -145 dBc/Hz -148 dBc/Hz -150 dBc/Hz	Phase Noise -115 dBc/Hz -140 dBc/Hz -145 dBc/Hz -145 dBc/Hz -148 dBc/Hz -150 dBc/Hz	Phase Noise -105 dBc/Hz -135 dBc/Hz -145 dBc/Hz -148 dBc/Hz -150 dBc/Hz	Phase Noise -115 dBc/Hz -140 dBc/Hz -145 dBc/Hz -148 dBc/Hz -148 dBc/Hz -150 dBc/Hz		Contact Factory	
FREQUENCY ADJUST	Mechanical: Range for settable to Electrical: VCXO cor	o 1 x 10 ⁻⁸ (5 x 10 ⁻⁹ with SC).	10 x10° minimum range for 0 to +6V control via external potentiometer	2 ×10° minimum range for 0 to +6V control via external potentiometer	10 x10° minimum range for 0 to +5V control		
CONFIGURATION/ MECHANICAL PACKAGE (see page 52)	l ''	2" x 2" x ½", pins for PCB mounting (51 x 51 x 12.7 mm)		1½" x 1½" x ½", pins for PCB mounting (38 x 38 x 12.7 mm)		1" x 1" x ½", pins for PCB mounting (25.4 x 25.4 x 12.7 mm)	
ENVIRONMENTAL	see general environmental specifications on page 98						
HOW TO ORDER		see page 52					

HOW TO ORDER

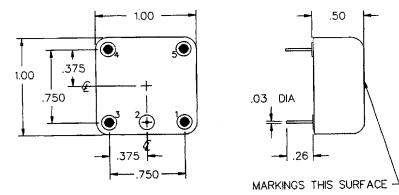


OUTLINE/INSTALLATION DRAWINGS





CO-760 SERIES



	PIN	FUNCTION
	1	RF OUTPUT
	2	OV. CASE
-	3	VCXO INPUT
	4	N/C
-	5	SUPPLY (+)

Markings do not appear on oscillators; they are for reference only. Dimensions are in inches. Case dimension tolerances are \pm .02"