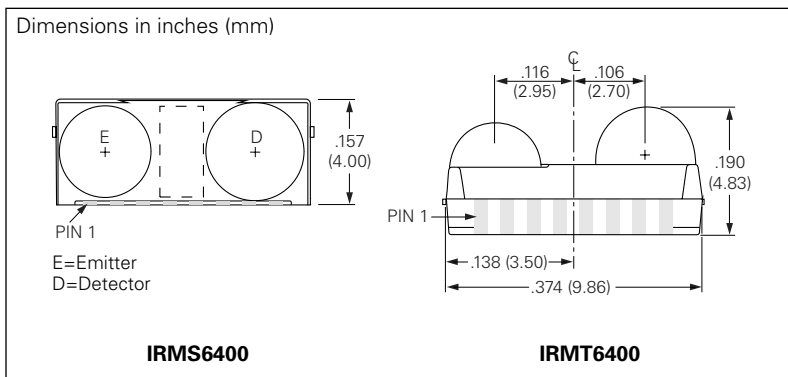
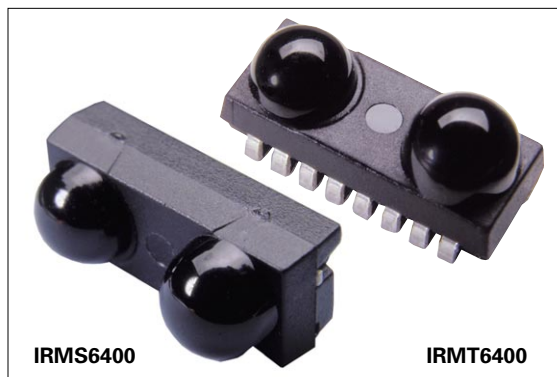




SIDE VIEW IRMS6400

TOP VIEW IRMT6400

4 Mb/s Infrared Data Transceiver



FEATURES

- Compliant with IrDA Specification
- Data Rates 9.6 Kb/s to 4.0 Mb/s
- Wide Range of Supply Voltage 2.7 to 5.5 V
- Standby Current 3.3 mA at 5.0 V Typical
- Shut Down Current 0.01 μ A Typical
- Excellent Power Supply Noise Rejection
- Tri-State Receiver Output and TxD Disable
- AC Coupled Transmit Input
 - Provides Integrated Protection for Eye Safety
- High DC Ambient Rejection
- Independent LED Supply, Anode Pin Can Take
 - up to 9.0 V DC when not Transmitting and
 - up to 4.0 V above V_{CC} when Transmitting
- Receiver Latency Less than 100 μ s
- Slimline Package:
 - H 4.0 mm x D 4.8 mm x L 9.8 mm

DESCRIPTION

With state of the art submicron BiCMOS circuitry, coupled with Infineon optoelectronic expertise, the IRMS6400/IRMT6400 outperforms its closest rival. Infineon has incorporated a mode selection pin that toggles the device to operate in FIR (4 Mb/s) or SIR/MIR (9.6 Kb/s to 1.152 Mb/s) mode. Its external Shut Down (SD) feature cuts current consumption to typically 0.01 μ A. The transmit input is AC coupled, limiting transmit pulse duration to 70 μ s, preventing transmitter damage and continuous LED output. Mode select determines the data rate. Low for 9.6 Kb/s to 1.152 Mb/s, high for 4 Mb/s.

Absolute Maximum Ratings, $T_A=25^\circ\text{C}$ (except where noted)

Supply Voltage Range, all states, V_{CC} -0.5 to +7.0 V
 LED Anode Voltage, not transmitting, V_{LEDA} 9.0 V
 LED Anode Voltage, transmitting, V_{LEDA} 4.0 V + V_{CC}
 Input Currents, (pin 3 and 5)..... 20 mA
 Output Sinking Current..... 50 mA
 Storage Temperature, T_S -40 to +100°C
 Ambient Temperature, operating, T_A -25 to +85°C
 Lead Solder Temperature, 230°C <10 s
 IC Junction Temperature, T_J 125°C
 Average IR LED Current, DC, I_{LED} 100 mA
 Repetitive Pulsed IR LED Current,
 <90 μ s, t_{ON} <25%, $I_{LED(RP)}$ 700 mA
 Transmit Data Input Voltage, V_{TXD} -0.5 to V_{CC} + 0.5 V
 Receive Data Output Voltage, V_{RXD} -0.5 to V_{CC} + 0.5 V

Table 1. Pin Functions

Pin no.	Function	Pin no.	Function
1	LED Anode	5	SD/Mode
2	LED Cathode	6	V_{CC}
3	TxD	7	*Do not connect
4	RxD	8	GND

*Pin 7 internally grounded

The block diagram illustrates the internal architecture of the ADXL010, which functions as both a receiver and a transmitter. Key components and their interconnections include:

- Photodiode:** Receives incoming optical signals and converts them into a current.
- Ambient DC Cancelling:** A feedback loop that compensates for background light levels to improve signal-to-noise ratio.
- AGC (Automatic Gain Control):** Monitors the signal level and adjusts the gain of the Preamplifier and Main Amplifier to maintain a consistent output level.
- Preamplifier and Main Amplifier:** Two-stage amplification blocks that boost the photodiode signal.
- Bandwidth Control:** A switchable filter that selects between different bandwidths (e.g., 100 kHz, 10 kHz, 1 kHz) to optimize performance for different data rates.
- Detector Reference:** Provides a stable reference voltage for the Receive Detector.
- Receive Detector:** Detects the presence of a valid signal and generates a 'Receive Output Blanking' signal.
- AGC & Signal Reference Processor:** Processes the AGC feedback and provides a reference for the transmitter's current source.
- Transmitter Section:**
 - TxD Input Buffer:** Buffers the digital data from the microcontroller (Pin 3) before driving the current source.
 - Switched Current Source:** Converts the digital data into an analog current signal.
 - Error AMP:** Amplifies the current signal to drive the LED.
 - Transmit IR LED:** Emits the modulated optical signal.
- Receiver Output:** The final processed signal, which can be configured as either a digital output (Pin 4) or an analog output (Pin 6) via a tri-state CMOS buffer.
- Control and Status:**
 - SD/Mode (Pin 5):** Selects between different operating modes (e.g., Standby, Receiver, Transmitter).
 - Power Down Control:** Manages the device's power state.
 - FIR/SIR-MIR Flip/Flop:** A digital filter that removes noise and improves signal integrity.

Table 2. Basic Parameters, $T_A=25^\circ\text{C}$ (except where noted)

Parameter	Min.	Typ.	Max.	Unit	Conditions
Data Rate	9.6 K	—	4 M	bits/s	Standard pulse width
V _{CC} Voltage	2.7	—	5.5	V	0 to 70°C, V _{CC} to V _{SS}
Maximum LED Anode Voltage	—	—	4 V+V _{CC}	V	V _{CC} =2.7 V to 5.5 V
I _{CC} Standby Current	—	3.3	4.5	mA	SD=0, V _{CC} =2.7 V to 5.5 V, no receive signal
I _{CC} Receiving Current	—	3.8	5.0	mA	10 μW, 4.0 Mb/s, 15 pF load
I _{CC} Shut Down Current (Note 1)	—	0.01	1.0	μA	SD=V _{CC} , V _{CC} =2.7 V to 5.5 V
I _{CC} Transmitting Current	—	12	40	mA	TxD=V _{CC} , V _{CC} =5.0 V, V _{LED} =5.0 V, Ro=5.1 Ω
Minimum V _{CC}	—	2.5	—	V	Reduced performance

Parameter	Min.	Typ.	Max.	Unit	Conditions
TxD, SD Input Capacitance	—	5.0	—	pF	V _{CC} =2.7 V to 5.5 V
TxD pull-down	350	500	700	kΩ	TxD=V _{CC} , V _{CC} =5.0 V
Minimum dV/dt of TxD Input Signal	0.7	1.4	—	V/μs	5.0 V pulse
TxD Minimum Hold Time for Mode Change	90	200	—	ns	V _{CC} =2.7 V to 5.5 V, referenced to SD negative clocking edge
TxD Minimum Setup Time for Mode Change (Note 2)	–70	–40	—	ns	V _{CC} =2.7 V to 5.5 V, referenced to SD negative clocking edge
TxD Input Threshold	1.0	1.4	1.8	V _{CC}	V _{CC} =2.7 V to 5.5 V, 125 ns input pulse
SD Transmit Enable	—	500	—	ns	V _{CC} =2.7 V to 5.5 V, (Note 3)
SD to TxD input disable & RxD tri-state	—	50	70	ns	V _{CC} =2.7 V to 5.5 V
SD Input Threshold	1.0	1.4	1.8	V	V _{CC} =5.0 V
SD Input Threshold	0.6	0.9	1.2	V	V _{CC} =2.7 V

Table 3. I/O Parameters (continued)

Parameter	Min.	Typ.	Max.	Unit	Conditions
RxD Output High	3.5	4.0	—	V	$V_{CC}=5.0\text{ V}$, $I_{OH}=24\text{ mA}$
RxD Output High	2.0	2.3	—	V	$V_{CC}=2.7\text{ V}$, $I_{OH}=6.0\text{ mA}$
RxD Output Low	—	1.0	1.5	V	$V_{CC}=5.0\text{ V}$, $I_{OL}=24\text{ mA}$
RxD Output Low	—	0.4	0.8	V	$V_{CC}=2.7\text{ V}$, $I_{OL}=6.0\text{ mA}$
RxD Short Circuit	—	48	—	mA	$V_{CC}=5.0\text{ V}$, $RxD=0$, $RxD=V_{CC}$
RxD Short Circuit	—	48	—	mA	$V_{CC}=2.7\text{ V}$, $RxD=0$, $RxD=V_{CC}$
RxD to V_{CC} Tri-State Impedance	350	500	650	k Ω	$SD=V_{CC}$, $V_{CC}=5.0\text{ V}$, measured between RxD to V_{CC}
RxD Rise/Fall Time	—	7.0	—	ns	$V_{CC}=5.0\text{ V}$, load=15 pF
RxD Rise/Fall Time	—	15	—	ns	$V_{CC}=5.0\text{ V}$, load=100 pF
RxD Rise/Fall Time	—	10	—	ns	$V_{CC}=2.7\text{ V}$, load=15 pF
RxD Rise/Fall Time	—	27	—	ns	$V_{CC}=2.7\text{ V}$, load=100 pF

Table 4. Receiver Parameters, High Rate Mode (FIR), $T_A=25^\circ\text{C}$ (except where noted)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Data Rate	—	—	4 M	—	bit/s	125 ns (low rate pulse), 0 to 70°C
Logic High Input Irradiance	E_{IHmin}	10	—	—	$\mu\text{W}/\text{cm}^2$	Bit error rate= 10^{-8}
Logic High Input Irradiance	E_{IHmax}	—	—	500	mW/cm^2	In-band irradiance maximum
Minimum Detection Threshold	E_{Emin}	—	4.0	—	$\mu\text{W}/\text{cm}^2$	4.0 Mb/s
Logic Low Input Irradiance	E_{IL}	—	—	0.4	$\mu\text{W}/\text{cm}^2$	Ambient interference pulsed
Receive Latency (Near-far)	—	—	500	800	μs	0 to 10 mW/cm^2 ambient input
Powerup Latency	—	—	50	100	μs	0 to 10 mW/cm^2 ambient input
Transmit Latency (turn around)	—	—	30	50	μs	0 to 10 mW/cm^2 ambient input
Pulse Width Correction Range	—	90	—	175	ns	40 $\mu\text{W}/\text{cm}^2$ input to produce 115 ns to 135 ns output, $V_{CC}=2.7\text{ V}$ to 5.5 V
Pulse Width Settling	—	—	8.0	16	μs	40 $\mu\text{W}/\text{cm}^2$ input
Output Pulse Edge Jitter & Bias at RxD	—	—	10	—	ns	125 ns, 40 $\mu\text{W}/\text{cm}^2$ input, load=15 pf, measured at 1.4 V
Output Pulse Width at RxD	—	110	125	140	ns	125 ns, 40 $\mu\text{W}/\text{cm}^2$ input, load=15 pf, measured at 1.4 V
Power Supply Rejection <5 MHz	—	—	50	—	mV _P	<0.1 pulse per second spurious output
Power Supply Rejection >5 MHz	—	—	25	—	mV _P	<0.1 pulse per second spurious output

Table 5. Receiver Parameters, Low Rate Mode (SIR/MIR), $T_A=25^\circ\text{C}$ (except where noted)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Data Rate	—	9.6 K	—	1.15 M	bits/s	9.6 Kb/s to 115 Kb/s=1.63 μs , 576 Kb/s=434 ns, 1.152 Mb/s=217 ns
Logic High Input Irradiance (MIR)	E_{IHmin}	10	—	—	$\mu\text{W}/\text{cm}^2$	Bit error rate= 10^{-8}
Logic High Input Irradiance (SIR)	E_{IHmin}	4.0	—	—	$\mu\text{W}/\text{cm}^2$	Bit error rate= 10^{-8}
Logic High Input Irradiance	E_{IHmax}	—	—	500	mW/cm^2	In band irradiance maximum
Logic Low Input Irradiance	E_{IL}	—	—	0.4	$\mu\text{W}/\text{cm}^2$	Ambient interference pulsed
Receive Latency (Near-far)	—	—	2.5	4.0	ms	0 to 10 mW/cm^2 ambient input
Transmit Latency (turn around)	—	—	50	100	μs	0 to 10 mW/cm^2 ambient input
Power-up Latency	—	—	50	150	μs	0 to 10 mW/cm^2 ambient input
Power Supply Rejection <5 MHz	—	—	50	—	mV _P	<0.1 pulse per second spurious output
Power Supply Rejection >5 MHz	—	—	25	—	mV _P	<0.1 pulse per second spurious output

Table 6. Transmitter Output

Parameter	Min.	Typ.	Max.	Unit	Conditions
Transmit Delay	—	15	—	ns	125 ns pulse, $V_{CC}=5.0$ V
Pulse Width Limit	50	70	100	μ s	TxD pulse > 100 μ s, 5.0 V pulse, $V_{CC}=5.0$ V
TxD Peak Wavelength	850	870	900	nm	$I_F=20$ mA DC
LED Anode Current Limit	510	610	680	mA	TxD= V_{CC} , LED anode=5.0 V, $V_{CC}=5.0$ V, averaged over 125 ns pulse
LED Anode Current Limit	310	450	550	mA	TxD= V_{CC} , LED anode=3.0 V, $V_{CC}=3.0$ V, averaged over 125 ns pulse
LED Cathode Saturation Drop	—	0.3	0.4	V	400 mA, $V_{CC}=2.7$ V
V_{CC} Transient Immunity	$V_{CC} \pm 1$	$V_{CC} \pm 2$	—	V peak	10 ns pulse height, $V_{CC}=5.0$ V (Note 4)
Transmit output rise and fall time (t_r , t_f)	10	—	40	ns	$V_{LED}=2.7$ V, no resistor

Note 1:

For Shut Down (SD) current to fall below 1 μ A requires driving the Shut Down (SD) pin to within 0.5 V of V_{CC} to ensure cutoff of the input PMOS transistor of the input CMOS totem pole.

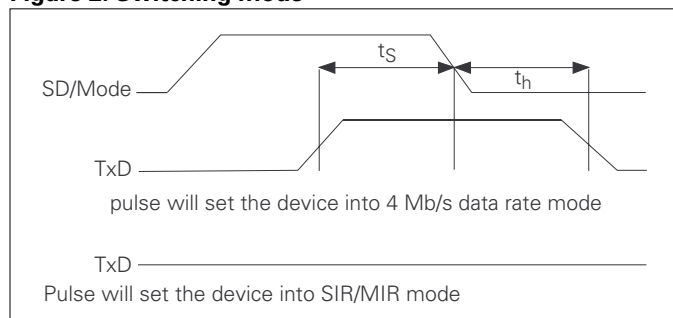
Note 2:

Negative setup times indicate that the internal data propagation time is longer than the latching signal propagation time. This is common on logic devices. It means that the TxD data can go away before the negative Shut Down (SD) edge occurs and not lose the data. (See application notes for more details).

Mode Switching (see Figure 2.)

Normally the IRMS6400/IRMT6400 Series initializes in the SIR/MIR (9.6 Kb/s to 1.152 Mb/s) mode upon power-up. Switching the module to FIR (4 Mb/s) Mode can be achieved as follows:

- Bring Shut Down (SD)/Mode pin to a logic “High” status.
- Bring TxD input to a logic “High” status. Wait for $t_s \geq -40$ ns.
- Bring Shut Down (SD)/Mode pin to a logic “Low” status. The negative transition of this pulse will set the module mode to (4 Mb/s) data rate by reading the state of TxD.
- Bring TxD to a logic “Low” after waiting for $t_h \geq 90$ ns.

Figure 2. Switching Mode**Switching to SIR/MIR (9.6 Kb/s to 1.15 Mb/s) Mode:**

- Bring Shut Down (SD)/Mode pin to a logic “High” status.
- Bring TxD input to a logic “Low” status. Wait for $t_s \geq -40$ ns.
- Bring Shut Down (SD)/Mode pin to a logic “Low” status. The negative transition of this pulse will set the module mode to 115 Kb/s data rate by reading the state of TxD.

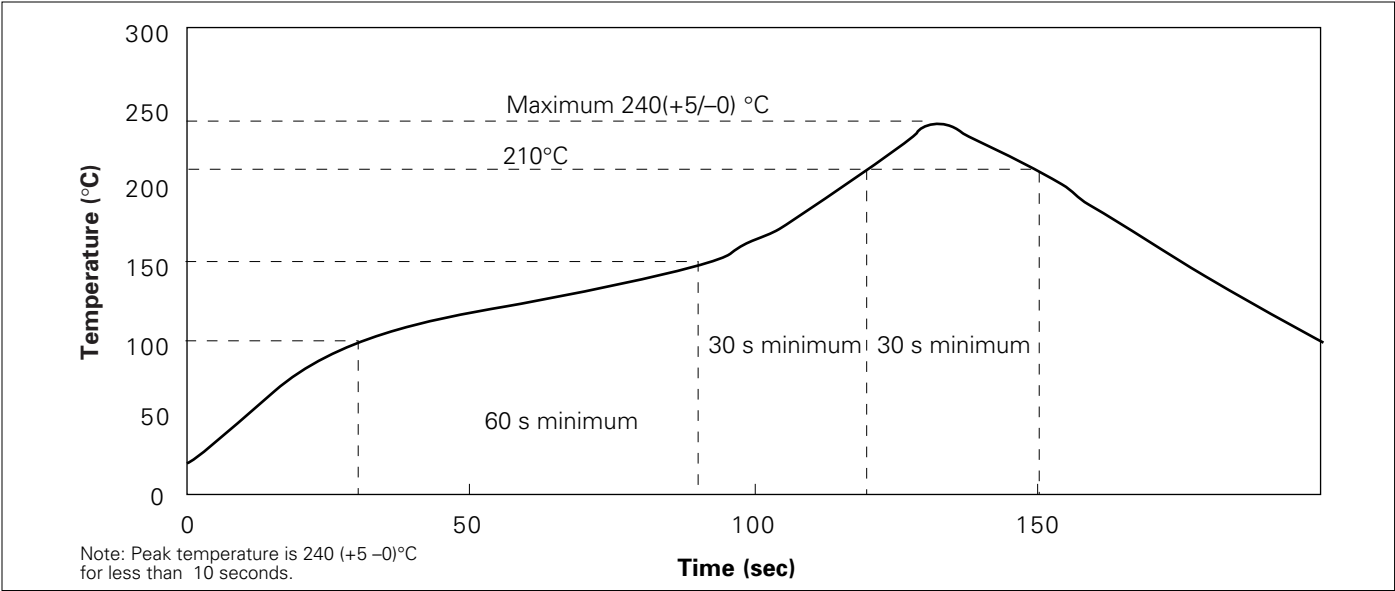
Note 3:

Shut Down (SD) to transmit enable is the time required for the transmit bias circuits to stabilize. Applying a transmit pulse before this time has elapsed will reduce the transmit pulse width.

Note 4:

Transmit V_{CC} Transient Immunity measures the transmitter circuitry immunity from large V_{CC} and ground return inductive transients arising from the action of large transmitter di/dt currents on V_{CC} and ground return trace inductances. This test applies a negative 10 ns V_{CC} pulse to simulate the effects of transmitter current induce V_{CC} transients (assuming the LED anode supply and V_{CC} have a shared inductance) and measures for less than a 10% change in pulse width or delivered charge on the initial pulse and subsequent pulse.

Figure 3. Infrared Reflow Soldering Profile



Interface Diagrams

Figure 4. Super I/O (PC87338VLJ) to IRMS6400/IRMT6400

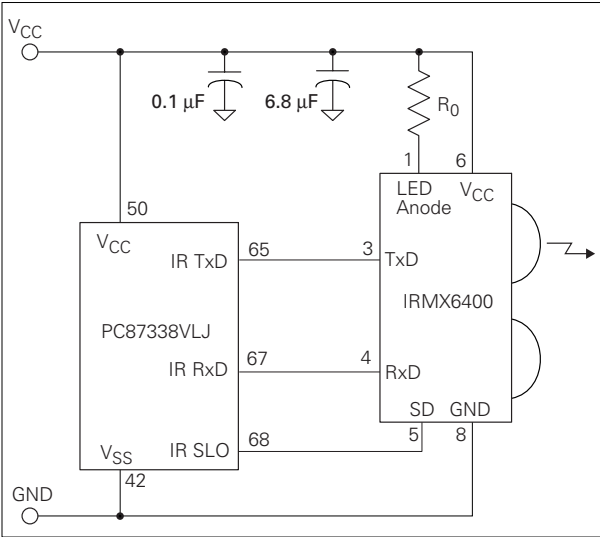


Figure 6. Super I/O (PC87338VLJ) to IRMS6400/IRMT6400 with independent V_{LED} power supply

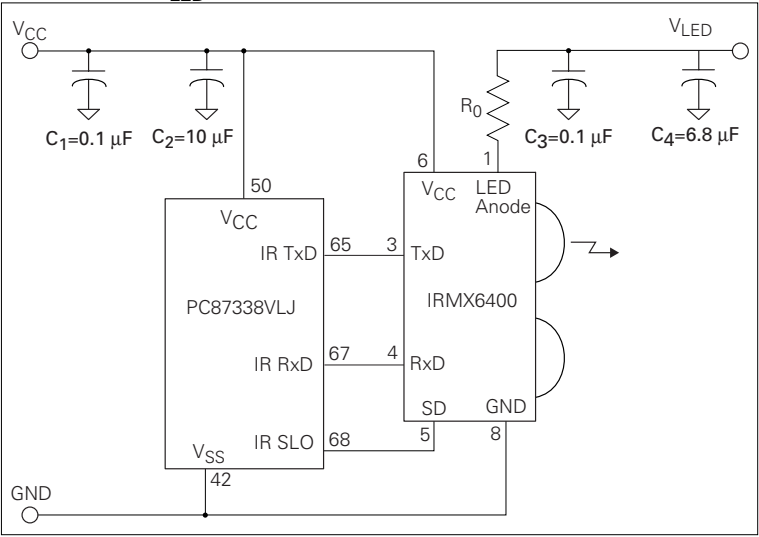


Figure 5. Ultra I/O controller with fast IR (FDC37C93XFR) to IRMS6400/IRMT6400

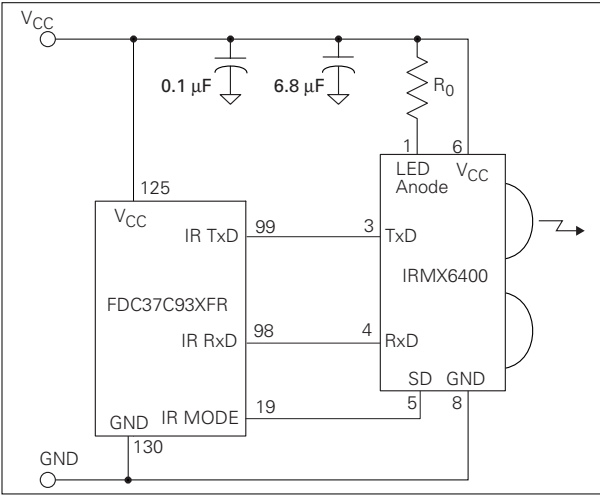


Table 7. Recommended R₀ values for different V_{LED}

Parameter	Values							Unit
V _{LED} power supply	2.4	2.7	3.0	3.5	4.0	4.5	5.0	V
Resistor	0	0	0	1.2	2.7	3.9	5.1	Ω

Ordering Information

Part Number	Description	PCB Mounting Orientation
IRMS6400	Integrated Transceiver —Side View	Packaged in Component Carrier Reel (1000/reel) for Side View Mounting on PCB
IRMT6400	Integrated Transceiver —Top View	Packaged in Component Carrier Reel (1000/reel) for Top View Mounting on PCB

Tape Leader and Trailer is 400 mm minimum.

Figure 8. Reel Dimensions in Inches (mm)

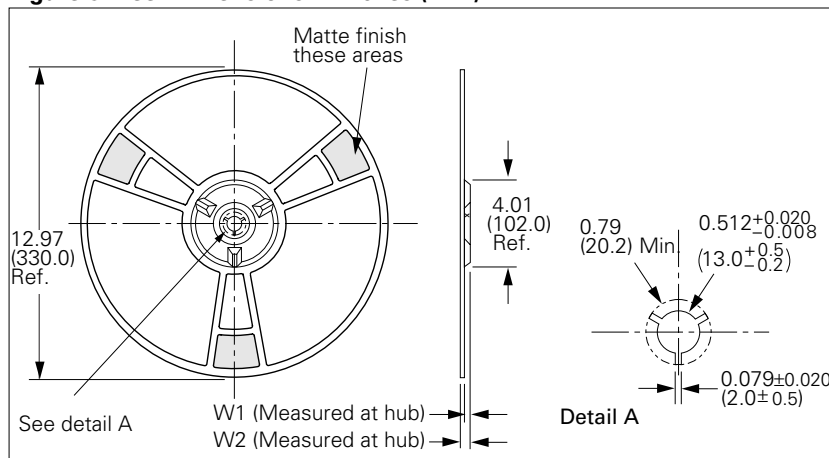
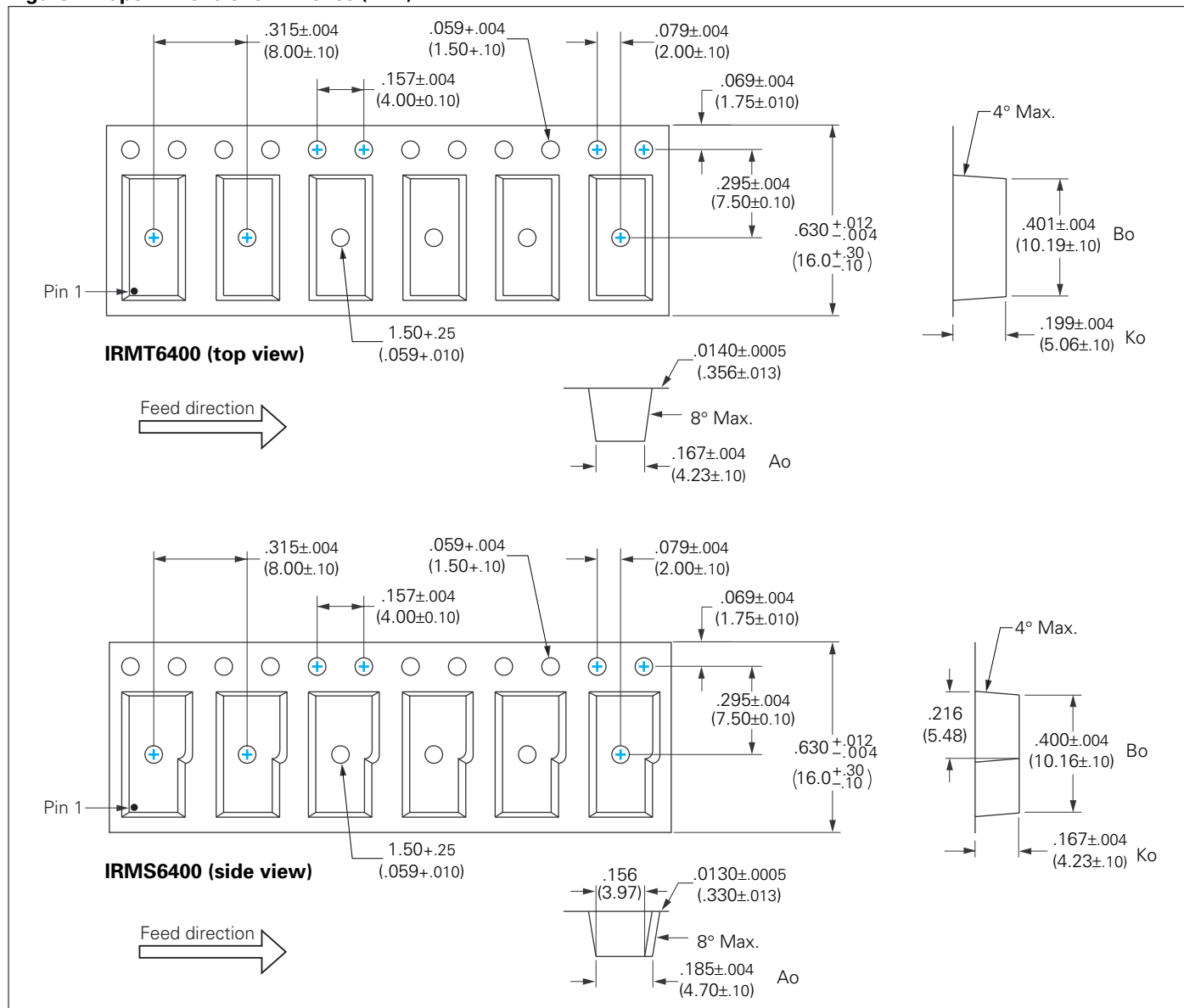


Figure 7. Tape Dimensions in Inches (mm)



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