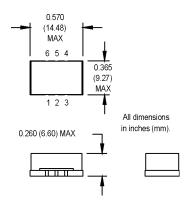
MXP Series 3.3 Volt FR-4 Based LVPECL/LVDS Compatible Surface Mount Oscillators

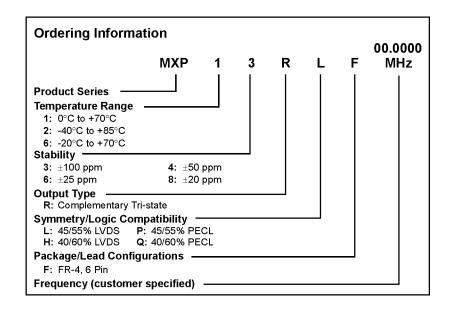
PARAMETER

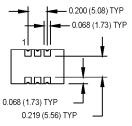


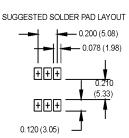
Condition











	Frequency Range	F	0.75		800	MHz		
	Frequency Stability	∆F/F	(See Ordering Information)				See Note 1	
	Operating Temperature	Ts	(See Ordering Information)					
	Storage Temperature	TA	-55		+125	°C		
	Input Voltage	Vcc	3.15	3.3	3.45	VDC		
	Input Current	Icc						
	0.75 MHz to 24 MHz			1	60/30	mA	PECL/LVDS	
	24 MHz to 160 MHz			1	100/50	mA	PECL/LVDS	
lus	160 MHz to 800 MHz			1	120/80	mA	PECL/LVDS	
atic	Symmetry (Duty Cycle)		(See Ordering Information)					
Electrical Specifications	Load						See Note 2	
	Rise/Fall Time	Tr/Tf			2	ns	PECL	
ιŠ					3	ns	LVDS	
ca	Logic "1" Level	Voh	Vcc -0.9			VDC	PECL	
당			1.385	1.43	1.6	%	LVDS	
👸	Logic "0" Level	Vol			Vcc -1.7	VDC	PECL	
					1.6	%	LVDS	
	Cycle to Cycle Jitter						1 Sigma	
	@ 19.44 MHz			5	10	ps RMS		
	@ 38.88 MHz			7	12	ps RMS		
	@ 77.76 MHz			8	13	ps RMS		
	@ 155.52 MHz			10	15	ps RMS		
	@ 250.00 MHz			10	15	ps RMS		
	@ 622.08 MHz			10	15	ps RMS		
1	Aging	< 2 ppm	< 2 ppm for first year					
		< 1 ppm/	1 ppm/year following the first year					
tal							_	
vironmental	Mechanical Shock	Per MIL-	Per MIL-STD-202, Method 213, Condition C					
ΙĒ	Vibration	Per MIL-	Per MIL-STD-202, Method 201 & 204					
l ĕ	Reflow Solder Conditions	240°C fo	240°C for 10 s max.					

Тур.

Max.

Units

Pin Connections

PIN	FUNCTION			
1	N/C			
2	Tri-state			
3	Ground			
4	Output 1			
5	Output 2			
6	+Vcc			

- 1. Calibration, deviation over temperature, shock, vibration, and aging
- 2. See load circuit diagram #5 on page 93.

Solderability

M-tron reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application. No rights under any patent accompany the sale of such product.

Per EIAJ-STD-002

Symbol