Image sensor heads for narrow-width scanners IA2004-CE70A

These image sensor heads feature high-speed scanning (1000mm/s) and are compatible with A6 size media. They are center connector type units with no frame protrusion, simplifying assembly.

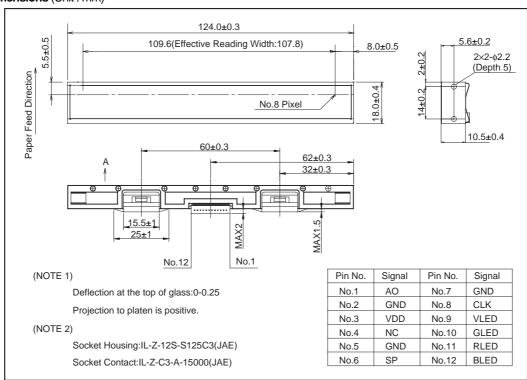
Applications

Check readers, card scanners, and a variety of other image input devices.

Features

- 1) Signal amplifier integrated into each sensor IC in order to eliminate external noise; compatible with 3.3V interface.
- 2) LED light source mounted on the same substrate as the sensor chip itself, resulting in a more compact, lightweight package.
- 3) Utilizes proprietary prism for improved lighting efficiency.
- 4) Ceramic substrate used, ensuring excellent dimensional and thermal stability.

●Dimensions (Unit: mm)





Characteristics

Parameter	Symbol	Тур.	Unit
Effective scanning width	-	107.8	mm
Primary scan dot density	-	200	dpi
Total dot number	-	864	dots
Power supply voltage	VDD	3.3	V
Scanning speed	SLT	0.125x3	ms / line *
Clock frequency	CLK	8	MHz
Maximum dynamic range	VRMax.	0.5	V
Minimum dynamic range	VRMin.	0.25	V
Dark output	Vod	1.2±0.2	V
Operating temperature	-	5 to 45	°C

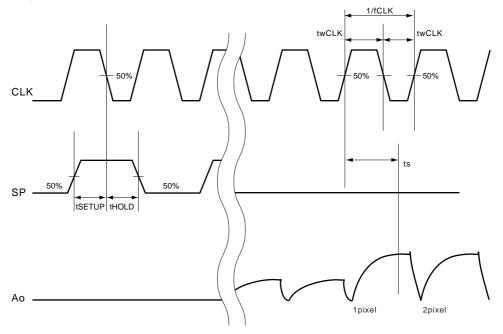
^{*} Analog signals are produced output at double rate of clock frequency.

●Pin assignments

No.	Circuit	1/0	Functions	
1	Ao	0	Analog Output	
2	GND	I	Ground	
3	V _{DD}	I	Power Supply	
4	NC	-	Non connect	
5	GND	I	Ground	
6	SP	I	Start pulse	
7	GND	I	Ground	
8	CLK	ı	Clock	
9	VLED	ı	LED power supply	
10	GLED	I	LED ground	
11	RLED	I	LED ground	
12	BLED	Ī	LED ground	

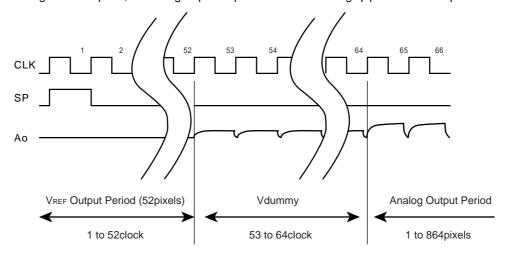
●Timing chart

(a) CLK Timing Chart



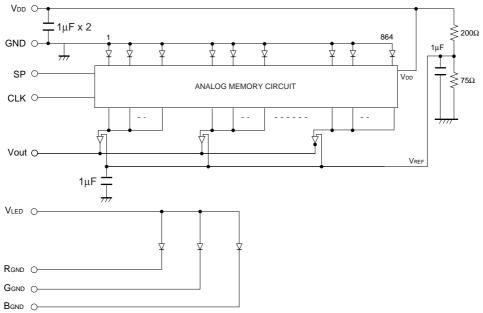
(b) Data Output Timing Chart

After turning on the SP pulse, the analog output shape starts from the setting up point of 65 clock pulse.

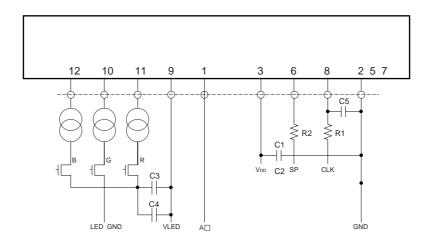


Note) Output blank part cannot be used as the analog output standard level.

●Equivalent circuit



●Peripheral circuit



 $\begin{array}{l} *\;R1{=}R2{=}100\Omega \\ C1{=}47\mu F \\ C3{=}100\mu F,\;C4{=}0.1\mu F,\;C5{=}100pF \end{array}$

* Please adjust the value of resistance to fit your interface circuit.

Notes

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