

## SCOPE :

This specification applies to the Pb Free high current type SMD Common mode filter  
for MCM-7060F-SERIES

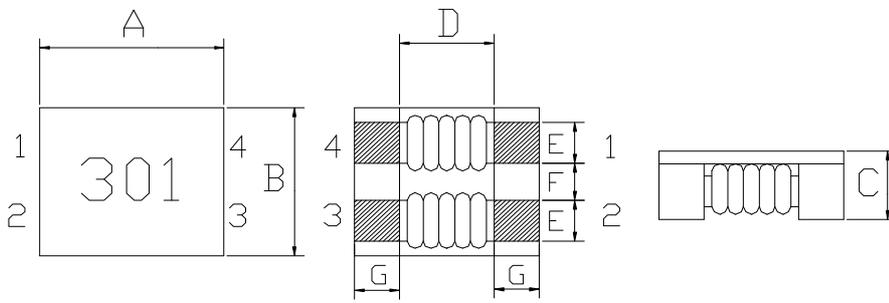
## PRODUCT IDENTIFICATION

**MCM-7060F- 301**

① ② ③

- ① Product Code
- ② Dimensions Code
- ③ Impedance Code

## (1) SHAPES AND DIMENSIONS



A:	7.0±0.5	mm
B:	6.0±0.5	mm
C:	3.8Max.	mm
D:	3.5Typ.	mm
E:	1.5±0.2	mm
F:	1.5±0.2	mm
G:	1.75±0.2	mm

## (2) ELECTRICAL SPECIFICATIONS

### SEE TABLE 1

#### TEST INSTRUMENTS

Z : HP 4291B IMPEDANCE ANALYZER (or equivalent)

RDC : CHROMA MODEL 16502 MILLIOHMMETER (or equivalent)

## (3) CHARACTERISTICS

- (3)-1 Temperature rise ..... +40°C Max.
- (3)-2 Ambient temperature ..... +60°C Max.
- (3)-3 Operate temperature range ..... -40°C ~ +105°C  
(Including self temp. rise)
- (3)-4 Storage temperature range ..... -40°C ~ +105°C

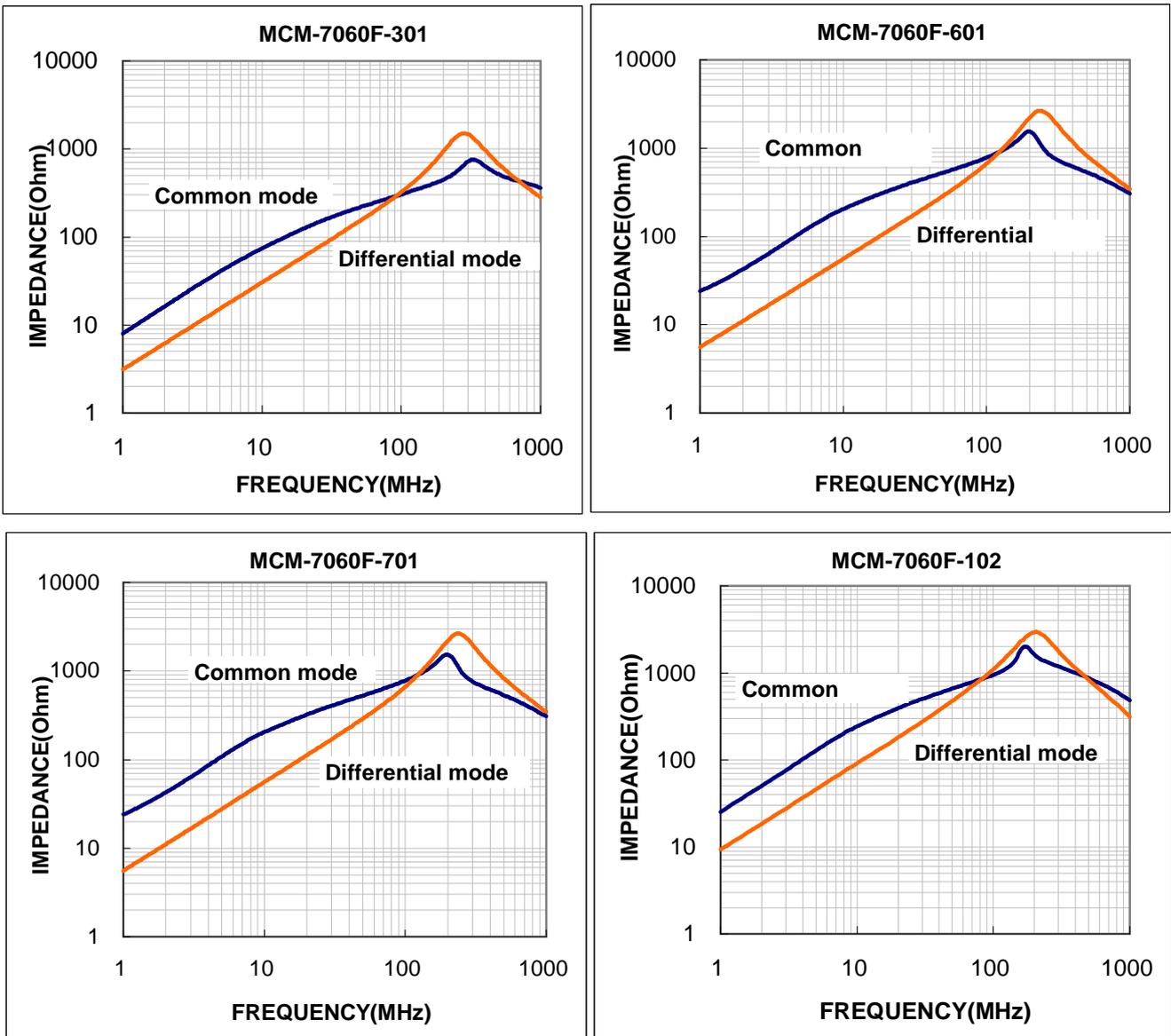


**MAG.LAYERS**

**TABLE 1**

MAGLAYERS PT/NO.	Impedance( $\Omega$ ) at 100MHz		Resistance RDC( $\Omega$ ) Max.(1 line)	Rated Current (A) Max.	Insulation Resistance (M $\Omega$ ) Min.	Rated Voltage (V)Max.
	Min.	Typ.				
MCM-7060F-101	100	140	10m	9	10	80
MCM-7060F-301	225	300	10m	5	10	80
MCM-7060F-501	275	350	10m	5	10	80
MCM-7060F-601	500	700	15m	4	10	80
MCM-7060F-701	500	700	15m	4	10	80
MCM-7060F-102	800	1020	17m	3	10	80
MCM-7060F-132	910	1300	21m	2.5	10	80

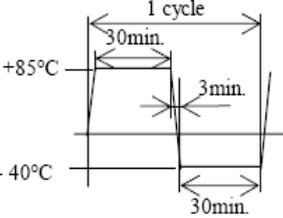
**CHARACTERISTICS(REFERENCE)**



**(4) RELIABILITY TEST METHOD  
MECHANICAL**

TEST ITEM	SPECIFICATION	TEST DETAILS
Solder ability	The product shall be connected to the test circuit board by the fillet (the height is 0.2mm).	Apply cream solder to the printed circuit board . Refer to clause 8 for Reflow profile.
Resistance to Soldering heat (reflow soldering)	There shall be no damage or problems.	<p>Temperature profile of reflow soldering</p> <p>The specimen shall be passed through the reflow oven with the condition shown in the above profile for 1 time. The specimen shall be stored at standard atmospheric conditions for 1 hour, after which the measurement shall be made.</p>
Terminal strength	The terminal electrode and the ferrite must not be damaged.	<p>Solder a chip to test substrate , and then laterally apply a load 9.8N in the arrow direction.</p>
Strength on PC board bending	The terminal electrode and the ferrite must not be damaged.	<p>Solder a chip to test substrate and then apply a load.</p> <p>Test board:FR4 100×40×1mm Fall speed:1mm/sec. Dimensions in mm</p>
High temperature resistance	<p>Impedance: Within <math>\pm 20\%</math> of the initial value.</p> <p>Insulation resistance and DC resistance on the specification (refer to clause 2-1) shall be met.</p> <p>The terminal electrode and the ferrite must not be damaged.</p>	<p>After the samples shall be soldered onto the test circuit board, the test shall be done.</p> <p>Measurement : After placing for 24 hours min.</p> <p>Temperature : <math>+85 \pm 2^\circ\text{C}</math></p> <p>Applied voltage : Rated voltage</p> <p>Applied current : Rated current</p> <p>Testing time : <math>500 \pm 12</math> hours</p>

**(4) RELIABILITY TEST METHOD**  
**MECHANICAL**

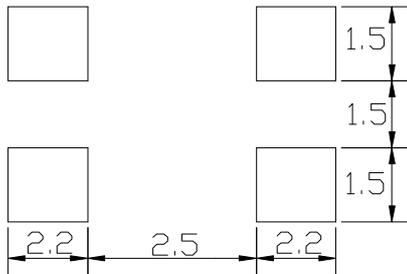
TEST ITEM	SPECIFICATION	TEST DETAILS
Humidity resistance	Impedance: Within $\pm 20\%$ of the initial value. Insulation resistance and DC resistance on the specification (refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not be damaged.	After the samples shall be soldered onto the test circuit board, the test shall be done. Measurement : After placing for 24 hours min. Temperature : $+60 \pm 2^\circ\text{C}$ , Humidity : 90 to 95 %RH Applied voltage : Rated voltage Applied current : Rated current Testing time : $500 \pm 12$ hours
Thermal shock	Impedance: Within $\pm 20\%$ of the initial value. Insulation resistance and DC resistance on the specification (refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not be damaged.	 <p>Testing time : 100 cycle</p>
Low temperature storage	Impedance: Within $\pm 20\%$ of the initial value. Insulation resistance and DC resistance on the specification (refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not be damaged.	After the samples shall be soldered onto the test circuit board, the test shall be done. Measurement : After placing for 24 hours min. Temperature : $-40 \pm 2^\circ\text{C}$ Testing time : $500 \pm 12$ hours
Vibration	Impedance: Within $\pm 20\%$ of the initial value. Insulation resistance and DC resistance on the specification (refer to clause 2-1) shall be met. The terminal electrode and the ferrite must not be damaged.	After the samples shall be soldered onto the test circuit board, the test shall be done. Frequency : 10 to 55 Hz Amplitude : 1.52 mm Dimension and times : X , Y and Z directions for 2 hours each.
Solderability	New solder More than 75%	Flux (rosin, isopropyl alcohol {JIS-K-1522}) shall be coated over the whole of the sample before hard, the sample shall then be preheated for about 2 minutes in a temperature of $130 \sim 150^\circ\text{C}$ and after it has been immersed to a depth 0.5mm below for $3 \pm 0.2$ seconds fully in molten solder M705 with a temperature of $245 \pm 2^\circ\text{C}$ . More than 75% of the electrode sections shall be covered with new solder smoothly when the sample is taken out of the solder bath.

## (5) LAND DIMENSION (Ref.)

PCB: GLASS EPOXY t=1.6mm

### (5)-1 LAND PATTERN DIMENSIONS

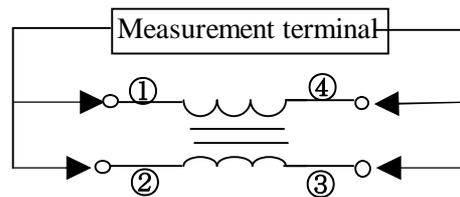
(STANDARD PATTERN) Unit:mm



## (6) TEST EQUIPMENT

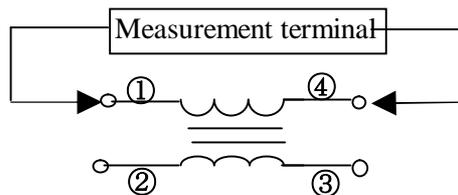
### (6)-1 Impedance

Measured by using HP4291B RF Impedance Analyzer.



### (6)-2 DC Resistance

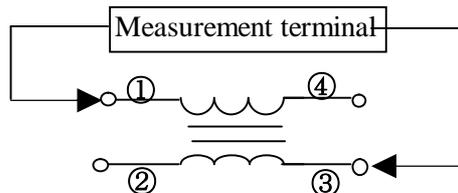
Measured by using Chroma 16502 milliohm meter.



### (6)-3 Insulation Resistance

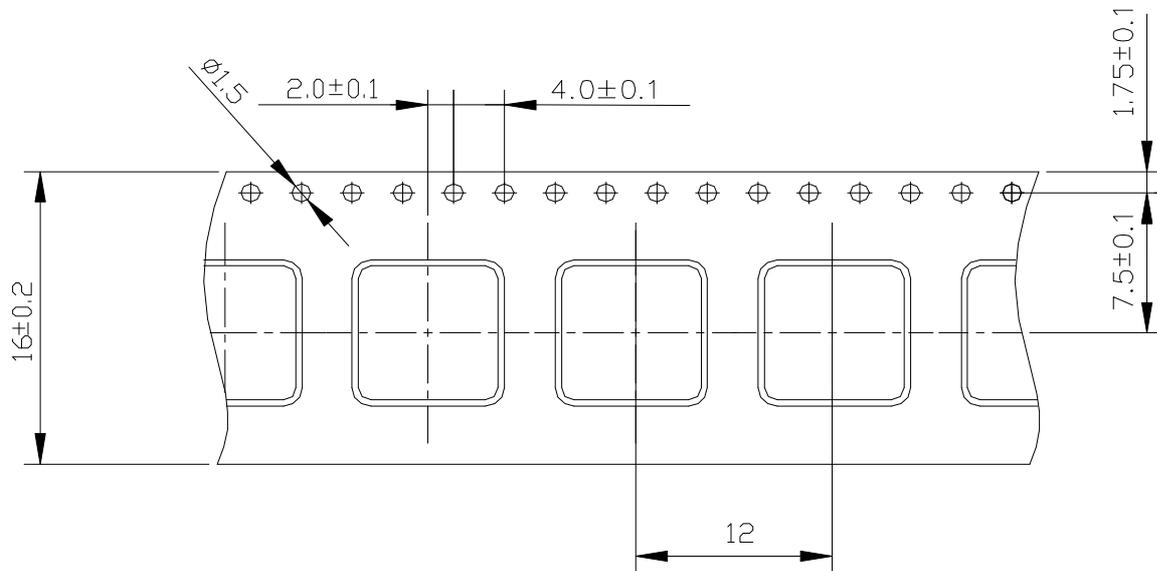
Measured by using Chroma 19073

Measurement voltage : 50v , Measurement time : 60 sec.

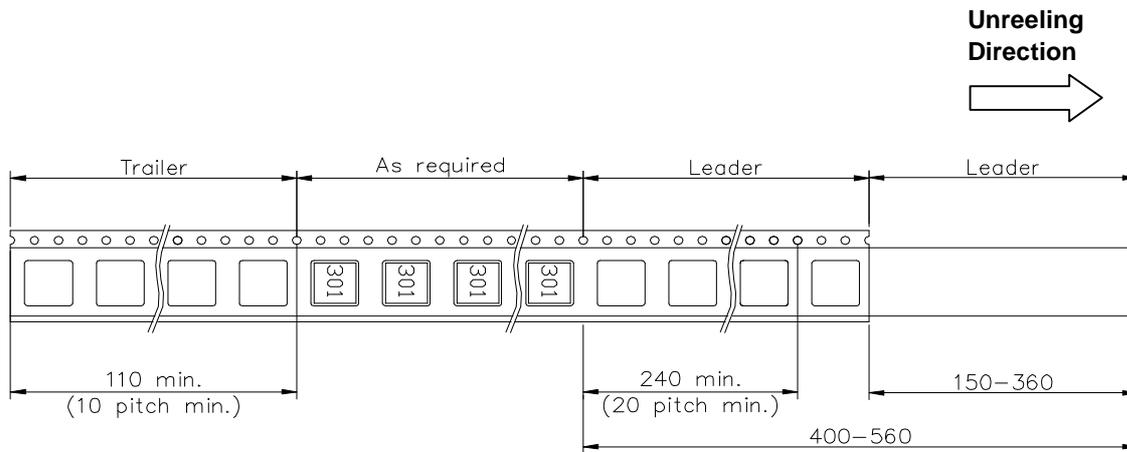


## (6) PACKAGING

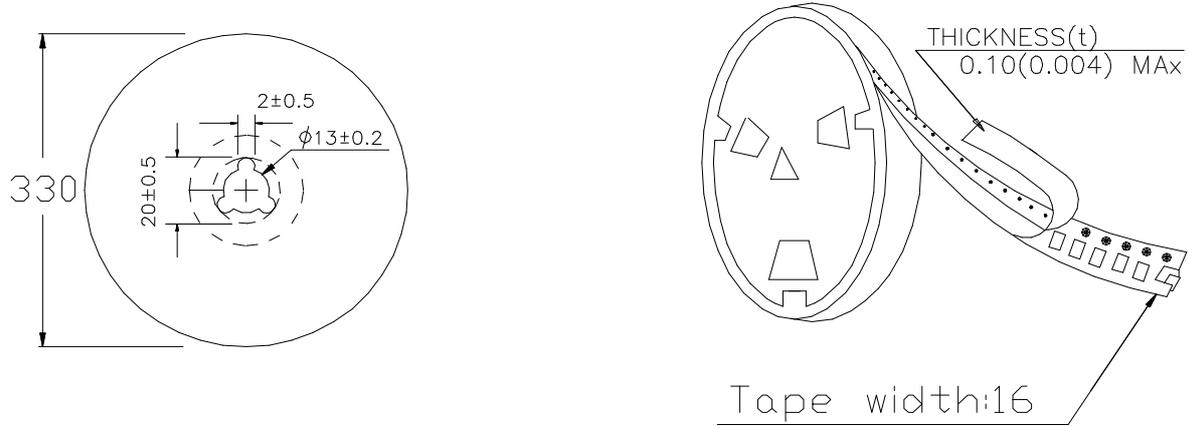
### (6)-1 CARRIER TAPE DIMENSIONS (mm)



### (6)-2 TAPING DIMENSIONS (mm)



### (6)-3 REEL DIMENSIONS (mm)



### (6)-4 QUANTITY

1500 pcs/Reel

The products are packaged so that no damage will be sustained.