



**QUICKSWITCH® PRODUCTS**  
**HIGH-SPEED CMOS**  
**QUICKSWITCH 8-BIT LOW**  
**RESISTANCE BUS SWITCH**

**IDTQS3R245**

**FEATURES:**

- Enhanced N channel FET with no inherent diode to Vcc
- 2.5Ω bidirectional switches connect inputs to outputs
- Pin compatible with 74F245, 74FCT245, and 74FCT245T
- Zero propagation delay, zero ground bounce
- Undershoot clamp diodes on all switch and control inputs
- TTL-compatible control inputs
- Available in SOIC and QSOP Packages

**APPLICATIONS**

- Hot-swapping and hot-docking (low resistance for PCI and Compact PCI applications)
- Bus switching and isolation
- Voltage translation (5V to 3.3V)
- Capacitance reduction and isolation
- Power conservation
- Logic replacement (data processing)
- Clock gating

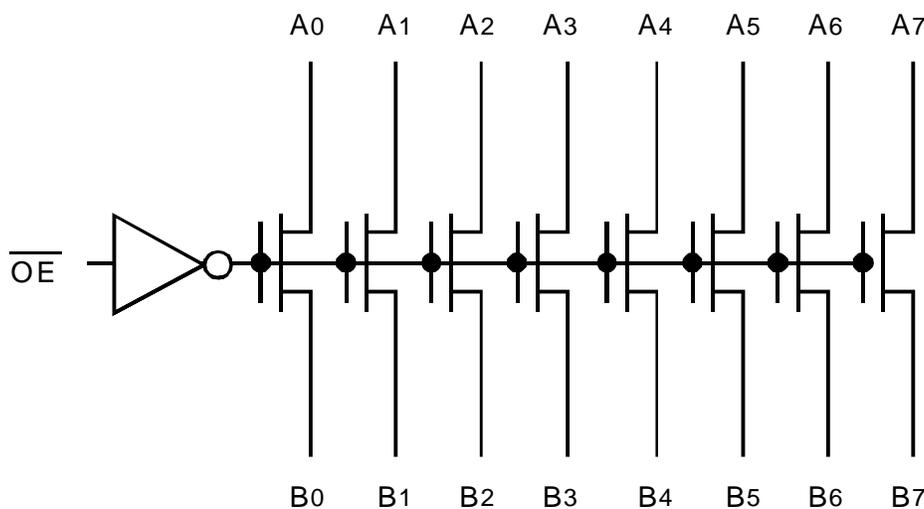
**DESCRIPTION:**

The QS3R245 provides a set of eight high-speed CMOS TTL-compatible bus switches in a pinout compatible with 74FCT245, 74F245, 74ALS/AS/LS245 8-bit transceivers. The low ON resistance of the QS3R245 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. The Output Enable ( $\overline{OE}$ ) signal turns the switches on similar to the  $\overline{OE}$  signal of the 74'245. The low ON resistance of QS3R245 makes it ideal for PCI hot docking applications.

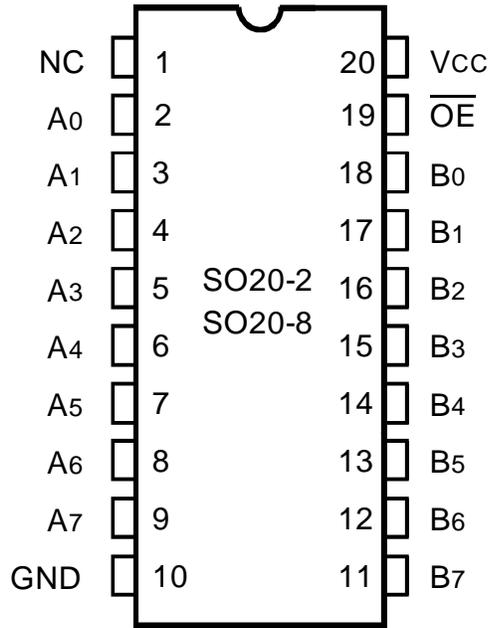
QuickSwitch devices provide an order of magnitude faster speed than conventional logic devices.

The QS3R245 is characterized for operation at -40°C to +85°C.

**FUNCTIONAL BLOCK DIAGRAM**



## PIN CONFIGURATION



SOIC/ QSOP  
TOP VIEW

## ABSOLUTE MAXIMUM RATINGS (1)

| Symbol                | Description                          | Max.         | Unit |
|-----------------------|--------------------------------------|--------------|------|
| V <sub>TERM</sub> (2) | Supply Voltage to Ground             | - 0.5 to +7  | V    |
| V <sub>TERM</sub> (3) | DC Switch Voltage V <sub>s</sub>     | - 0.5 to +7  | V    |
| V <sub>TERM</sub> (3) | DC Input Voltage V <sub>IN</sub>     | - 0.5 to +7  | V    |
| V <sub>AC</sub>       | AC Input Voltage (pulse width ≤20ns) | -3           | V    |
| I <sub>OUT</sub>      | DC Output Current                    | 120          | mA   |
| P <sub>MAX</sub>      | Maximum Power Dissipation            | .5           | W    |
| T <sub>STG</sub>      | Storage Temperature                  | - 65 to +150 | °C   |

### NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- V<sub>CC</sub> Terminals.
- All terminals except V<sub>CC</sub>.

## CAPACITANCE

(T<sub>A</sub> = +25°C, f = 1.0MHz, V<sub>IN</sub> = 0V, V<sub>OUT</sub> = 0V)

| Pins                              | Typ. | Max. (1) | Unit |
|-----------------------------------|------|----------|------|
| Control Inputs                    | 3    | 4        | pF   |
| Quickswitch Channels (Switch OFF) | 5    | 6        | pF   |

### NOTE:

- This parameter is guaranteed but not production tested.

## PIN DESCRIPTION

| Pin Names       | Description   |
|-----------------|---------------|
| $\overline{OE}$ | Output Enable |
| A <sub>n</sub>  | Data I/Os     |
| B <sub>n</sub>  | Data I/Os     |

## FUNCTION TABLE(1)

| $\overline{OE}$ | Function                        |
|-----------------|---------------------------------|
| H               | Disconnected                    |
| L               | A <sub>n</sub> = B <sub>n</sub> |

### NOTE:

- H = HIGH Voltage Level  
L = LOW Voltage Level

**DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE**

Following Conditions Apply Unless Otherwise Specified:

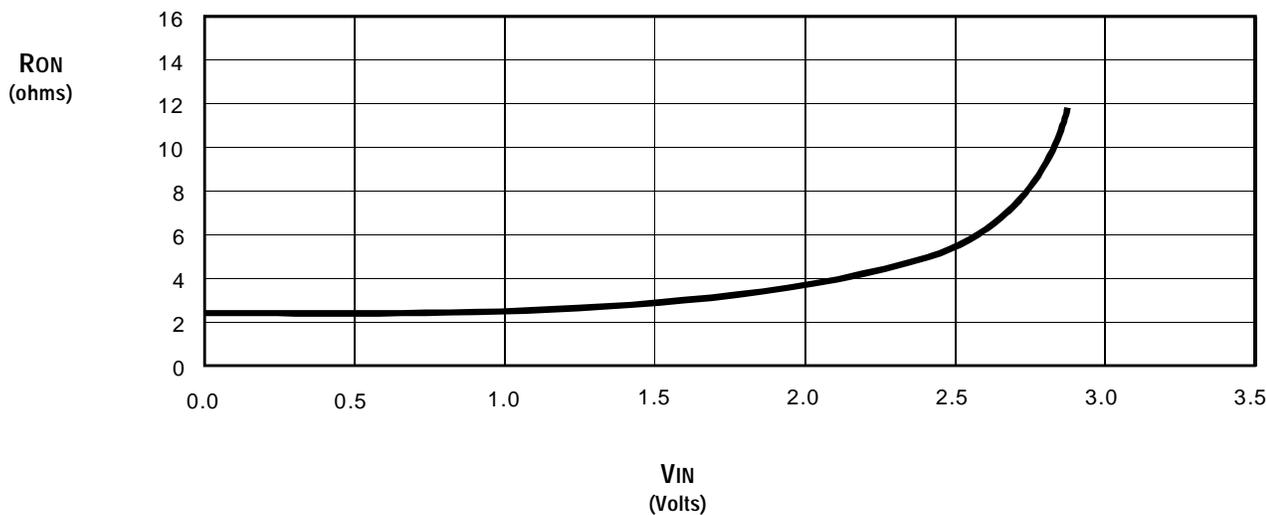
Industrial:  $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ,  $V_{CC} = 5.0\text{V} \pm 10\%$

| Symbol   | Parameter                              | Test Conditions  | Min. | Typ. <sup>(1)</sup> | Max.    | Unit          |
|----------|--|--|------|---------------------|---------|---------------|
| $V_{IH}$ | Input HIGH Voltage                     | Guaranteed Logic HIGH for Control Pins                                   | 2    | —                   | —       | V             |
| $V_{IL}$ | Input LOW Voltage                      | Guaranteed Logic LOW for Control Pins                                    | —    | —                   | 0.8     | V             |
| $I_{IN}$ | Input Leakage Current (Control Inputs) | $0\text{V} \leq V_{IN} \leq V_{CC}$                                      | —    | —                   | $\pm 1$ | $\mu\text{A}$ |
| $I_{OZ}$ | Off-State Current (Hi-Z)               | $0\text{V} \leq V_{OUT} \leq V_{CC}$ , Switches OFF                      | —    | $\pm 0.001$         | $\pm 1$ | $\mu\text{A}$ |
| $R_{ON}$ | Switch ON Resistance                   | $V_{CC} = \text{Min.}$ , $V_{IN} = 0\text{V}$ , $I_{ON} = 30\text{mA}$   | —    | 2.5                 | 5       | $\Omega$      |
| $R_{ON}$ | Switch ON Resistance                   | $V_{CC} = \text{Min.}$ , $V_{IN} = 2.4\text{V}$ , $I_{ON} = 15\text{mA}$ | —    | 4                   | 8.5     | $\Omega$      |
| $V_P$    | Pass Voltage <sup>(2)</sup>            | $V_{IN} = V_{CC} = 5\text{V}$ , $I_{OUT} = -5\mu\text{A}$                | 3.7  | 4                   | 4.3     | V             |

**NOTES:**

1. Typical values are at  $V_{CC} = 5.0\text{V}$ ,  $T_A = 25^{\circ}\text{C}$ .
2. Pass voltage is guaranteed but not production tested.

**TYPICAL ON RESISTANCE vs  $V_{IN}$  AT  $V_{CC} = 5\text{V}$**



## POWER SUPPLY CHARACTERISTICS

| Symbol           | Parameter  | Test Conditions <sup>(1)</sup>  | Max. | Unit   |
|------------------|--|---|------|--------|
| I <sub>CCQ</sub> | Quiescent Power Supply Current                             | V <sub>CC</sub> = Max., V <sub>IN</sub> = GND or V <sub>CC</sub> , f = 0              | 3    | μA     |
| ΔI <sub>CC</sub> | Power Supply Current per Control Input HIGH <sup>(2)</sup> | V <sub>CC</sub> = Max., V <sub>IN</sub> = 3.4V, f = 0                                 | 2.5  | mA     |
| I <sub>CCD</sub> | Dynamic Power Supply Current per MHz <sup>(3)</sup>        | V <sub>CC</sub> = Max., A and B pins open<br>Control Input Toggling at 50% Duty Cycle | 0.25 | mA/MHz |

### NOTES:

- For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- Per TLL driven input (V<sub>IN</sub> = 3.4V, control inputs only). A and B pins do not contribute to ΔI<sub>CC</sub>.
- This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

## SWITCHING CHARACTERISTICS OVER OPERATING RANGE

T<sub>A</sub> = -40°C to +85°C, V<sub>CC</sub> = 5.0V ± 10%

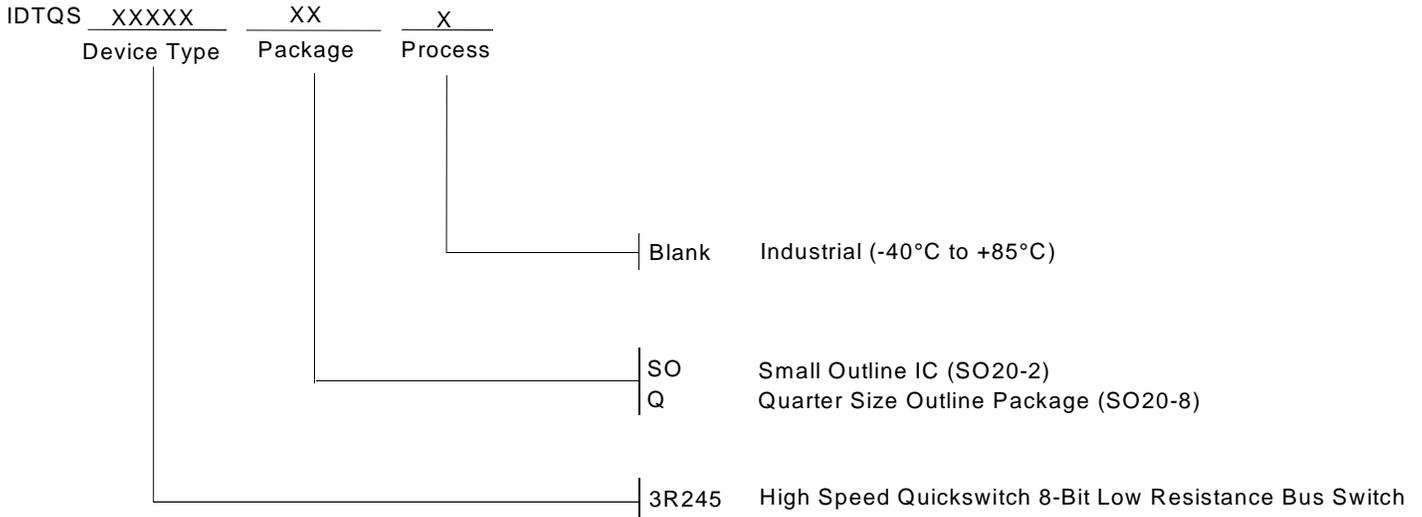
C<sub>LOAD</sub> = 50pF, R<sub>LOAD</sub> = 500Ω unless otherwise noted.

| Symbol                               | Parameter   | Min. <sup>(1)</sup> | Typ. | Max.                | Unit |
|--------------------------------------|---|---------------------|------|---------------------|------|
| t <sub>PLH</sub><br>t <sub>PHL</sub> | Data Propagation Delay <sup>(2,3)</sup><br>A <sub>n</sub> to/from B <sub>n</sub>          | —                   | —    | 0.12 <sup>(3)</sup> | ns   |
| t <sub>PZL</sub><br>t <sub>PZH</sub> | Switch Turn-on Delay<br>$\overline{OE}$ to A <sub>n</sub> /B <sub>n</sub>                 | 0.5                 | —    | 5.6                 | ns   |
| t <sub>PLZ</sub><br>t <sub>PHZ</sub> | Switch Turn-off Delay <sup>(2)</sup><br>$\overline{OE}$ to A <sub>n</sub> /B <sub>n</sub> | 0.5                 | —    | 4.5                 | ns   |

### NOTES:

- Minimums are guaranteed but not production tested.
- This parameter is guaranteed but not production tested.
- The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.12ns for C<sub>L</sub> = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## ORDERING INFORMATION



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