

# NL17SZ06

## Single Inverter with Open Drain Outputs

The NL17SZ06 is a high performance triple inverter with open drain outputs operating from a 1.65 to 5.5 V supply.

The internal circuit is composed of multiple stages, including an open drain output which provides the capability to set output switching level. This allows the NL17SZ06 to be used to interface 5 V circuits to circuits of any voltage between  $V_{CC}$  and 7 V using an external resistor and power supply.

- Extremely High Speed:  $t_{PD}$  2.5 ns (typical) at  $V_{CC} = 5$  V
- Designed for 1.65 V to 5.5 V  $V_{CC}$  Operation
- Over Voltage Tolerant Inputs and Outputs
- LVTTL Compatible – Interface Capability with 5 V TTL Logic with  $V_{CC} = 3$  V
- LVC MOS Compatible
- 24 mA Output Sink Capability
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- Chip Complexity: FET = 24

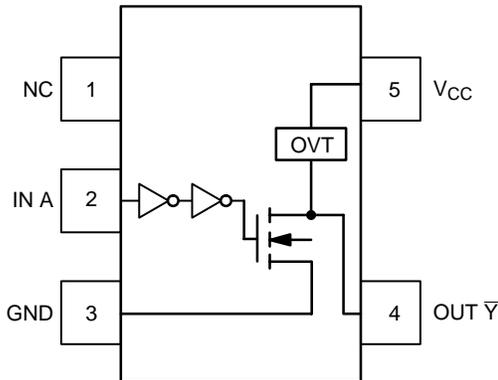


Figure 1. Pinout (Top View)



Figure 2. Logic Symbol



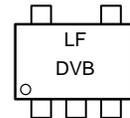
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### MARKING DIAGRAM



SC70-5/SC-88A/SOT-353  
DF SUFFIX  
CASE 419A



Pin 1

D = Date Code

### PIN ASSIGNMENT

Pin	Assignment
1	NC
2	IN A
3	GND
4	OUT $\bar{Y}$
5	$V_{CC}$

### FUNCTION TABLE

A Input	$\bar{Y}$ Output
L	Z
H	L

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

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## MAXIMUM RATINGS (Note 1)

Symbol	Characteristics	Value	Unit
V <sub>CC</sub>	DC Supply Voltage	-0.5 to +7.0	V
V <sub>I</sub>	DC Input Voltage	-0.5 ≤ V <sub>I</sub> ≤ +7.0	V
V <sub>O</sub>	DC Output Voltage Output in Z or LOW State (Note 2)	-0.5 ≤ V <sub>O</sub> ≤ 7.0	V
I <sub>IK</sub>	DC Input Diode Current V <sub>I</sub> < GND	-50	mA
I <sub>OK</sub>	DC Output Diode Current V <sub>O</sub> < GND	-50	mA
I <sub>O</sub>	DC Output Sink Current	±50	mA
I <sub>CC</sub>	DC Supply Current Per Supply Pin	±100	mA
I <sub>GND</sub>	DC Ground Current Per Ground Pin	±100	mA
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
P <sub>D</sub>	Power Dissipation in Still Air SC-88	150	mW
θ <sub>JA</sub>	Thermal resistance SC-88	350	°C/W
T <sub>L</sub>	Lead temperature, 1 mm from case for 10 s	260	°C
T <sub>J</sub>	Junction temperature under bias	+150	°C
I <sub>Latch-Up</sub>	Latch-Up Performance Above V <sub>CC</sub> and Below GND at 85°C (Note 6)	±500	mA

1. Absolute maximum continuous ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute-maximum-rated conditions is not implied.
2. I<sub>O</sub> absolute maximum rating must be observed.
3. Tested to EIA/JESD22-A114-A
4. Tested to EIA/JESD22-A115-A
5. Tested to JESD22-C101-A
6. Tested to EIA/JESD78

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage Operating Data Retention Only	1.65 1.5	5.5 5.5	V
V <sub>I</sub>	Input Voltage	0	5.5	V
V <sub>O</sub>	Output Voltage (Z or LOW State)	0	5.5	V
T <sub>A</sub>	Operating Free-Air Temperature	-40	+85	°C
Δt/ΔV	Input Transition Rise or Fall Rate V <sub>CC</sub> = 2.5 V ±0.2 V V <sub>CC</sub> = 3.0 V ±0.3 V V <sub>CC</sub> = 5.0 V ±0.5 V	0 0 0	20 10 5	ns/V

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## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			-40°C ≤ T <sub>A</sub> ≤ 85°C		Unit
				Min	Typ	Max	Min	Max	
V <sub>IH</sub>	High-Level Input Voltage		1.65 to 1.95 2.3 to 5.5	0.75 V <sub>CC</sub> 0.7 V <sub>CC</sub>			0.75 V <sub>CC</sub> 0.7 V <sub>CC</sub>		V
V <sub>IL</sub>	Low-Level Input Voltage		1.65 to 1.95 2.3 to 5.5			0.25 V <sub>CC</sub> 0.3 V <sub>CC</sub>		0.25 V <sub>CC</sub> 0.3 V <sub>CC</sub>	V
I <sub>LKG</sub>	Z-State Output Leakage Current	V <sub>IN</sub> = V <sub>IL</sub> V <sub>OUT</sub> = V <sub>CC</sub> or GND	2.3 to 5.5			± 5.0		± 10.0	μA
V <sub>OL</sub>	Low-Level Output Voltage V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	1.65 to 5.5		0.0	0.1		0.1	V
		I <sub>OL</sub> = 4 mA	1.65		0.08	0.24		0.24	
		I <sub>OL</sub> = 8 mA	2.3		0.22	0.3		0.3	
		I <sub>OL</sub> = 12 mA	2.7		0.22	0.4		0.4	
		I <sub>OL</sub> = 16 mA	3.0		0.28	0.4		0.4	
		I <sub>OL</sub> = 24 mA	3.0		0.38	0.55		0.55	
		I <sub>OL</sub> = 32 mA	4.5		0.42	0.55		0.55	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> or V <sub>OUT</sub> = V <sub>CC</sub> or GND	0 to 5.5			± 0.1		± 1.0	μA
I <sub>OFF</sub>	Power Off-Output Leakage Current	V <sub>OUT</sub> = 5.5 V	0			1.0		10	μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	5.5			1.0		10	μA

## AC ELECTRICAL CHARACTERISTICS t<sub>R</sub> = t<sub>F</sub> = 2.5 ns; C<sub>L</sub> = 50 pF; R<sub>L</sub> = 500 Ω

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			-40°C ≤ T <sub>A</sub> ≤ 85°C		Unit
				Min	Typ	Max	Min	Max	
t <sub>PZL</sub>	Propagation Delay (Figure 3 and 4)	R <sub>L</sub> = R <sub>1</sub> = 500 Ω, C <sub>L</sub> = 50 pF	1.65	0.8	5.3	11.6	0.8	12.0	ns
			2.5 ± 0.2	0.8	3.0	3.6	0.8	4.1	
			3.3 ± 0.3	0.8	2.4	3.2	0.8	3.7	
			5.0 ± 0.5	0.5	2.4	3.0	0.5	3.5	
t <sub>PLZ</sub>	Propagation Delay (Figure 3 and 4)	R <sub>L</sub> = R <sub>1</sub> = 500 Ω, C <sub>L</sub> = 50 pF	1.65	0.8	5.3	11.6	0.8	12.0	ns
			2.5 ± 0.2	0.8	2.5	3.6	0.8	4.1	
			3.3 ± 0.3	0.8	2.1	3.2	0.8	3.7	
			5.0 ± 0.5	0.5	1.2	3.0	0.5	3.5	

## CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	2.5	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	4.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 7)	10 MHz, V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	4.0	pF

7. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

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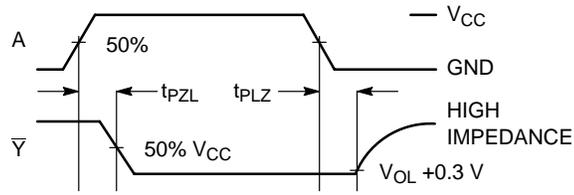
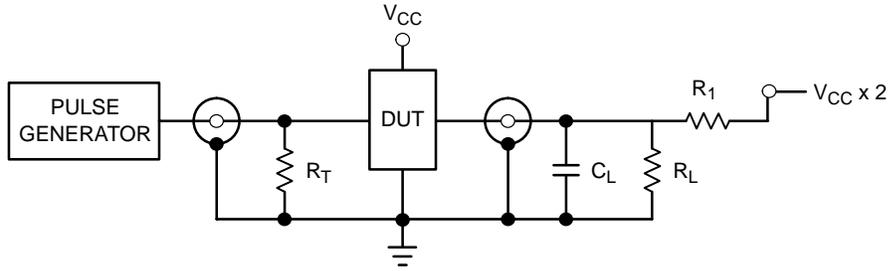


Figure 3. Switching Waveforms



$R_T = Z_{OUT}$  of pulse generator (typically 50  $\Omega$ )

Figure 4. Test Circuit

## DEVICE ORDERING INFORMATION

Device Order Number	Device Nomenclature							Package Type (Name/SOT#/Common Name)	Tape and Reel Size
	Logic Circuit Indicator	No. of Gates per Package	Temp Range Identifier	Technology	Device Function	Package Suffix	Tape & Reel Suffix		
NL17SZ06DFT2	NL	1	7	SZ	06	DF	T2	SC-88 / SOT-363 / SC-70	178 mm (7") 3000 Unit

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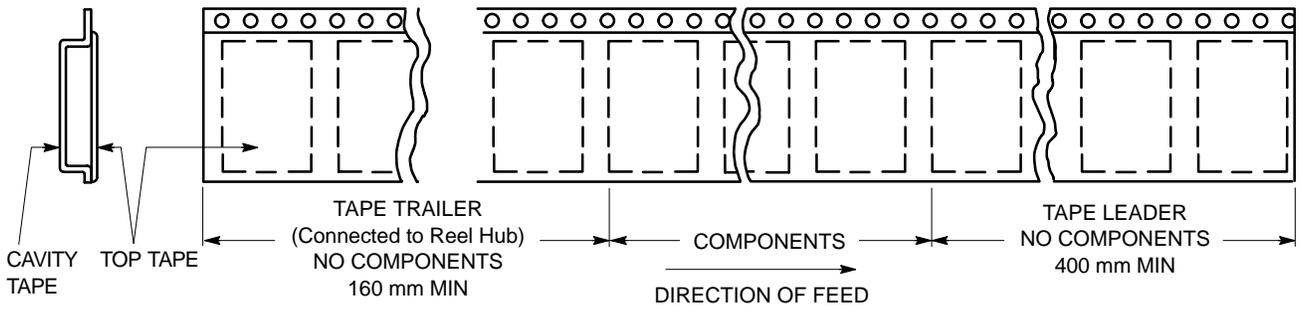


Figure 5. Tape Ends for Finished Goods

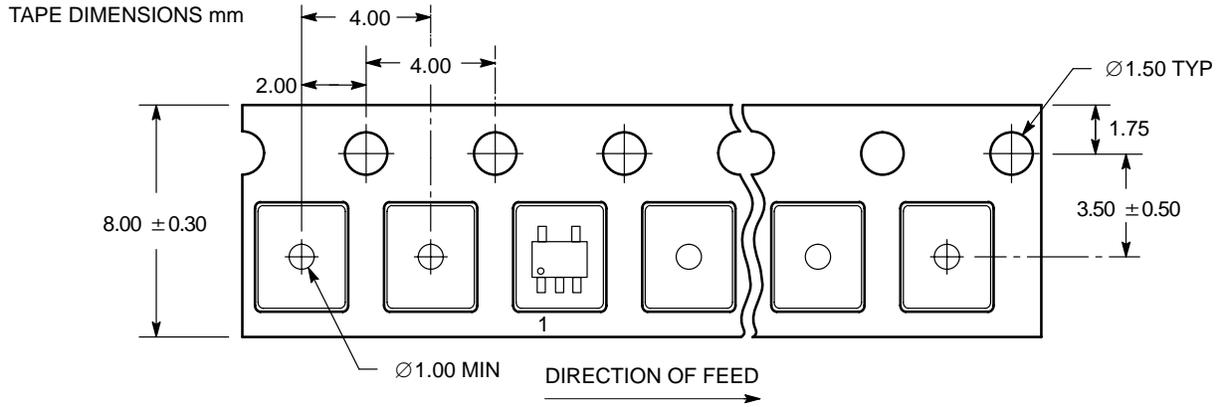


Figure 6. SC-70/SC-88A/SOT-353 DFT2 Reel Configuration/Orientation

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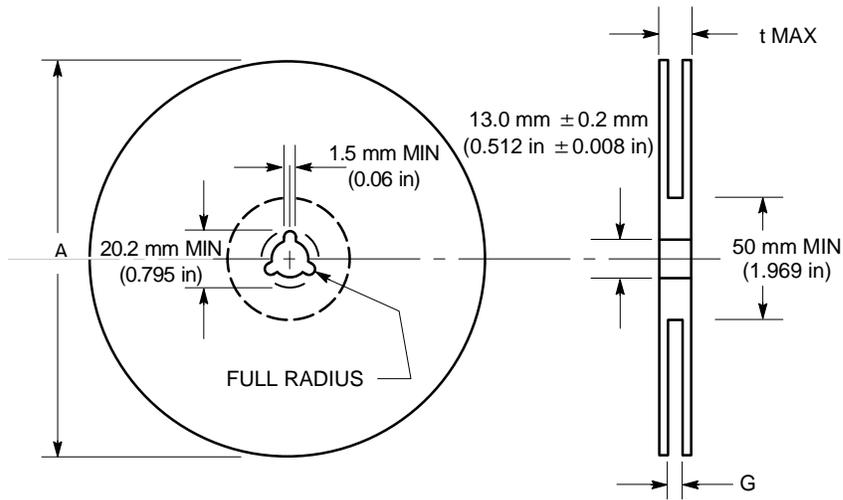


Figure 7. Reel Dimensions

## REEL DIMENSIONS

Tape Size	T and R Suffix	A Max	G	t Max
8 mm	T1, T2	178 mm (7 in)	8.4 mm, + 1.5 mm, -0.0 (0.33 in + 0.059 in, -0.00)	14.4 mm (0.56 in)

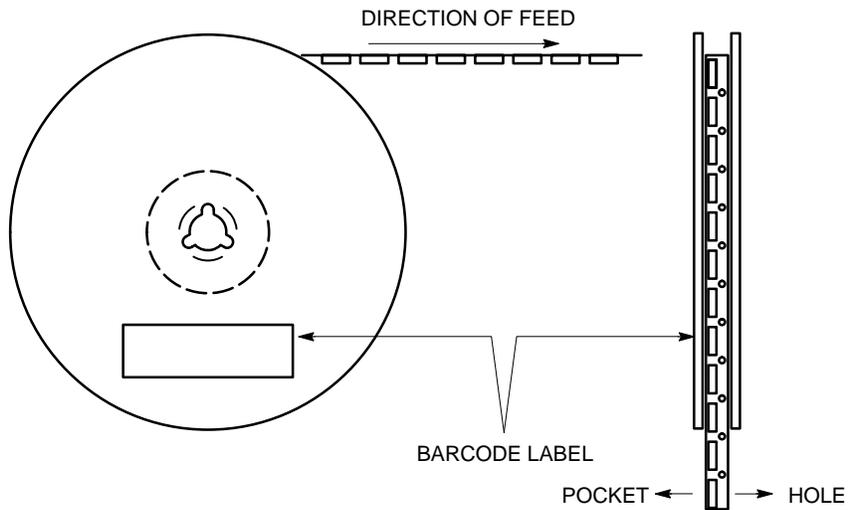
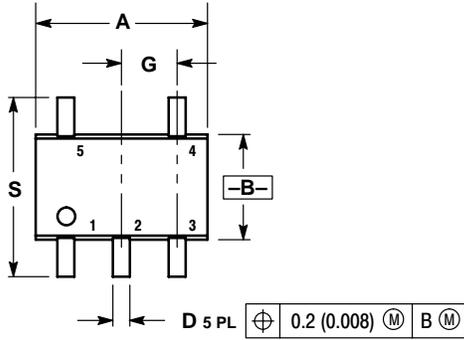


Figure 8. Reel Winding Direction

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## PACKAGE DIMENSIONS

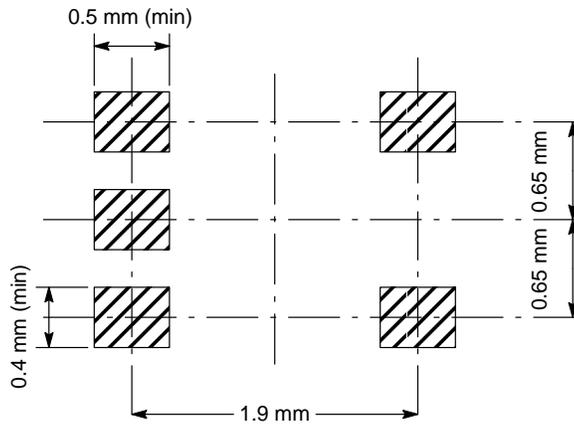
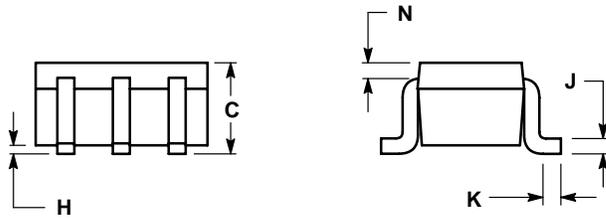
SC70-5/SC-88A/SOT-353  
 DF SUFFIX  
 5-LEAD PACKAGE  
 CASE 419A-02  
 ISSUE F



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 419A-01 OBSOLETE. NEW STANDARD 419A-02.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
H	---	0.004	---	0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2.00	2.20



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